

μPC177, 339

Single Power Supply Quad Comparator

R03DS0139EJ0100 Rev.1.00 2019.1.16

DESCRIPTION

 μ PC177, μ PC339 comparators are designed for single power supply operation. The features include low-voltage operation, common-mode input voltage range from V-(GND) level, open-collector output and low current consumption. Furthermore, these products can operate with both power supplies and can be widely used for various voltage comparison application.

Depending on the usage and operating ambient temperature range, the μ PC177 is design for communication industries and μ PC339 is design for general purposes.

In addition, compatible DC parameter selection for the comparators are available under special products.

Along with this series of lineup, the dual type comparators, µPC277 and 339 with the same circuit configuration are also available.

FEATURES

Input Offset Voltage	±2 mV (TYP.)
Input Bias Current	25 nA (TYP.)
Voltage Gain	1.3 μs (TYP.)
Pulse Response Time	16 mA (TYP.)
 Δ wired ΩR is nossible as the output is an open collector. 	

A wired OR is possible as the output is an open collector

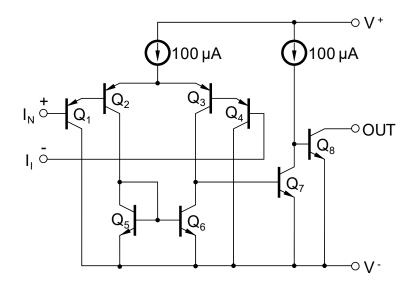
• Low Voltage Operation V⁺ - V⁻: 2 ~ 32 V

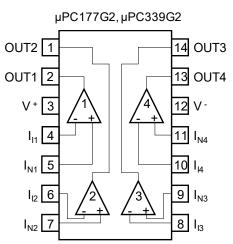
ORDERING INFORMATION

Order Name	Selected Grade	Package
μPC177G2-A	Standard	14-pin plastic SOP (5.72 mm (225))
μPC177G2(5)-A	DC Parameter Selection	14-pin plastic SOP (5.72 mm (225))
μPC339G2-A	Standard	14-pin plastic SOP (5.72 mm (225))
μPC339G2(5)-A	DC Parameter Selection	14-pin plastic SOP (5.72 mm (225))

EQUIVALENT CIRCUIT (1/4 Circuit)

PIN CONFIGURATION (Top View)





ABSOLUTE MAXIMUM RATINGS ($T_A = 25 \, ^{\circ}C$)

Parameter	Symbol	μPC177G2,	μPC339G2,	
		μPC177G2(5)	μPC339G2(5)	Unit
Power Supply Voltage Note 1	V + - V -	-0.3	~ +36	V
Differential Input Voltage	V_{ID}	±36		V
Input Voltage Note 2	VI	V⁻-0.3 ~ V⁻+36		V
Output Voltage Note 3	Vo	V⁻-0.3 ~ V⁻+36		V
Total Power Dissipation Note 4	P _T	550		mW
Output Short Circuit Duration (vs. GND) Note 5	ts	Inde	efinite	s
Operating Ambient Temperature	T _A	-40 ~ +85	-20 ~ +80	°C
Storage Temperature	T _{stg}	-55 ~	+125	°C

[Note] 1. Note that reverse connections of the power supply may damage the ICs.

- 2. This is the input voltage range that can be applied to the input terminal without any characteristics degradation or breakdown. It can be applied regardless of the supply voltage. Do not apply the voltage below V-(GND)-0.3V. The comparator electrical characteristics will operate normally when the input voltage is operating within the common-mode input voltage range.
- 3. Voltage range that can be applied externally to the output terminal without deteriorating or damage to the product. It can be applied regardless of the power supply. Caution not to exceed the ratings, including transient conditions such as when the power supply is ON/OFF.
- 4. Power dissipation value when $T_A \leq +25$ °C. For $T_A > 25$ °C, de-rate at -5.5 mW/°C
- 5. A short circuit to the V+ side may destroy the IC. Please use the total loss and de-rating value from Note 4.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Power Supply Voltage (Dual Supply)	V ±	±1		±16	V
Power Supply Voltage (V - = GND)	V *	+2		+32	V

ELECTRICAL CHARACTERISTICS

 μ PC177G2, μ PC339G2 (T_A = 25 °C, V + = +5 V, V - = GND)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Input Offset Voltage	Vio		±2	±5	mV	$V_0 = 1.4 \text{ V}, V_{REF} = 1.4 \text{ V}, R_S = 0 \Omega$
Input Offset Current	lio		±5	±50	nA	V _O ≒ 1.4 V
Input Bias Current Note 6	I _B		25	250	nA	V _O ≒ 1.4 V
Large Signal Voltage Gain	Av		200000			R _L = 15 kΩ
Circuit Current Note 7	Icc		0.8	2	mA	R _L = ∞, I _O = 0 A
Common Mode Input Voltage Range	VICM	0		V + -1.5	٧	
Output Saturation Voltage	Vol		0.2	0.4	V	V _{IN(-)} = 1 V, V _{IN(+)} = 0 V, I _{O SINK} = 4 mA
Output Sink Current	losink	6	16		mA	$V_{IN(-)} = 1 \text{ V}, V_{IN(+)} = 0 \text{ V}, V_O \leq 1.5 \text{ V}$
Output Leakage Current	I _{O LEAK}		0.1		nA	$V_{IN(+)} = 1 \text{ V}, V_{IN(-)} = 0 \text{ V}, V_O = 5 \text{ V}$
Pulse Response Time Note 8			1.3		μs	$R_L = 5.1 \text{ k}\Omega, V_{RL} = 5 \text{ V}$

μ PC177G2 (5), μ PC339G2 (5) (T_A = 25 °C, V + = +5 V, V = GND)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION
Input Offset Voltage	Vio		±2	±2.5	mV	$V_{O} = 1.4 \text{ V}, V_{REF} = 1.4 \text{ V}, R_{S} = 0 \Omega$
Input Offset Current	lio		±5	±50	nA	$V_0 = 1.4 V$
Input Bias Current Note 6	lΒ		25	60	nA	V _O ≒ 1.4 V
Large Signal Voltage Gain	A _V		200000			$R_L = 15 \text{ k}\Omega$
Circuit Current Note 7	Icc		0.8	1.2	mA	$R_L = \infty$, $I_O = 0$ A
Common Mode Input Voltage Range	VICM	0		V + -1.4	٧	
Output Saturation Voltage	V _{OL1}			0.2	V	V _{IN(-)} = 1 V, V _{IN(+)} = 0 V, I _O SINK = 4 mA
	V _{OL2}			1.5	V	V _{IN(-)} = 1 V, V _{IN(+)} = 0 V, I _O SINK =10mA
Output Sink Current	lo sink	10	16		mA	$V_{IN(-)} = 1 \text{ V}, V_{IN(+)} = 0 \text{ V}, V_{O} \le 1.5 \text{ V}$
Output Leakage Current	lo leak		0.1	100	nA	$V_{IN(+)} = 1 \text{ V}, V_{IN(-)} = 0 \text{ V}, V_O = 5 \text{ V}$
Pulse Response Time Note 8			1.3		μs	$R_L = 5.1 \text{ k}\Omega, V_{RL} = 5 \text{ V}$

[Note] 6. Direction of the input bias current flowing out of the IC due to first stage of IC consists of PNP transistors In addition, the value of this item is the value when the differential amplifier circuit of the input stage is balanced. It means that twice the current amount will flow to the lower potential terminal.

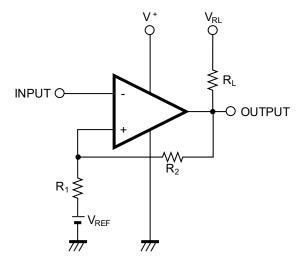
- 7. Current flowing through the internal circuit. This current flow regardless of the channel used.
- 8. Value when the input amplitude is 100mV and the overdrive is 5mV. The response time can be reduces by increasing the overdrive value.

TYPICAL APPLICATION CIRCUIT EXAMPLE

4, 6, 8, 10 V_{IN} O 5, 7, 9, 11 V_{REF} V 12 V 12 V 12

 $V_{REF} : V^- \sim V^+ - 1.5 [V]$

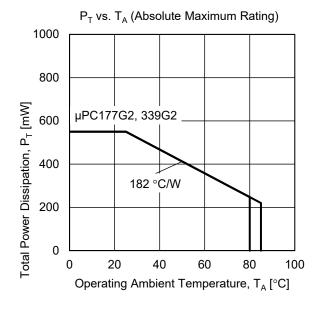
Comparator with Hysteresis

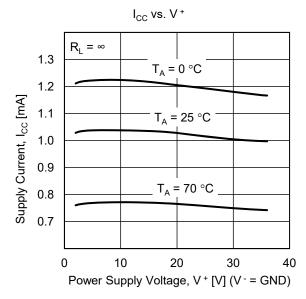


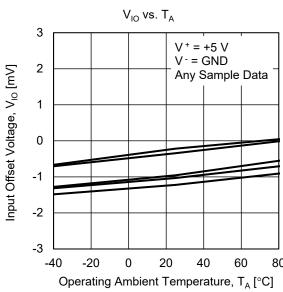
• Threshold Voltage

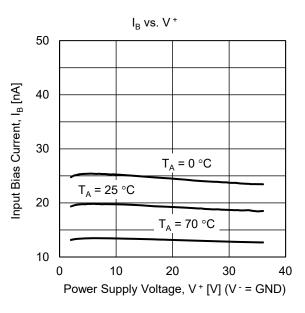
$$\begin{split} V_{\text{TH (High)}} & \doteq V_{\text{REF}} + \frac{R_1}{R_L + R_2 + R_1} \left(V_{\text{RL}} - V_{\text{REF}} \right) \\ V_{\text{TH (Low)}} & \doteq V_{\text{REF}} - \frac{R_1}{R_1 + R_2} \left(V_{\text{REF}} - V_{\text{OL}} \right) \\ \left(V_{\text{RL}} > V_{\text{REF}} > V_{\text{OL}} \right) \end{split}$$

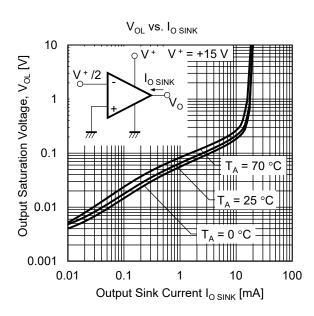
ELECTRICAL CHARACTERISTICS CURVE (T_A = 25 °C, TYP.) (Reference Value)

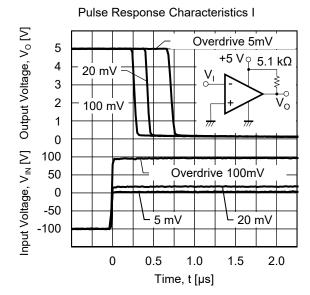


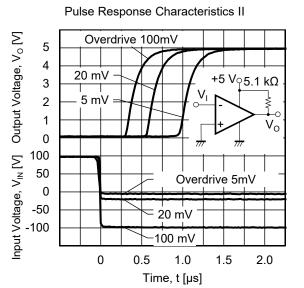










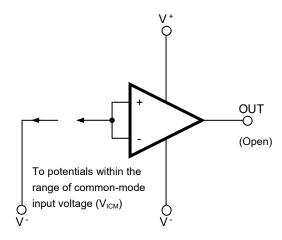


USE WITH PRECAUTIONS

. Managing unused circuits

If there is an unused circuit, the following connection is recommended.

Example of unused circuit process



Ratings of input/output pin voltage

When the voltage of input/output pin exceeds the absolute maximum rating, the parasitic diode within the IC may conduct, causing characteristics degradation or damage. In addition, if the input pin is lower than V^- , or the output pin exceeds the power supply voltage, it is recommended to make a clamping circuit using a diode with low forward voltage (e.g.: Schottky diode) as protection.

• Range of common-mode input voltage

When the supply voltage does not meet the condition of electrical characteristics, the range of common-mode input voltage is as follows.

$$V_{ICM}$$
 (TYP.): $V^- \sim V^+ - 1.5$ [V] (T_A = 25 °C)

During designing, do include some tolerance by considering temperature characteristics etc.

Regarding Input Current

The Input Bias Current $[I_B]$ specified in the electrical characteristics table, is the average value of current flowing through the +input terminal $[I_N]$ and the current flowing through the –input terminal $[I_I]$ in the balanced state of the differential amplifier circuit of the input stage (with negative feedback).

Therefore, since the differential amplifier circuit of the input stage is not balanced during comparison operation (in the case of comparator operation), the input current flows twice as much towards the low potential terminal.

Handling of ICs

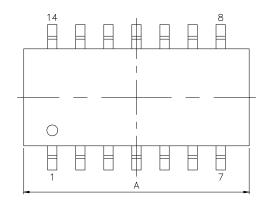
When stress is added to the ICs due to warpage or bending of a board, the characteristic may fluctuates due to piezoelectric (piezo) effect. Therefore, pay attention to warpage or bending of a board.

PACKAGE DRAWINGS

14-PIN PLASTIC SOP

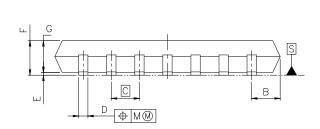
JEITA Package code	RENESAS code	Previous code	MASS (TYP.) [g]
P-SOP14-0225-1.27	PRSP0014DI-A	P14GR-50-225B	0.14

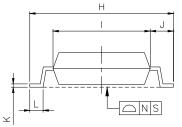
Unit: mm



detail of lead end







NOTE

Each lead centerline is located within 0.12 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
A	10.2 ±0.26
В	1.42 MAX
С	1.27 (T.P)
D	0.42 ^{+0.08} -0.07
Е	0.1 ±0.1
F	1.59 ^{+0.21} -0.2
G	1.49
H	6.5 ±0.2
	4.4 ±0.1
J	1.1 ±0.16
K	0.17 ^{+0.08} _{-0.07}
L	0.6 ±0.2
М	0.1
N	0.10
Р	3° +7° -3°

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Renesas Electronics Corporation TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061, Japan

Renesas Electronics America Inc. 1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A. Tel: +1-408-432-8888, Fax: +1-408-434-5351

Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tei: +44-1628-651-700

Renesas Electronics Europe GmbH Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709 Quantum Plaza, No.27 ZhichunLu, Haidian District, Beijing, 100191 P. R. China
Tel: +861-0-235-1155, Fax: +861-0-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, 200333 P. R. China
Tei: +86-21-2226-0888, Fas: +86-21-2226-0999

nesas Electronics Hong Kong Limited it 1601-1611, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong Tel: +852-2265-6688, Fax: +852 2886-9022

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangro Darut Ebsan, Malaysia Tel: +60-3-7955-9390, Fax. +60-3-7955-9510

Tel: -00-5-7955-9599, rax. -90-5-7955-9510 Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd. 17F, KAMCO Yangjae Tower, 262, Gangnam-daero, Gangnam-gu, Seoul, 06265 Korea Tel: +82-2-558-3737. Fax: +82-2-558-5338