輸出令 Provisions ((export order)		ided Table 1			CU+84									
order)	項 etions) 1	イロハ	輸出令 (export Provisions of the Appended Table 1 of the Export Control Order and the Appended Table of the Foreign Exchange Order)				ion num	ıber)	EU規制文言(英文)(EU regulatory wording)	EU規制の差異注意(要)(Attention to differences in EU regulations (required))	l Area	Ri Area	To(2) Area	Chi Area
7 6	1	alphabet)	細番(fine number)		5桁(5 digits)	細番(fine	e humber)		EU況前又言(央文)(EU regulatory wording)	EU規制の左共注息(安)(Attention to differences in EU regulations (required))	Group A	Group B	Group C	Group D
		alonabet/			3A001, 3A002, 3A003, 3B001, 3B002, 3C001, 3C002, 3C002, 3C003, 3C004, 3C005,						GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —				3C006 3A001 a	a			3A001 Electronic items as follows:		GBL	SGBL	SGBL	
7(1) 6	1 —	 1			3A001 a	a 1			a. General purpose integrated circuits, as follows: 1. Integrated circuits designed or rated as radiation hardened to		SGBL GBL	SGBL	SGBL	-
7(1) 6	1 —	۔ ۲			3A001 a	a 1	a		withstand any of the following: a. A total dose of 5 x 10^3 Gy (silicon) or higher;		SGBL GBL	SGBL	SGBL	-
7(1) 6	1—	7	(二)		3A001 a	a 1	b		b. A dose rate upset of 5 x 10^6 Gy (silicon)/s or higher; or		SGBL GBL			-
7(1) 6	1—	7	(三)		3A001 é	a 1	c		c. A fluence (integrated flux) of neutrons (1 MeV equivalent) of 5 x 10^13 n/cm2 or higher on silicon, or its equivalent for other materials;		SGBL GBL SGBL	SGBL SGBL	SGBL SGBL	-
7(1) 6	1 —	П			3A001	a 2			2. "Microprocessor microcircuits", "microcomputer microcircuits", microcontroller microcircuits, storage integrated circuits manufactured from a compound semiconductor, analogue-to-digital converters, integrated circuits that contain analogue-to-digital converters and store or process the digitised data, digital-to-analogue converters, electro-optical or "optical integrated circuits" designed for "signal processing", field programmable logic devices, custom integrated circuits for which either the function is unknown or the control status of the equipment in which the integrated circuit will be used is unknown, Fast Fourier Transform (FFT) processors, Static Random-Access Memories (SRAMs), or 'non-volatile memories', having any of the following: Note: 3A001.a.2. does not control integrated circuits designed for civil automobiles or railway train applications. Technical Note: Non-volatile memories' are memories with data retention over a period of time after a power shutdown.		GBL SGBL	SGBL	SGBL	_
									 "Microprocessor microcircuit" (3) means a "monolithic integrated circuit" or "multichip integrated circuit" containing an arithmetic logic unit (ALU) capable of executing a series of general purpose instructions from an external storage. N.B. 1: The "microprocessor microcircuit" normally does not contain integral user-accessible storage, although storage present on-the- chip may be used in performing its logic function. N.B. 2: This includes chip sets which are designed to operate together to provide the function of a "microprocessor microcircuit". "Microcomputer microcircuit" (3) means a "monolithic integrated circuit" or "multichip integrated circuit" containing an arithmetic logic unit (ALU) capable of executing general purpose instructions from an internal storage, on data contained in the internal storage. N.B.: The internal storage may be augmented by an external storage. 		GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —		(—)		3A001 a	a 2	a		a. Rated for operation at an ambient temperature above 398 K (125 $^\circ$ C);		GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —		(二)	¢	3A001 a	a 2	b		b. Rated for operation at an ambient temperature below 218 K (– 55° C); or		GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —		(三)		3A001 a	a 2	c		c. Rated for operation over the entire ambient temperature range from 218 K (-55°C) to 398 K (125°C);		GBL GBL SGBL	SGBL	SGBL	-
7(1) 6	1—				3A001 é	a 3			3. "Microprocessor microcircuits", "microcomputer microcircuits" and microcontroller microcircuits, manufactured from a compound semiconductor and operating at a clock frequency exceeding 40 MHz;		GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —	Ξ									GBL SGBL	SGBL	SGBL	-
7(1) 6	1—	 赤		•	3A001 a	a 5			5. Analogue-to-Digital Converter (ADC) and Digital-to-Analogue Converter (DAC) integrated circuits, as follows:		GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —	·····	(—)		3A001 a	a 5	a		a. ADCs having any of the following:		GBL	SGBL	SGBL	-
7(1) 6	1 —	*	(—) 1	<u>.</u>	3A001 a	a 5	a 1		1. A resolution of 8 bit or more, but less than 10 bit, with a "sample rate" greater than 1,3 Giga Samples Per Second (GSPS);		GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —	 木	(—) 2		3A001 a	a 5	a 2		2. A resolution of 10 bit or more, but less than 12 bit, with a "sample rate" greater than 600 Mega Samples Per Second (MSPS);		GBL SGBL	SGBL	SGBL	-
7(1) 6	1—	т	(—) 3		3A001 a	a 5	a 3		3. A resolution of 12 bit or more, but less than 14 bit, with a "sample rate" greater than 400 MSPS;		GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —		(—) 4		3A001 a	a 5	a 4		4. A resolution of 14 bit or more, but less than 16 bit, with a "sample rate" greater than 250 MSPS; or		GBL SGBL	SGBL	SGBL	-
7(1) 6	1 —	т	(—) 5	¢	3A001 a	a 5	a 5		5. A resolution of 16 bit or more with a "sample rate" greater than 65 MSPS;		GBL SGBL	SGBL	SGBL	-
7(1) 6	1—	*	(二)		3A001 a	a 5	b		b. Digital-to-Analogue Converters (DAC) having any of the following:		GBL SGBL	SGBL	SGBL	-

Group A	Ireland, United States, Argentina, Australia, Austria, Netherlands, C Sweden, Spain, Czech Republic, E Zealand, Norway, Hungary, Finlan Poland, Portugal, Luxembourg
Group B	South Korea
Group C	Other than Group A,B,D
Group D	Afghanistan, Iraq, Iran, North Kore Congo, Sudan, Somalia, Central A Lebanon
GBL	General Bulk license
<u>SGBL</u>	<u>Special General Bulk Export L</u>
SBL	Special Bulk Export License

Ireland, United States, Argentina, Italy, United Kingdom, , Netherlands, Canada, Greece, Switzerland, zech Republic, Denmark, Germany, New Hungary, Finland, France, Bulgaria, Belgium, Luxembourg

A,B,D

Iran, North Korea, Democratic Republic of the malia, Central Africa, South Sudan, Libya,

ense <u>I Bulk Export License</u>(Renesas has it)

7(1)	6	1	—	*	(二)) 1		3A001 a	5 b	1		1. A resolution of 10 bit or more but less than 12 bit, with an 'adjusted update rate' exceeding 3 500 MSPS; or	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	赤	(二)) 2		3A001 a	5 b	2		2. A resolution of 12 bit or more and having any of the following:	GBL	SGBL	SGBL	-
7(1)	6	1	—	······· 赤	(=)	2	—	3A001 a	5 b	2	a	a. An 'adjusted update rate' exceeding 1 250 MSPS but not exceeding 3 500 MSPS, and having any of the following: 1. A settling time less than 9 ns to arrive at or within	SGBL			
												0,024% of full scale from a full scale step; or 2. A 'Spurious Free Dynamic Range' (SFDR) greater than 68 dBc (carrier) when synthesising a full scale analogue signal of 100 MHz or the highest full scale analogue signal frequency specified below 100 MHz;	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	*	(=)	2	=	3A001 a	5 b	2	b	or b. An 'adjusted update rate' exceeding 3 500 MSPS;	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	_	~				3A001 a	6			 6. Electro-optical and "optical integrated circuits", designed for "signal processing" and having all of the following: a. One or more than one internal "laser" diode; b. One or more than one internal light detecting element; and c. Optical waveguides; "Optical integrated circuit" (3) means a "monolithic integrated circuit" or a "hybrid integrated circuit", containing one or more parts designed to function as a photosensor or photoemitter or to perform (an) optical or (an) electro-optical function(s). "Signal processing" (3 4 5 6) means the processing of externally derived information-bearing signals by algorithms such as time compression, filtering, extraction, selection, correlation, convolution or transformations between domains (e.g., fast Fourier transform or Walsh transform). "Laser" (0 1 2 3 5 6 7 8 9) is an item that produces spatially and temporally coherent light through amplification by stimulated emission of radiation. 	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	۲				3A001 a	7			 7. Field programmable logic devices having any of the following: Note: 3A001.a.7. includes: Complex Programmable Logic Devices (CPLDs) Field Programmable Gate Arrays (FPGAs) Field Programmable Logic Arrays (FPLAs) Field Programmable Interconnects (FPICs) N.B. For integrated circuits having field programmable logic devices that are combined with an analogue-to-digital converter, see 3A001.a.14. 	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	_	<u>۲</u>	(—))		3A001 a	7 a			a. A maximum number of single-ended digital input/outputs of	GBL			
7(1)	Ŭ								, , , , , , , , , , , , , , , , , , ,			greater than 700; or	SGBL	SGBL	SGBL	-
7(1)	6	1	-	r r	(二))		3A001 a	/ b			b. An 'aggregate one-way peak serial transceiver data rate' of 500 Gb/s or greater;	GBL SGBL	SGBL	SGBL	-
7(1)	6		—	チ 				3A001 a	9			9. Neural network integrated circuits;	200 1902	SGBL	SGBL	-
7(1)	6	1	—	y y				3A001 a	10			10. Custom integrated circuits for which the function is unknown, or the control status of the equipment in which the integrated circuits will be used is unknown to the manufacturer, having any of the following:	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	IJ	(—))		3A001 a	10 a			a. More than 1 500 terminals;	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	IJ	()			3A001 a	10 b			 b. A typical "basic gate propagation delay time" of less than 0,02 ns; or "Basic gate propagation delay time" (3) means the propagation delay time value corresponding to the basic gate used in a "monolithic integrated circuit". For a 'family' of "monolithic integrated circuits", this may be specified either as the propagation delay time per typical gate within the given 'family' or as the typical propagation delay time per gate within the given 'family'. N.B. 1: "Basic gate propagation delay time" is not to be confused with the input/output delay time of a complex "monolithic integrated circuit". N.B. 2: "Family' consists of all integrated circuits to which all of the following are applied as their manufacturing methodology and specifications except their respective functions: a. The common hardware and software architecture; b. The common basic characteristics. 	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	IJ	(三))		3A001 a	10 c			c. An operating frequency exceeding 3 GHz;	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	-	ד				3A001 a	11			11. Digital integrated circuits, other than those described in 3A001.a.3. to 3A001.a.10. and 3A001.a.12., based upon any compound semiconductor and having any of the following:	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	<u>ج</u>	(—))		3A001 a	11 a			a. An equivalent gate count of more than 3 000 (2 input gates); or	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	<u>ج</u>	(二))		3A001 a	11 b			b. A toggle frequency exceeding 1,2 GHz;	GBL	SGBL	SGBL	-
7(1)	6	1	—	ι JL			<u>.</u>	3A001 a	12			12. Fast Fourier Transform (FFT) processors having a rated execution time for an N-point complex FFT of less than (N log2 N) /20 480 ms, where N is the number of points;	<u>SGBL</u> GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	J J		<u>.</u>		3A001 a	13			13. Direct Digital Synthesizer (DDS) integrated circuits having any of the following:	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	F	(—))		3A001 a	13 a			a. A Digital-to-Analogue Converter (DAC) clock frequency of 3,5 GHz or more and a DAC resolution of 10 bit or more, but less than 12 bit; or	GBL SGBL	SGBL	SGBL	-
7(1)	6	1	—	7	(=))		3A001 a	13 b			b. A DAC clock frequency of 1,25 GHz or more and a DAC resolution of 12 bit or more;	GBL SGBL	SGBL	SGBL	-

7(1)	6	6 1	—	ס ס		3A001	a	14		3A001 Electronic items as follows: a. General purpose integrated circuits, as follows:	[]		Γ	
										 a. General purpose integrated circuits, as follows: 14. Integrated circuits that perform or are programmable to perform all of the following: a. Analogue-to-digital conversions meeting any of the following: 1. A resolution of 8 bit or more, but less than 10 bit, with a "sample rate" greater than 1,3 Giga Samples Per Second (GSPS); 2. A resolution of 10 bit or more, but less than 12 bit, with a "sample rate" greater than 1,0 GSPS; 3. A resolution of 12 bit or more, but less than 14 bit, with a "sample rate" greater than 1,0 GSPS; 4. A resolution of 14 bit or more, but less than 16 bit, with a "sample rate" greater than 400 Mega Samples Per Second (MSPS); or 5. A resolution of 16 bit or more with a "sample rate" greater that 180 MSPS; and b. Any of the following: Storage of digitised data; or Processing of digitised data; 	GBL SGBL	SGBL	SGBL	-
7(80)2			八号の二			3A001	g			g. Solid-state pulsed power switching thyristor devices and 'thyristor modules', using either electrically, optically, or electron radiation controlled switch methods and having any of the following: Note 1: 3A001.g. includes: - Silicon Controlled Rectifiers (SCRs) - Electrical Triggering Thyristors (ETTs) - Light Triggering Thyristors (LTTs) - Integrated Gate Commutated Thyristors (IGCTs) - Gate Turn-off Thyristors (GTOs) - MOS Controlled Thyristors (MCTs) - Solidtrons Note 2: 3A001.g. does not control thyristor devices and 'thyristor modules' incorporated into equipment designed for civil railway or "civil aircraft" applications. Technical Note: For the purposes of 3A001.g., a 'thyristor module' contains one or more thyristor devices.	GBL SGBL	SGBL	SGBL	-
7(8 の 2			八号の二			3A001	g	1		1. A maximum turn-on current rate of rise (di/dt) greater than 30 000 A/μs and off-state voltage greater than 1 100 V; or	GBL SGBL	SGBL	SGBL	-
7(80)2) 6		八号の二			3A001	g	2		2. A maximum turn-on current rate of rise (di/dt) greater than 2 000 A/μs and having all of the following: a. An off-state peak voltage equal to or greater than 3 000 V; and b. A peak (surge) current equal to or greater than 3 000 A.	GBL SGBL	SGBL	SGBL	-
7(80)3) 6	6 1	八号の三			3A001	h			 h. Solid-state power semiconductor switches, diodes, or 'modules', having all of the following: Rated for a maximum operating junction temperature greater than 488 K (215° C); Repetitive peak off-state voltage (blocking voltage) exceeding 300 V; and Continuous current greater than 1 A. Note 1: Repetitive peak off-state voltage in 3A001.h. includes drain to source voltage, collector to emitter voltage, repetitive peak reverse voltage and peak repetitive off-state blocking voltage. 	GBL SGBL	SGBL	SGBL	-
										Note 2: 3A001.h. includes: - Junction Field Effect Transistors (JFETs) - Vertical Junction Field Effect Transistors (VJFETs) - Metal Oxide Semiconductor Field Effect Transistors (MOSFETs) - Double Diffused Metal Oxide Semiconductor Field Effect Transistor (DMOSFET) - Insulated Gate Bipolar Transistors (IGBT) - High Electron Mobility Transistors (HEMTs) - Bipolar Junction Transistors (BJTs) - Thyristors and Silicon Controlled Rectifiers (SCRs) - Gate Turn-Off Thyristors (GTOs) - Emitter Turn-Off Thyristors (ETOs) - PiN Diodes - Schottky Diodes	GBL SGBL	SGBL	SGBL	-
										Note 3: 3A001.h. does not control switches, diodes, or 'modules', incorporated into equipment designed for civil automobile, civil railway or "civil aircraft" applications. Technical Note: For the purposes of 3A001.h., 'modules' contain one or more solid-state power semiconductor switches or diodes.	GBL SGBL	SGBL	SGBL	-

2.	Category 5 Part2 - Product As of 14/April,2021				Please check SG	BL (Renes	as Electror	nics Corpora	ation has it)
· 南	貨物等省令(Ordinance of the Ministry Specifying Goods and Technologies Pursuant to Provisions of the 出令 Appended Table 1 of the Export Control Order and the Appended Table of the Foreign Exchange Order)	EU規制番号(EU regulation number)	参考(Refference)	. EU規制文言(英文)(EU regulatory wording)	EU規制の差異注意 (要)(Attention to differences in FU	l Area	Ri Area	To(2) Area	Chi Area
C	rder) 条 項 号 イロハ (articles)) (issues) alphabet)	5桁(5 digits) 細番(fine humber)	対比用文言(Contrast wording)	LO成响文音(英文)(EU regulatory wording)	regulations (required))	Group A	Group B	Group C	Group D

											(Comprehensive notebook GISHN added)
						 			 	 	(Part 2 note revised)
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9(7)	8	1	九				5A002	а			

GENERAL "INFORMATION SECURITY" NOTE (GISN) "Information security" items or functions should be considered against the provisions in Category 5 - Part 2, even if they are components, "software" or functions of other items. "Information security" (GSN GISN 5) is all the means and functions ensuring the accessibility, confidentiality or integrity of information or communications, excluding the means and functions intended to safeguard against malfunctions. This includes "cryptography", "cryptographic activation", 'cryptanalysis', protection against compromising emanations and computer security. Technical Note: 'Cryptanalysis': analysis of a cryptographic system or its inputs and outputs to derive confidential variables or sensitive data, including clear text. "Cryptography" (5) means the discipline which embodies principles, means and methods for the transformation of data in order to hide its information content, prevent its undetected modification or prevent its unauthorized use. "Cryptography" is limited to the transformation of information using one or more 'secret parameters' (e.g., crypto variables) or associated key management.	GBL SGBL	SGBL	SGBL	-
 Part 2 - "INFORMATION SECURITY" Note 1: Not used. Note 2: Category 5 - Part 2 does not control products when accompanying their user for the user's personal use. "Note 3: Cryptography Note 5A002, 5D002.a.1., 5D002.b. and 5D002.c.1. do not control items as follows:" a. Items that meet all of the following: 1. Generally available to the public by being sold, without restriction, from stock at retail selling points by means of any of the following: a. Over-the-counter transactions; b. Mail order transactions; c. Electronic transactions; 2. The cryptographic functionality cannot easily be changed by the user; 3. Designed for installation by the user without further substantial support by the supplier; and 4. When necessary, details of the goods are accessible and will be provided, upon request, to the competent authorities of the Member State in which the exporter is established in order to ascertain compliance with conditions described in paragraphs 1. to 3. above; 	GBL SGBL	SGBL	SGBL	-
 b. Hardware components or 'executable software', of existing items described in paragraph a. of this Note, that have been designed for these existing items, meeting all of the following: "Information security" is not the primary function or set of functions of the component or 'executable software'; The component or 'executable software' does not change any cryptographic functionality of the existing items, or add new cryptographic functionality to the existing items; The feature set of the component or 'executable software' is fixed and is not designed or modified to customer specification; and When necessary as determined by the competent authorities of the Member State in which the exporter is established, details of the component or 'executable software' and details of relevant end-items are accessible and will be provided to the competent authority upon request, in order to ascertain compliance with conditions described above. 	GBL SGBL	SGBL	SGBL	-
Technical Note: For the purpose of the Cryptography Note, 'executable software' means "software" in executable form, from an existing hardware component excluded from 5A002 by the Cryptography Note. Note: 'Executable software' does not include complete binary images of the "software" running on an end-item. Note to the Cryptography Note: 1. To meet paragraph a. of Note 3, all of the following must apply: a. The item is of potential interest to a wide range of individuals and businesses; and b. The price and information about the main functionality of the item are available before purchase without the need to consult the vendor or supplier. A simple price enquiry is not considered to be a consultation. 2. In determining eligibility of paragraph a. of Note 3, competent authorities may take into account relevant factors such as quantity, price, required technical skill, existing sales channels, typical customers, typical use or any exclusionary practices of the supplier.	GBL SGBL	SGBL	SGBL	-
5A002 "Information security" systems, equipment and components, as follows: N.B. For the control of "satellite navigation system" receiving equipment containing or employing decryption, see 7A005 and for related decryption "software" and "technology" see 7D005 and 7E001.	GBL SGBL	SGBL	SGBL	-

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9(7)	8	1	九	ſ	(三)				5A002	a			 5A002Technical Note 2.b.3	<u>†1</u>
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, Ł	a. Designed or modified to use 'cryptography for data confidentiality' having a described security algorithm', where that cryptographic capability is usable, has been activated, or can be activated by any means other than secure "cryptographic activation", as follows:				
٢	Technical Notes:	GBL SGBL	SGBL	SGBL	-
f	2. For the purposes of 5A002.a., 'described security algorithm' means any of the following:				
	a. A "symmetric algorithm" employing a key length in excess of 56 bits, not				
	ncluding parity bits; b. An "asymmetric algorithm" where the security of the algorithm is based on any of the following:	GBL	SGBL	SGBL	_
·····	 Factorisation of integers in excess of 512 hits (e.g. RSA): Computation of discrete logarithms in a multiplicative group of a finite 	GBL			
f	ield of size greater than 512 bits (e.g., Diffie-Hellman over Z/pZ); or 3. Discrete logarithms in a group other than mentioned in paragraph b.2.	SGBL GBL	SGBL	SGBL	-
i	n excess of 112 bits (e.g., Diffie-Hellman over an elliptic curve); or c. An "asymmetric algorithm" where the security of the algorithm is based on	SGBL	SGBL	SGBL	-
	any of the following: 1. Shortest vector or closest vector problems associated with lattices (e.g.,	GBL SGBL	SGBL	SGBL	-
	NewHoneErodoNTRUEporvintKvberTitanium): 2. Finding isogenies between Supersingular elliptic curves (e.g., Supersingular Isogeny Key Encapsulation);	GBL SGBL	SGBL	SGBL	-
	3. Decoding random codes (e.g., McEliece, Niederreiter).	GBL SGBL	SGBL	SGBL	-
	1. Items having "information security" as a primary function;	GBL	SGBL	SGBL	-
	2. Digital communication or networking systems, equipment or components, not	GBL GBL	SGBL	SGBL	-
	specified in 5A002.a.1.; 3. Computers, other items having information storage or processing as a	SGBL			
1	primary function, and components therefor, not specified in 5A002.a.1. or 5A002.a.2.; N.B. For operating systems, see also 5D002.a.1. and 5D002.c.1.	GBL SGBL	SGBL	SGBL	-
	4. Items, not specified in 5A002.a.1. to 5A002.a.3., where the 'cryptography for data confidentiality' having a 'described security algorithm' meets all of the following:	GBL			
s	 a. It supports a non-primary function of the item; and b. It is performed by incorporated equipment or "software" that would, as a standalone item, be specified in Category 5 – Part 2. 	SGBL	SGBL	SGBL	-
	Note2: 5A002.a. does not control any of the following items, or specially designed 'information security'' components therefor:	GBL SGBL	SGBL	SGBL	-
	a. Smart.cards.and smart.card.'readers/writers' as follows: 1. A smart card or an electronically readable personal document (e.g., token coin, e-passport) that meets any of the following:	GBL SGBL	SGBL	SGBL	-
	a. The cryptographic capability meets all of the following:	GBL	SGBL	SGBL	-
	1. It is restricted for use in any of the following: a. Equipment or systems not described by 5A002.a.1. to 5A002.a.4.;	GBL SGBL	SGBL	SGBL	-
	b. Equipment or systems not using 'cryptography for data	GBL	SGBL	SGBL	-
	confidentiality' having a 'described security algorithm'; or c. Equipment or systems, excluded from 5A002.a., by paragraphs b. to f. of this Note; and	GBL SGBL	SGBL	SGBL	-
	2. It cannot be reprogrammed for any other use: or: b. Having all of the following:				
t " "	 It is specially designed and limited to allow protection of 'personal data' stored within; Has been, or can only be, personalised for public or commercial transactions or individual identification; and Where the cryptographic capability is not user-accessible; Technical Note: Personal data' includes any data specific to a particular person or entity, such as the amount of money stored and data necessary for 'authentication". Technical Note: Personal data' includes any data specific to a particular person or entity, such as the amount of money stored and data necessary for "authentication". Readers/writers' specially designed or modified, and limited, for items 	GBL SGBL	SGBL	SGBL	-
	specified in paragraph a.1. of this Note. Technical Note: Readers/writers' include equipment that communicates with smart cards or electronically readable documents through a network.	GBL SGBL	SGBL	SGBL	-
	b. Cryptographic equipment specially designed and limited for banking use or money transactions'; Technical Note: Money transactions' in 5A002.a. Note 2.b. includes the collection and settlement of faces or credit functions.	GBL SGBL	SGBL	SGBL	-
c e A	c. Portable or mobile radiotelephones for civil use (e.g., for use with commercial civil cellular radio communication systems) that are not capable of transmitting encrypted data directly to another radiotelephone or equipment (other than Radio Access Network (RAN) equipment), nor of passing encrypted data through RAN equipment (e.g., Radio Network Controller (RNC) or Base Station Controller (BSC));	GBL SGBL	SGBL	SGBL	-
ı	d. Cordless telephone equipment not capable of end-to-end encryption where the maximum effective range of unboosted cordless operation (i.e. a single, unrelayed hop between terminal and home base station) is less than 400 metres according to the manufacturer's specifications:	GBL SGBL	SGBL	SGBL	-
t (e. Portable or mobile radiotelephones and similar client wireless devices for civil use, that implement only published or commercial cryptographic standards (except for anti-piracy functions, which may be non-published) and also meet the provisions of paragraphs a.2. to a.4. of the Cryptography Note (Note 3 in Category 5, Part 2), that have been customised for a specific civil industry application with	GBL SGBL	SGBL	SGBL	-

								,				 	
9(7)	8	1	九	1	(十六)				5A002	а			5A002.a.Note2.f
9(7)	8	1	九	1	(+七)				5A002	а		 	 5A002.a.Note2.g
9(7)	8	1	九	1	(十八)				5A002	а		 	 5A002.a.Note2.h
9(7)	8	1	九	۲ ۲	(十九)				5A002	а			 5A002.a.Note2.i
9(7)	8	1	九	۲ ۲	(十九)				5A002	а	••••••	 	 5A002.a.Note2.i.1
9(7)	8	1	九	1	(十九)	2			5A002	а		 	 5A002.a.Note2.i.2
9(7)	8	1	九	1	(二十)				5A002	а			5A002.a.Note2.j
9(7)	8	1	九	1	(二十)				5A002	а			5A002.a.Note2.j.1
9(7)	8	1	九	1	(二十)	1	—		5A002	а			5A002.a.Note2.j.1.a
9(7)	8	1	九	ſ	(二十)	1	_		5A002	а			5A002.a.Note2.j.1.b
9(7)	8	1			(二+)	2			5A002			 	
5(1)	0	Ţ	九	1	()	2			37002	a			50002.8.100162.3.2
9(7)	8	1	九	П					5A002	b			
9(7)	8	1	九	П	(—)				5A002	b	1	 	
9(7)	8	1	九	П	(二)				5A002	b	2		
9(7)	8	1	九	л					5A002	с			
0(7)	8	1	+						5A002	d		 	
9(7)	0	1	九	—					SAUUZ	a			
9(7)	8	1	九	_	(—)				5A002	d	1		
	8								5A002		2	 	
9(7)	8									е		 	
9(7)	8	1	九	~					5A002		•••••	 	 Cat5part2のノート3 Cryptograpy Note
9(7)	8	1	九		(—)				5A002			 	 Note3a
0(7)	_				-				5 4 0 0 0				N 21
9(7)	8	1	九	^	(二)				5A002				Note3b
9(7)	8		+-						5A004				
L						ē	ā	ä	L		غ	 ā	 I

"pe	f. Items, where the "information security" functionality is limited to wireless rsonal area network" functionality, implementing only published or commercial otographic standards:	GBL SGBL	SGBL	SGBL	-
des the	g. Mobile telecommunications Radio Access Network (RAN) equipment signed for civil use, which also meet the provisions of paragraphs a.2. to a.4. of cryptography Note (Note 3 in Category 5, Part 2), having an RF output power ited to 0,1W (20 dBm) or less, and supporting 16 or fewer concurrent users.	GBL SGBL	SGBL	SGBL	-
fun	n. Routers, switches, gateways or relays, where the "information security" actionality is limited to the tasks of "Operations, Administration or Maintenance" AM") implementing only published or commercial cryptographic standards; or	GBL SGBL	SGBL	SGBL	-
sec	i. General purpose computing equipment or servers, where the "information curity" functionality meets all of the following:	GBL SGBL	SGBL	SGBL	-
	1. Uses only published or commercial cryptographic standards; and	GBL SGBL	SGBL	SGBL	-
Par	 2. Is any of the following: a. Integral to a CPU that meets the provisions of Note 3 to Category 5– rt 2; b. Integral to an operating system that is not specified in 5D002; or c. Limited to "OAM" of the equipment. 	GBL SGBL	SGBL	SGBL	-
all	j. Items specially designed for a 'connected civil industry application', meeting of the following:	GBL SGBL	SGBL	SGBL	-
u	1. Being any of the following:	GBL	SGBL	SGBL	-
arb or	 a. A network-capable endpoint device meeting any of the following: The "information security" functionality is limited to securing 'non- bitrary data' or the tasks of "Operations, Administration or Maintenance" ("OAM"); The device is limited to a specific 'connected civil industry 	GBL SGBL	SGBL	SGBL	-
'coi or t	 b. Networking equipment meeting all of the following: Being specially designed to communicate with the devices specified baragraph j.1.a. above; and The "information security" functionality is limited to supporting the nnected civil industry application' of devices specified in paragraph j.1.a. above, the tasks of "OAM" of this networking equipment or of other items specified in ragraph j. of this Note; and 	GBL SGBL	SGBL	SGBL	-
	 Where the "information security" functionality implements only published commercial cryptographic standards, and the cryptographic functionality cannot silv, he, changed, by, the, user. 	GBL SGBL	SGBL	SGBL	-
Tec	b. Being a 'cryptographic activation token'; chnical Note: A 'cryptographic activation token' is an item designed or modified any of the following:	GBL SGBL	SGBL	SGBL	-
Cat	1. Converting, by means of "cryptographic activation", an item not specified in tegory 5 – Part 2 into an item specified in 5A002.a. or 5D002.c.1., and not released the Cryptography Note (Note 3 in Category 5 – Part 2); or	GBL SGBL	SGBL	SGBL	-
spe	2. Enabling, by means of "cryptographic activation", additional functionality ecified in 5A002.a. of an item already specified in Category 5 – Part 2.	GBL SGBL	SGBL	SGBL	-
	c. Designed or modified to use or perform "quantum cryptography"; Technical Note: "Quantum cryptography" is also known as Quantum Key tribution. (ОКП).	GBL SGBL	SGBL	SGBL	-
cod	d. Designed or modified to use cryptographic techniques to generate channelising des, scrambling codes or network identification codes, for systems using ultra- deband modulation techniques and having any of the following:	GBL SGBL	SGBL	SGBL	-
	1. A bandwidth exceeding 500 MHz; or	GBL SGBL	SGBL	SGBL	-
	2. A "fractional bandwidth" of 20% or more;	GBL SGBL	SGBL	SGBL	-
spr	e. Designed or modified to use cryptographic techniques to generate the reading code for "spread spectrum" systems, other than those specified in 202.d., including the hopping code for "frequency hopping" systems.	GBL SGBL	SGBL	SGBL	-
Par	TEGORY 5 - TELECOMMUNICATIONS AND "INFORMATION SECURITY" rt 2 - "INFORMATION SECURITY" Note 3: Cryptography Note 002, 5D002.a.1., 5D002.b. and 5D002.c.1. do not control items as follows:	GBL SGBL	SGBL	SGBL	-
	 a. Items that meet all of the following: Generally available to the public by being sold, without restriction, from a. Over-the-counter transactions; Mail order transactions; Electronic transactions; or Telephone call transactions; 2. The cryptographic functionality cannot easily be changed by the user; Designed for installation by the user without further substantial support the supplier; and 	GBL SGBL	SGBL	SGBL	-
con	 b. Hardware components or 'executable software', of existing items described baragraph a. of this Note, that have been designed for these existing items, eting all of the following: "Information security" is not the primary function or set of functions of the mponent or 'executable software'; The component or 'executable software' does not change any ptographic functionality of the existing items, or add new cryptographic functionality to the existing items; 	GBL SGBL	SGBL	SGBL	-
5.47	004 Systems, equipment and components for defeating, weakening or bypassing	GBL	SGBL	SGBL	

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	8	1	+-	イ				5A004	а			
 	Q	1	-					5A004	h	÷	 	
	0	T	+-	н				37004	D			
	8	1	+-	П	(—)			5A004	b			
	8	1	+-	П	(二)			5A004	b			
 						<u>.</u>		 				
	8	1	+-	П	(三)			 5A004	b			
	8	1	+-	П	(四)			 5A004	b		 	

3. Cate	gory 5 -	Tech	As of 14/A	April,2021		Please	check SG	BL (Renesa	esas Electron	
外為令 (External order)		sterial ordinance 項号 (sections)	regulation number)	EU規制文言(英文)(EU regulatory wording)	EU規制の差異 注意(要) (Attention to differences in EU regulations (required))	l Area Group A	Ri Area Group B	To(2) Area Group C	Chi Area Group D	
9	21	1	5.E.1	"Technology" according to the General Technology Note for the "development", "production" of equipment, functions or features specified by 5.A.1. or "software" specified by 5.D.1.a. or 5.D.1.e.;		GBL SGBL	SGBL	SGBL	-	
9(1)	21	1	— 5.E.1	"Technology" "required" for the "development" or "production" of equipment, functions or features specified by 5.A.1.b.3		GBL SGBL	SGBL	SBL	-	
9(1)	21	1	二 5.E.1	"Technology" "required" for the "use" (excluding operation) of equipment, functions or features specified by 5.A.1.a~j/ 5.A.2.a~e./ 5.A.3,/ 5.A.4 ,/ 5.B.2/		GBL SGBL	SGBL	SGBL	-	
9(1)	21	1	≡ 5.E.1	"Technology" "required" for the "use" (excluding operation) of equipment, functions or features specified by 5.A.2.a~e./ 5.A.3,/ 5.A.4 ,/ 5.B.2/		GBL SGBL	SGBL	SGBL	-	
	21	1	四 5.E.1	"Technology" "required" for the "use" (excluding operation) of equipment, functions or features specified by 5.A.1.a~j		GBL SGBL	SGBL	SGBL	-	
	21	1	五 5.D.1	"Program" required for the "development", "production" of equipment, functions or features specified by 5.A.1.b.3		GBL SGBL	SGBL	SBL	-	
9(1)	21	1	六 5.D.1	"Program" required for the "development", "production" of equipment, functions or features specified by 5.A.1.a~d		GBL SGBL	SGBL	SGBL	-	
9(1)	21	1	七 5.D.1	"Program" required for the "use" of equipment, functions or features specified by 5.4.2/5.4.3/5.4.4		GBL SGBL	SGBL	SGBL	-	
	21	1	八 5.D.1	"Program" required for the the "development", "production" of equipment, functions or features specified by 5.A.1.		GBL SGBL	SGBL	SGBL	-	

 		-	4	,
 a. Designed or modified to perform 'cryptanalytic functions'. Note: 5A004.a.includes systems or equipment, designed or modified to perform 'cryptanalytic functions' by means of reverse engineering. Technical Note: Cryptanalytic functions' are functions designed to defeat cryptographic mechanisms in order to derive confidential variables or sensitive data, including clear text, passwords or cryptographic keys. 	GBL SGBL	SGBL	SGBL	-
 b. Items, not specified in 4A005 or 5A004.a., designed to perform all of the following: 'Extract raw data' from a computing or communications device; and 2. Circumvent "authentication" or authorisation controls of the device, in order to perform the function described in 5A004.b.1. Technical Note: Extract raw data' from a computing or communications device means to retrieve binary data from a storage medium (e.g., RAM, flash or hard disk) of the device without interpretation by the device's operating system or filesystem. Note1: 5A004.b. does not control systems or equipment specially designed for 	GBL SGBL	SGBL	SGBL	-
 a. Debuggers, hypervisors;	 GBL SGBL	SGBL	SGBL	-
b. Items limited to logical data extraction;	GBL SGBL	SGBL	SGBL	-
c. Data extraction items using chip-off or JTAG; or	GBL SGBL	SGBL	SGBL	-
 d. Items specially designed and limited to jail-breaking or rooting.	 GBL SGBL	SGBL	SGBL	-

	21	1	八号二	5.D.1	"Program" required for the "use" of equipment, functions or features specified by 5.A.1. /5.A.2/5.A.3/5.A.4		GBL SGBL		SGBL	-
9(1)	21	1	٦. T	Note 5.D.2.c	"Program" having the characteristics of, or performing or simulating the functions of, any of the following: 1.Equipment specified by 5.A.2.a., 5.A.2.c., 5.A.2.d. or 5.A.2.e.; Note5.D.2.c.1. does not apply to "software" limited to the tasks of "OAM" implementing only published or commercial cryptographic standards.		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	+-	5.E.1	"Program" required for the "development", "production", "use"of equipment, functions or features specified by 5.A.1. d		GBL SGBL	SGBL	SBL	-
9(1)	21	1	+=	5.E.1	"Program" required for the "development", "production", "use"of equipment, functions or features specified by 5.B.1.a/.b/5.a.2		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	+=	5.E.1	"Program" required for the "development", "production" "use" of equipment, functions or features specified by 5.B.1.a/.b		GBL SGBL	SGBL	SGBL	-
9(1)	21	1	十六	_	"Technology" that has the function specified by 5.A.2, activates the encryption function of the program and the product.	要	GBL SGBL	SGBL	SGBL	-
9(1)	21	1	+七	–	"Program" that has the function specified by 5.A.2, activates the encryption function of the program and the product.	要 (Required)	GBL SGBL	SGBL	SGBL	-