

R32C/100 系列

串行接口操作（时钟同步串行接口模式下的发送）

1. 要点

本篇资料，介绍了使用时钟同步串行接口模式向外部设备传送与传送时钟同步的数据的设定方法和使用例子。

2. 说明

本篇资料，适用于 R32C/111 群单片机。

本篇应用说明也适用于 R32C/100 系列中与上面所述 R32C/111 群具有相同 SFR（特殊功能寄存器）定义的产品。关于产品功能的改进，请参看手册中的相关信息。在使用本篇应用说明的程序前，需进行详细的评价。

3. 应用实例

本篇资料，描述了使用时钟同步串行接口模式，在通信速率为 500kbps（XIN = 16MHz、PLL 时钟 = 100MHz）的条件下发送数据的情况。

通信速率的计算公式如下所示。

$$\text{通信速率} = \frac{\text{传送时钟的计数源}}{2 \times (\text{UiBRG 寄存器设定值} + 1)}$$

本篇资料中，时钟同步串行接口模式发送数据的设定条件如表 3.1 所示。

表 3.1 时钟同步串行接口模式发送数据的设定条件

项目	设定条件
传送数据长度	8 位
传送时钟	内部时钟
发送控制	CTS 功能
传送格式	LSB 先发送
发送中断请求发生时序	从 UiTB 寄存器向 UARTi 发送寄存器传送数据时（发送开始时）
CLK 极性	在传送时钟的下降沿发送数据，在上升沿接收数据
TXD、RXD 输入/输出极性切换	不反转

这里使用 CLK 输出和 TXD 输出来发送数据。在 R32C/111 中，因为要使用 CLK 输出和 TXD 输出，所以需要设定相应端口的方向位和功能选择寄存器。

CLK 引脚、TXD 引脚和端口方向位以及功能选择寄存器的设定如表 3.2 所示。

表 3.2 CLK 引脚、TXD 引脚和端口方向位以及功能选择寄存器的设定

通道	引脚	端口	端口方向位	设定值	功能选择寄存器	设定值
UART0	CLK0	P6_1	PD6_1	1	P6_1S	03h
	TXD0	P6_3	PD6_3	1	P6_3S	03h
UART1	CLK1	P6_5	PD6_5	1	P6_5S	03h
	TXD1	P6_7	PD6_7	1	P6_7S	03h
UART2	CLK2	P7_2	PD7_2	1	P7_2S	03h
	TXD2	P7_0（注 1）	PD7_0	1	P7_0S	03h
UART3	CLK3	P4_1	PD4_1	1	P4_1S	03h
	TXD3	P4_3	PD4_3	1	P4_3S	03h
UART4	CLK4	P9_5	PD9_5（注 2）	1	P9_5S（注 2）	03h
	TXD4	P9_6	PD9_6（注 2）	1	P9_6S（注 2）	03h
UART5	CLK5	P7_7	PD7_7	1	P7_7S	03h
	TXD5	P7_6	PD7_6	1	P7_6S	03h
UART6	CLK6	P4_5	PD4_5	1	P4_5S	03h
	TXD6	P4_7	PD4_7	1	P4_7S	03h

注 1：N 沟道漏极开路输出。

注 2：请在 PRC2 位设为“1”（允许改写）之后的下一条指令修改这些寄存器。另外，请不要在 PRC2 位设“1”和改写指令之间插入中断或 DMA 传送。

3.1 时钟同步串行接口模式发送数据的说明

- (1) 将 UiC1 (i = 0~6) 寄存器的 TE 位设置为“1”（允许发送）后，向 UiTB 寄存器中写入发送数据，进入发送等待状态。
- (2) 一旦 $\overline{\text{CTS}}_i$ 引脚的输入信号变为“L”，发送即开始（ $\overline{\text{CTS}}_i$ 引脚的输入信号由接收方控制）。
- (3) 与传送时钟的第一个下降沿同步，UiTB 寄存器中写入的发送数据被传送到 UARTi 发送寄存器中。同时，SiTIC 寄存器的 IR 位变为“1”（有中断请求）。另外，发送数据的 D0 位从 TXDi 引脚发送出去。然后，从 D1 位数据开始，发送数据在传送时钟的下降沿被同步发送出去。
- (4) 当一个字节的数据发送结束时，UiC0 寄存器的 TXEPT 位变为“1”（发送完成），表示发送结束。并且，传送时钟停止输出，并保持为“H”电平。
- (5) 如果在 D7 位数据输出之前，向 UiTB 寄存器中设定下一个数据，发送继续。

连接示例如图 3.1 所示，动作时序图如图 3.2 所示。

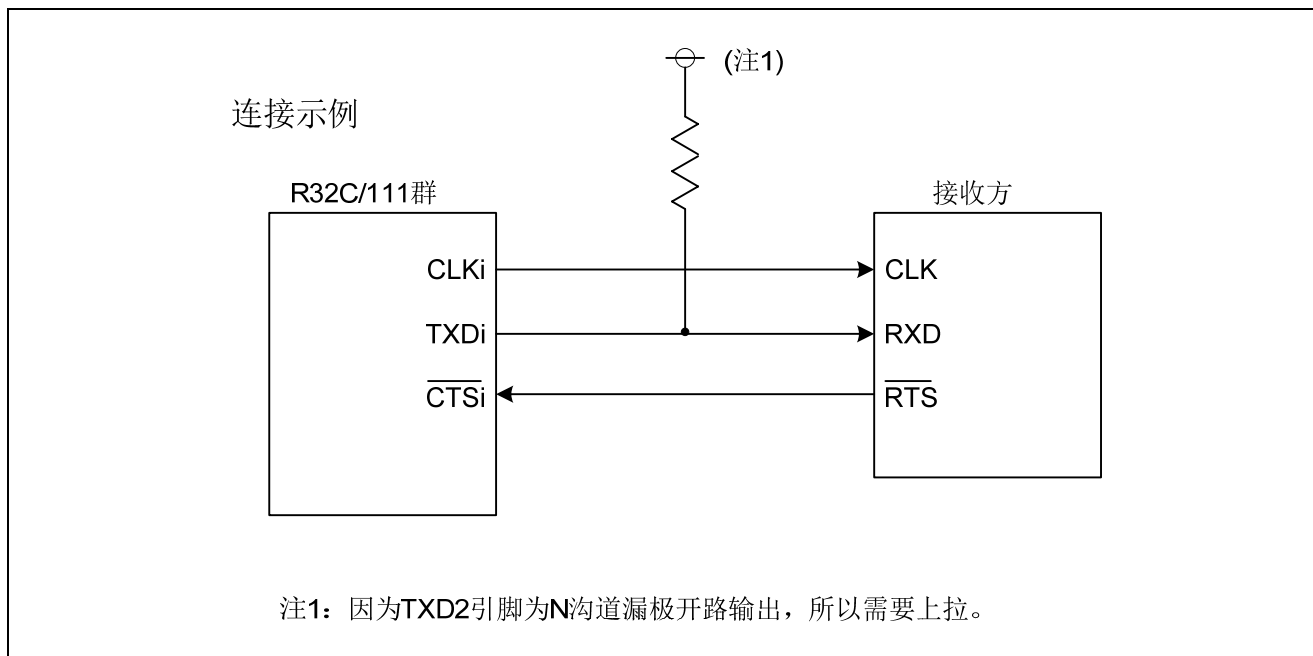
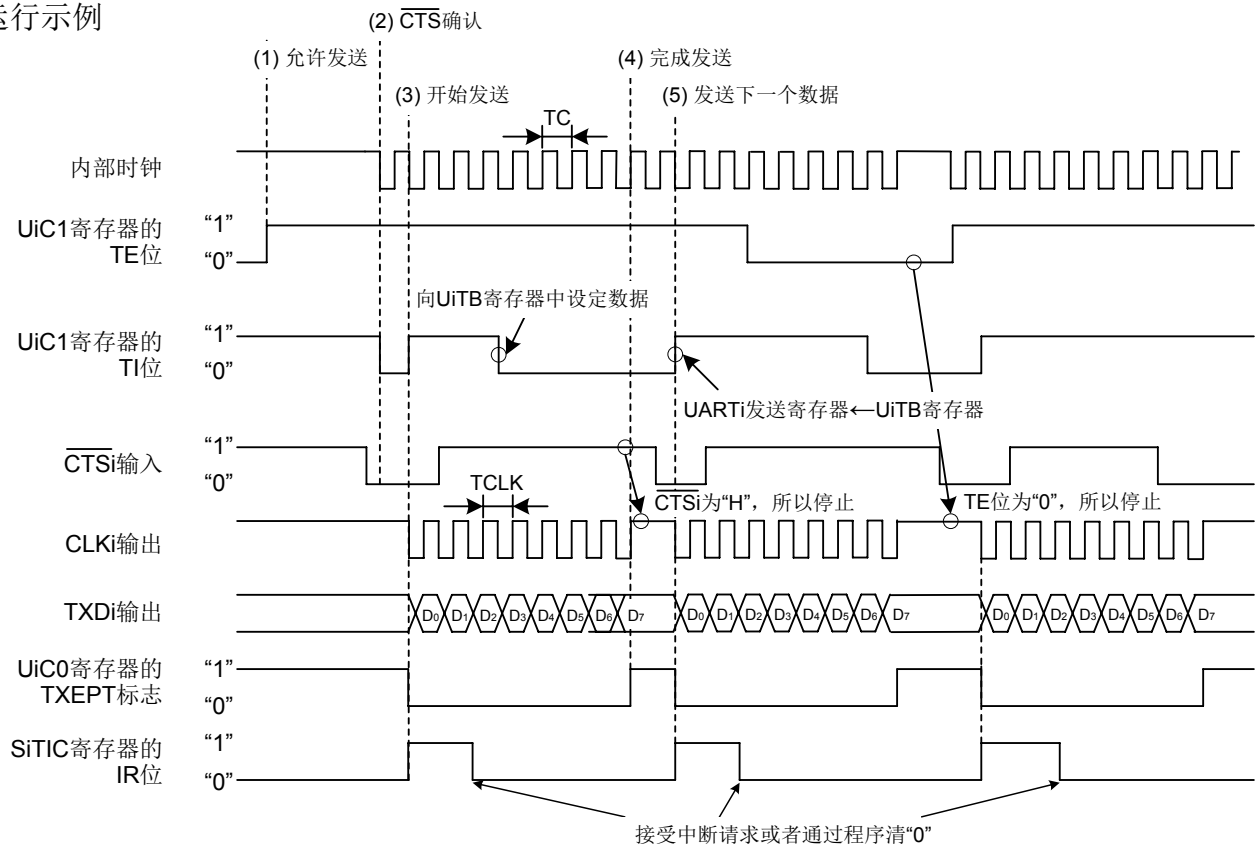


图 3.1 发送数据的连接示例

运行示例


 $i = 0 \sim 6$
 $TC = TCLK = 2(m+1)/f_j$
 f_j : UiBRG计数源的频率

 $(f_j, f_8, f_{2n} \text{ (注1)})$
 m : UiBRG寄存器的设定值

注1: 可以通过TCSPR寄存器的CNT3~CNT0选择无分频 ($n = 0$) 或者2n分频 ($n = 1 \sim 15$)。

图 3.2 发送动作时序图

3.2 设定方法

本节描述了“3.1 时钟同步串行接口模式发送数据的说明”中的设定步骤及设定值。关于各寄存器的详细信息请参照硬件手册。

参考程序中，发送数据一旦被写入 UARTi 发送缓冲寄存器（i = 0~6），发送即开始。当程序检测出 UARTi 发送中断的中断请求位变为“1”（有中断请求），并且对发送数据进行递增计数之后，再次向发送缓冲寄存器中写入数据。

主函数处理的流程图如图 3.3 所示，UARTi 初始化设置处理的流程图及寄存器设置如图 3.4 所示

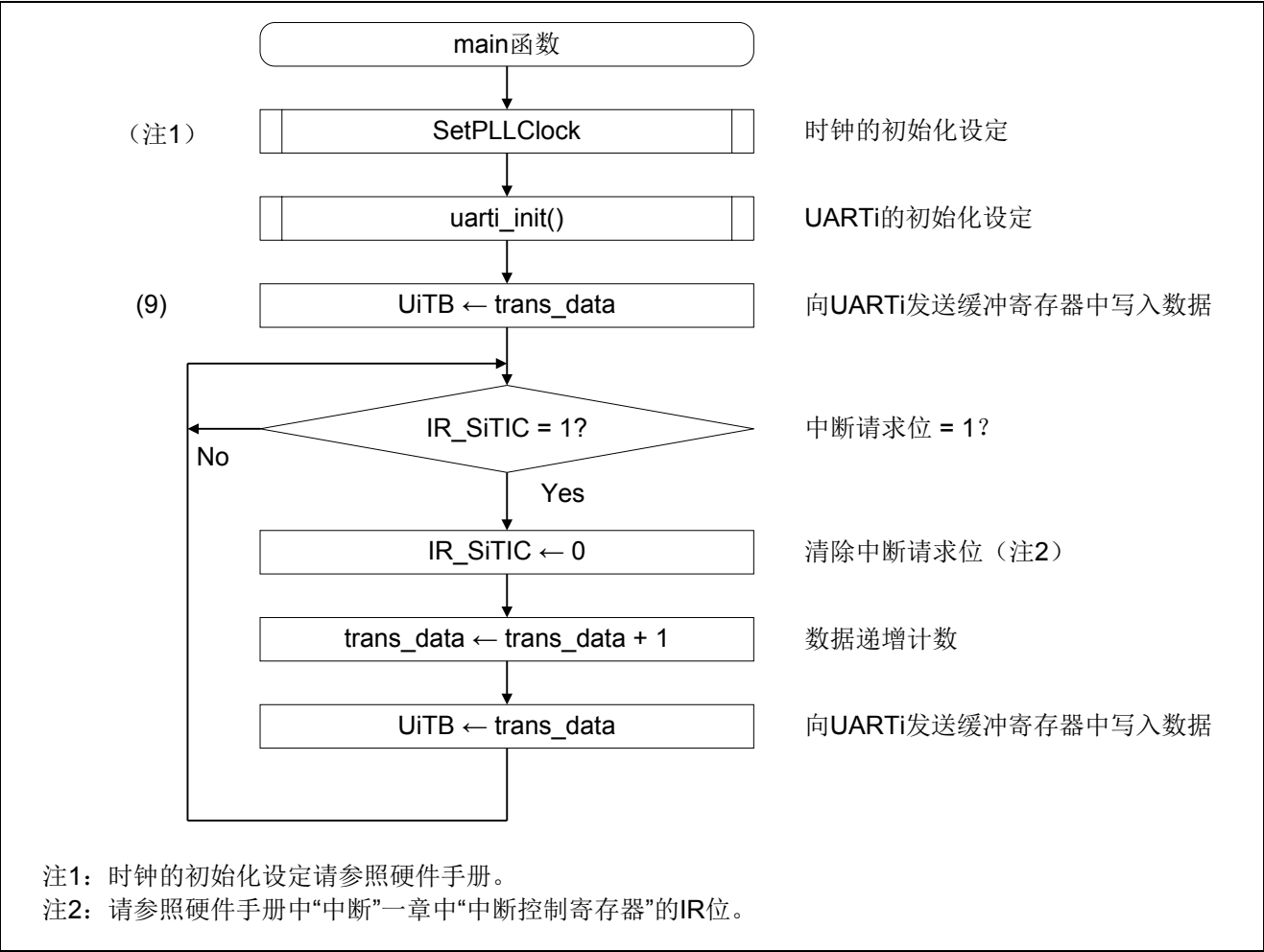


图 3.3 主函数的流程图（i = 0~6）

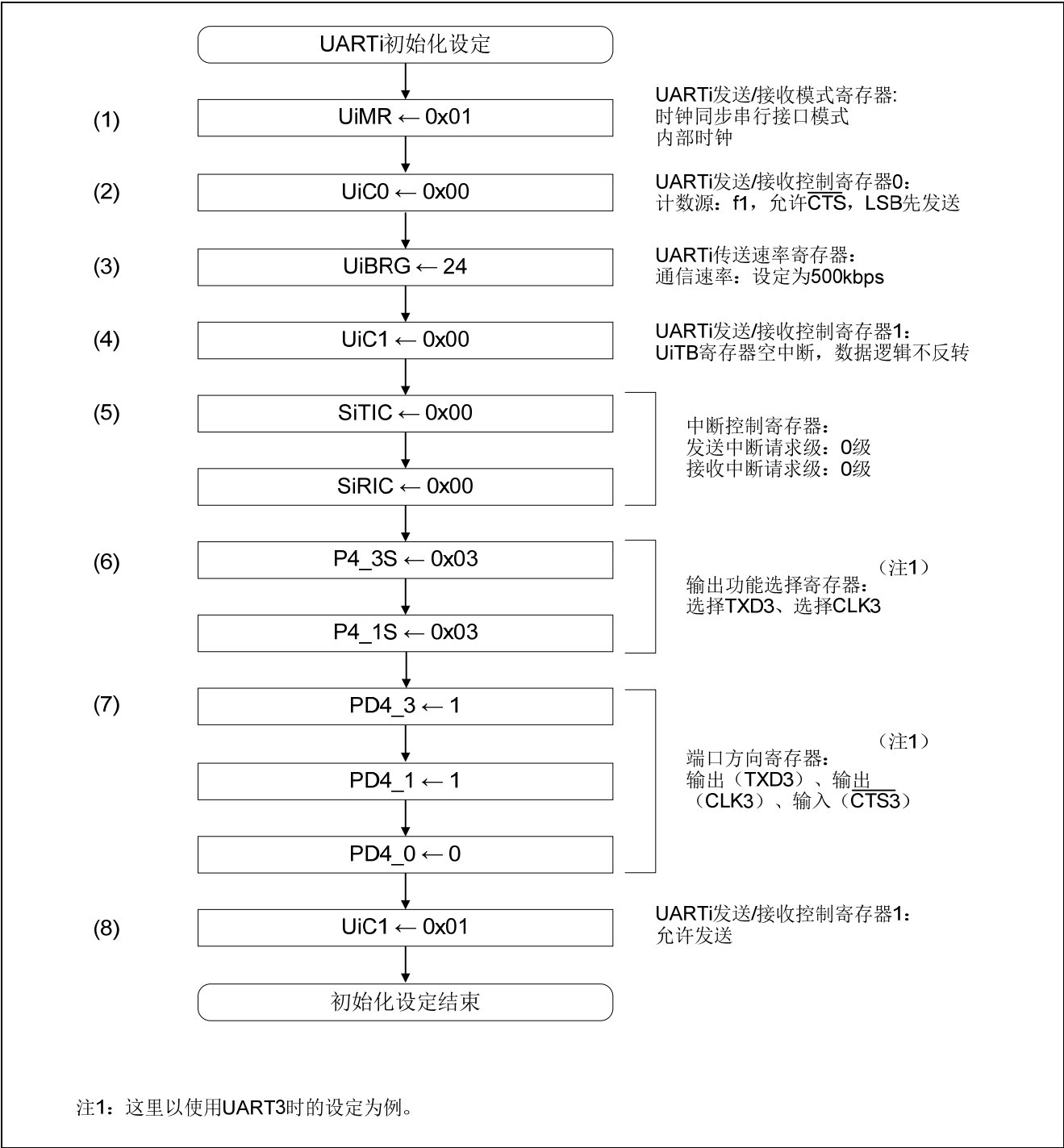
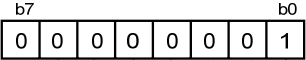


图 3.4 UARTi 初始化设定处理的流程图 (i=0~6)

3.3 详细设定步骤

设置UARTi发送/接收模式寄存器（i=0~6）



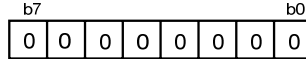
UARTi发送/接收模式寄存器（UiMR）

SMOD2~SMOD0 串行接口模式选择位
001b：时钟同步串行接口模式

CKDIR 内部/外部时钟选择位
置为“0” 0：内部时钟

IOPOL TXD、RXD输入/输出极性切换位
0：无反转

设置UARTi发送/接收控制寄存器0



UARTi发送/接收控制寄存器（UiC0）

CLK1, CLK0 UiBRG计数源选择位
00b：选择f1
置为“0”

CLK1、CLK0位不能设为“11b”。

TXEPT 发送寄存器空标志
0：发送寄存器中有数据（在发送中）
1：发送寄存器中无数据（发送结束）

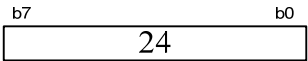
CRD CTS功能禁止位
0：允许CTS功能

NCH 数据输出选择位
0：TXDi/SDAi、SCLi引脚为推挽输出

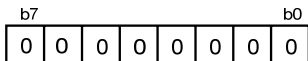
CKPOL CLK极性选择位
0：在传送时钟的下降沿发送数据，
在上升沿接收数据

UFORM 传送格式选择位
0：LSB先发送

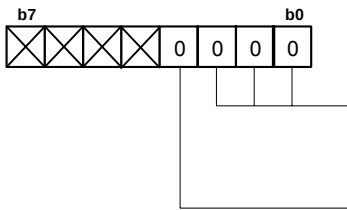
设置UARTi传送速率寄存器（i=0~6）



设置UARTi发送/接收控制寄存器1



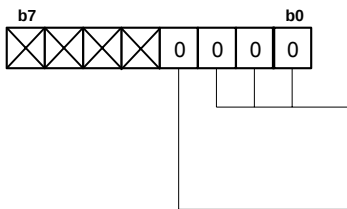
设置中断控制寄存器（i=0~6）



UARTi发送中断控制寄存器（SiTIC）

ILVL2~ILVL0 中断请求级选择位
000b：0级（禁止中断）

IR 中断请求位
0：无中断请求



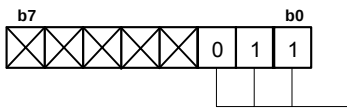
UARTi接收中断控制寄存器（SiRIC）

ILVL2~ILVL0 中断请求级选择位
000b：0级（禁止中断）

IR 中断请求位
0：无中断请求

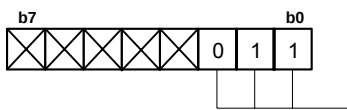
设置功能选择寄存器

这里以使用UART3时的设定为例。



端口P4_3功能选择寄存器（P4_3S）

PSEL2~PSEL0 端口P4_3输出功能选择位
011b：TXD3输出

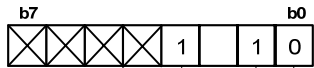


端口P4_1功能选择寄存器（P4_1S）

PSEL2~PSEL0 端口P4_1输出功能选择位
011b：CLK3输出

设置端口方向寄存器

这里以使用UART3时的设定为例。



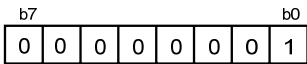
端口P4方向寄存器（PD4）

PD4_0 端口P4_0方向位
0：输入

PD4_1 端口P4_1方向位
1：输出

PD4_3 端口P4_3方向位
1：输出

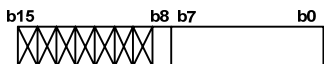
设置UARTi发送/接收控制寄存器1（i=0~6）



UARTi发送/接收控制寄存器1（UiC0）

TE 发送允许位
1：允许发送

写入发送数据



UARTi发送缓冲器控制寄存器（UiTB）

发送数据 设定发送数据

4. 参考文献

硬件手册

R32C/111 群硬件手册

（最新版本请从瑞萨电子网站上取得）

技术更新/技术信息

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C 编译器手册

R32C/100 系列用的 C 编译器包 V.1.02 C 编译器用户手册 Rev.1.00

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		页	要点
1.00	2011.12.16	—	初版发行

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同样，使用内部上电复位功能对产品进行复位时，从通电到达到复位产生的一定电压的期间，不能保证引脚的状态。

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