

RL78/G13

APPLICATION NOTE

R01AN0793EE0100 Rev1.00

Utilizing the Serial Array Unit (SAU) for LIN Communications (Using IAR Toolchain) Sep 12, 2011

Introduction

The purpose of this Application Note is to show the user how to add the associated RL78/G13 sample code to a new or existing IAR Embedded Workbench workspace; as well as give an explanation of what the sample code does.

The sample code demonstrates usage of the Serial Array Unit (SAU) implementing LIN communication. The program runs on the RL78/G13 RSK and can configure the RSKRL78/G13 as a LIN master or slave. This is through the inclusion or exclusion of the macro '#define MASTER_LIN' found in r_cg_userdefine.h header file. In master mode, the RL78/G13 transmits a LIN header frame which includes a wake-up signal and waits for the slave to respond with 3 data bytes which includes a checksum. In slave mode, the RL78/G13 waits for reception of a header frame from the master. Upon detection of the header frame, the slave checks if the address, 0x10, contained in the header frame matches its address before proceeding with transmitting the data bytes. By default, the sample code is configured to build and execute in the master mode.

Target Device

RL78/G13

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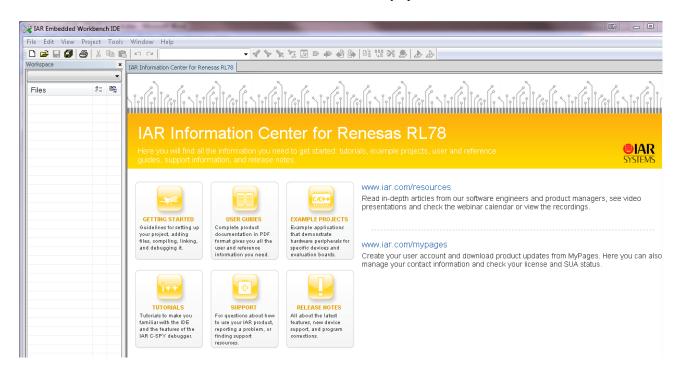
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1. Installation

This section assumes IAR Embedded Workbench (IAR EW) IDE is already installed on the user's personal computer (PC). Create a new folder and name it as 'RSKRL78G13_Workspace'. Copy the zipped file LIN.zip, available in the Application Note package downloaded from the website, to this folder. Extract the LIN.zip file to the RSKRL78G13_Workspace folder.

2. Creating the Project Workspace

Run IAR Embedded Workbench IDE by clicking the Windows Start button, select All Programs > IAR Systems > IAR Embedded Workbench for Renesas RL78 1.10. This will automatically open IAR Information Centre for RL78.





| 🔀 IAR Embedded Workbench IDE | | | | | | | | |
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| | New | | | | × | | File | CTRL+N |
| | Open | | | | • | | Workspace | |
| | Close | | | | | ٩ | | ٩ |

From the menu bar select File > New > Workspace as shown below:

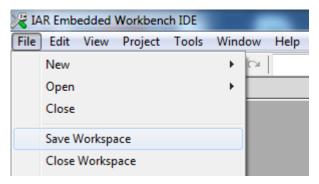
IAR EW will open an empty workspace.

To add the sample code select 'Project > Add Existing Project' from the menu bar as hown below:

| V IAR Embedded Workbench IDE | | | | | |
|------------------------------|---------------------------|--|--|--|--|
| File Edit View | Project Tools Window Help | | | | |
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| Workspace | Add Group | | | | |
| | Import File List | | | | |
| Files | Edit Configurations | | | | |
| | Remove | | | | |
| | Create New Project | | | | |
| | Add Existing Project | | | | |
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A new window titled 'Add Existing Project to Current Workspace' will appear. Navigate to the RSKRL78G13_Workspace folder and open the LIN folder. Locate the IAR EW project file LIN.ewp. Single-click the project file to select it. Click the < Open > button on the 'Add Existing Project to Current Workspace' window to add the project to the workspace.

Save the workspace by selecting File > Save Workspace



A 'Save Workspace As' window will appear. Open the 'RSKRL78G13_Workspace' folder from within the window's view and specify the 'File name'.



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| Save Workspace As | | X |
|--|----------------------|-------------|
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| Organize 🔻 New folder | :== :== | • 🔞 |
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| Ibraries ■ Documents → Music ■ Pictures ■ Videos ■ | | |
| Computer | | 4 |
| File name: RSKRL78G13_Workspace | | - |
| Save as type: Workspace Files (*.eww) | | • |
| Hide Folders | Save | ancel |

Save the workspace by selecting File > Save Workspace.

Note:

If a workspace already exists, open the workspace. Add the sample code project by selecting 'Project > Add Existing Project...' from the menu bar. Locate the LIN.ewp file and double click on the file's name to add to the workspace. Save the workspace.



3. Opening Sample Code and Source Files

Once the project has been added, the source code and all dependant files can be opened in the editor by expanding the folders in the Workspace window and double clicking the files in the folders.

| a IAR Embedded Workbench IDE | | _ 0 × |
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Workspace view

Each source file listed in Workspace window in IAR EW can be expanded to reveal its dependant files ; as well as the output files.

4. Source Code

The source code project is specifically written to run on the appropriate RSK. However this source code can be useful as an example even without the RSK.

The project was written using source files containing API functions generated using Applilet. The project will contain a C source file 'r_main.c'. This source file will include the C function main(). All source files and dependant files whose filenames are prefixed with 'r_' were generated using Applilet.

5. Code Execution

This sample code requires an external 5V regulated power supply.

1. When in master mode, configure the non-RSKRL78/G13 slave device to transmit 3 data bytes upon reception of a LIN header from the master.

2. Make the following connections between master RSKRL78/G13 board and slave device.

| RSKRL78/G13 MASTER | | SLAVE device |
|---|--------|--|
| a. LIN pin no.1 (VBAT) b. LIN pin no.2 (LIN) c. LIN pin no.3 (GROUND) | > > | LIN pin no.1 (VBAT) LIN pin no.2 (LIN) LIN pin no.3 (GROUND) |



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For master mode, ensure R83 and R84 are fitted. For slave mode, ensure R83 and R84 are removed.

3. Connect the 5V power supply.

4. Download the LIN Master application to the RSKRL78/G13 by pressing the 'Build and Download' button on the debug toolbar.

5. Press the 'Go' button on debug tool bar.

6. Reset the slave device if applicable.

7. In master operations, data received from the slave is displayed on the debug LCD. In slave mode, a 10-bit A/D conversion value is stored in two bytes and a checksum of the two is calculated and stored in another byte. The three bytes are then transmitted to the master. Different A/D values and checksum can be obtained by adjusting the potentiometer. The displayed value will be as follow

=H'xxxx where xxxx denotes the 10-bit A/D conversion value.

NOTE: Usually there is no need to connect power supply to the LIN slave device as it will draw power from RSKRL78/G13 master board via the LIN connector.

6. Website, Inquiries and Support

Renesas Electronics Website http://www.renesas.com/

Inquiries

http://www.renesas.com/inquiry

Support

http://www.renesas.com/rskrl78g13

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7. Revision Record

| | | Description | |
|------|--------------|-------------|----------------------|
| Rev. | Date | Page | Summary |
| 1.00 | Sep 12, 2011 | — | First edition issued |
| | | | |

General Precautions in the Handling of MPU/MCU Products

The following usage notes are applicable to all MPU/MCU products from Renesas. For detailed usage notes on the products covered by this manual, refer to the relevant sections of the manual. If the descriptions under General Precautions in the Handling of MPU/MCU Products and in the body of the manual differ from each other, the description in the body of the manual takes precedence.

- 1. Handling of Unused Pins
 - Handle unused pins in accord with the directions given under Handling of Unused Pins in the manual.
 - The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible. Unused pins should be handled as described under Handling of Unused Pins in the manual.
- 2. Processing at Power-on

The state of the product is undefined at the moment when power is supplied.

 The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the moment when power is supplied.

In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the moment when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the moment when power is supplied until the power reaches the level at which resetting has been specified.

- 3. Prohibition of Access to Reserved Addresses Access to reserved addresses is prohibited.
 - The reserved addresses are provided for the possible future expansion of functions. Do not access
 these addresses; the correct operation of LSI is not guaranteed if they are accessed.
- 4. Clock Signals

After applying a reset, only release the reset line after the operating clock signal has become stable. When switching the clock signal during program execution, wait until the target clock signal has stabilized.

- When the clock signal is generated with an external resonator (or from an external oscillator) during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Moreover, when switching to a clock signal produced with an external resonator (or by an external oscillator) while program execution is in progress, wait until the target clock signal is stable.
- 5. Differences between Products

Before changing from one product to another, i.e. to one with a different type number, confirm that the change will not lead to problems.

— The characteristics of MPU/MCU in the same group but having different type numbers may differ because of the differences in internal memory capacity and layout pattern. When changing to products of different type numbers, implement a system-evaluation test for each of the products.

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