

INTRODUCING HVPAK™

INNOVATE YOUR PRINTER SOLUTION WITH HVPAK MOTOR DRIVER!

HVPAK (up to 26.4 V and 3 A per OUT) takes its roots from the [GreenPAK family](#) featuring its configurability and free GUI-based [software](#) to design the circuit without any programming language needed. It combines mixed-signal logic and **high-voltage H-/Half-bridge** functionality in **a tiny 2 mm x 3 mm QFN package**.

The HVPAK incorporates advanced PWM macrocells, enabling the simultaneous control of multiple motors with varying PWM frequencies and duty cycles. Its compact size and low idle current consumption make it highly versatile, expanding its range of potential applications. This tiny and thermally-efficient IC provides an ideal platform for designing mixed-signal functions alongside its high-voltage capabilities.



Full-step / Micro-step mode with programmable steps and frequency



The exact number of steps can be counted



Integrated function: paper jam detection and paper detection

Datasheet
SLG47105

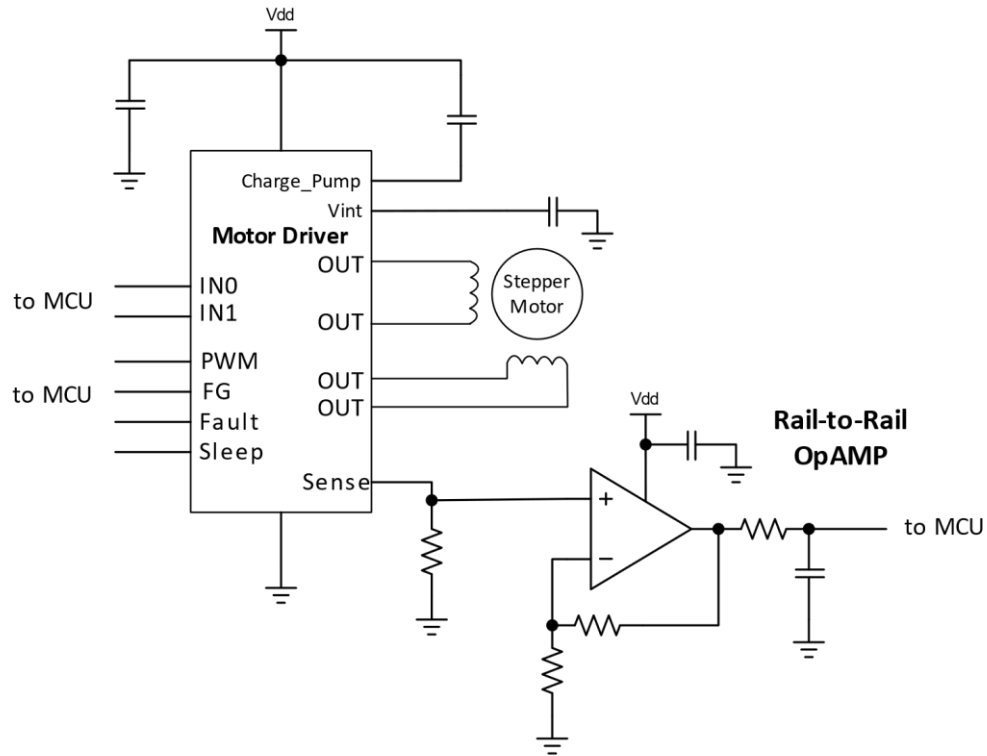
Datasheet
SLG47115

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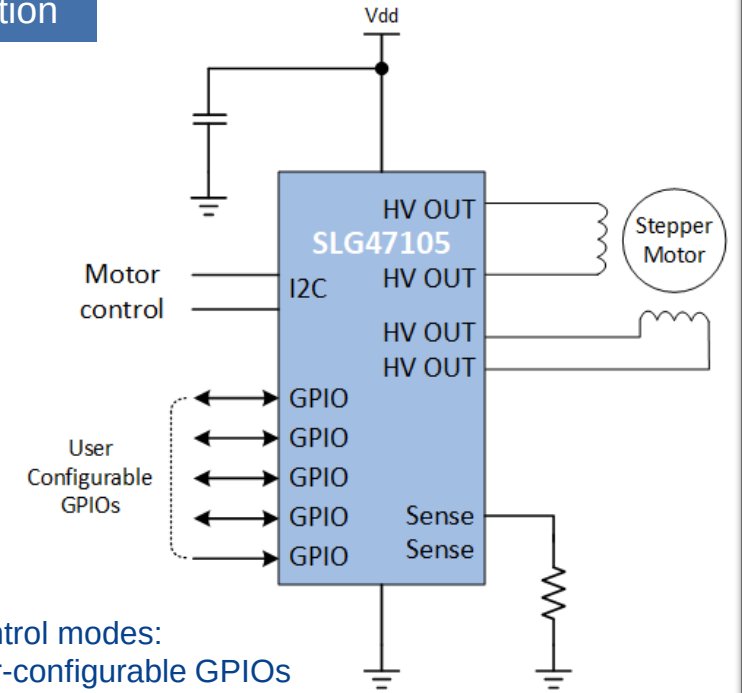
Application Note:
Stepper Motor Driver

HVPAK APPROACH: PRINTER DESIGN

Original Design



HVPAK Solution



Two motor control modes:
via I2C or user-configurable GPIOs

Design reduced by:

- 1 IC
- up to 6 passive components
- reduced current consumption by three times