

# CR02AM-8

400V - 0.2A - Thyristor

Low Power Use

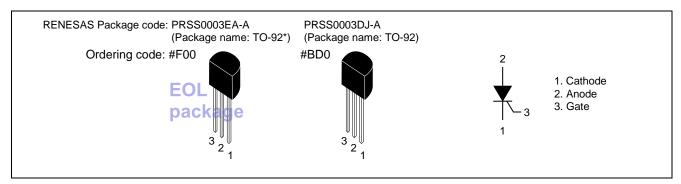
R07DS1423EJ0400 Rev.4.00 Feb. 22, 2022

## **Features**

- I<sub>T (AV)</sub>: 0.3 A
   V<sub>DRM</sub>: 400 V
   I<sub>GT</sub>: 100 μA
- RoHS Compliant

- Planar Passivation Type
- Halogen-free (PRSS0003DJ-A)
- Completely Pb-free (PRSS0003DJ-A)

## **Outline**



## **Application**

Solid state relay, leakage protector, timer, electric blanket, strobe flasher, and other general purpose applications.

## **Maximum Ratings**

Parameter	Symbol	Voltage class	Unit
		8	
Repetitive peak reverse voltage	V <sub>RRM</sub>	400	V
Non-repetitive peak reverse voltage	V <sub>RSM</sub>	500	V
Repetitive peak off-state voltage Note1	V <sub>DRM</sub>	400	V

Notes: 1. With gate to cathode resistance R<sub>GK</sub>=1  $k\Omega$ 

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I <sub>T (RMS)</sub>	0.47	Α	
Average on-state current	I <sub>T (AV)</sub>	0.3	А	Commercial frequency, sine half wave 180°conduction, Ta = 30°C
Surge on-state current	I <sub>TSM</sub>	10	А	60 Hz sinewave 1 full cycle, peak value, non-repetitive
I <sup>2</sup> t for fusing	l <sup>2</sup> t	0.4	A <sup>2</sup> s	Value corresponding to 1 cycle of half wave 60 Hz, surge on-state current
Peak gate power dissipation	P <sub>GM</sub>	0.1	W	
Average gate power dissipation	P <sub>G (AV)</sub>	0.01	W	
Peak gate forward voltage	$V_{FGM}$	6	V	
Peak gate reverse voltage	$V_{RGM}$	6	V	
Peak gate forward current	I <sub>FGM</sub>	0.1	Α	
Junction temperature	Tj	-40 to +125	°C	
Storage temperature	Tstg	-40 to +125	°C	

## **Electrical Characteristics**

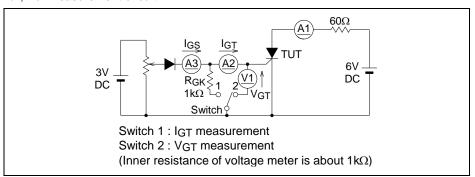
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak reverse current	I <sub>RRM</sub>	_	_	0.1	mA	Tj = 125°C, V <sub>RRM</sub> applied
Repetitive peak off-state current	I <sub>DRM</sub>	_	_	0.1	mA	$Tj = 125$ °C, $V_{DRM}$ applied $R_{GK}=1$ k $\Omega$
On-state voltage	V <sub>ТМ</sub>	_		1.6	٧	Tc = 25°C, I <sub>TM</sub> = 0.6 A, instantaneous value
Gate trigger voltage	V <sub>GT</sub>	_	_	0.8	<b>V</b>	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A Note3
Gate non-trigger voltage	V <sub>GD</sub>	0.2	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$ Rgk=1 k $\Omega$
Gate trigger current	lgт	1	_	100 Note2	μА	$Tj = 25$ °C, $V_D = 6$ V, $I_T = 0.1$ A $^{Note3}$
Holding current	Ін	_	_	3	mA	$Tj = 25$ °C, $V_D = 12$ V, $R_{GK}=1$ k $\Omega$
Thermal resistance	R <sub>th (j-a)</sub>	_	_	180	°C/W	Junction to ambient

Notes: 2. If special values of I<sub>GT</sub> are required, choose item D or E from those listed in the table below if possible.

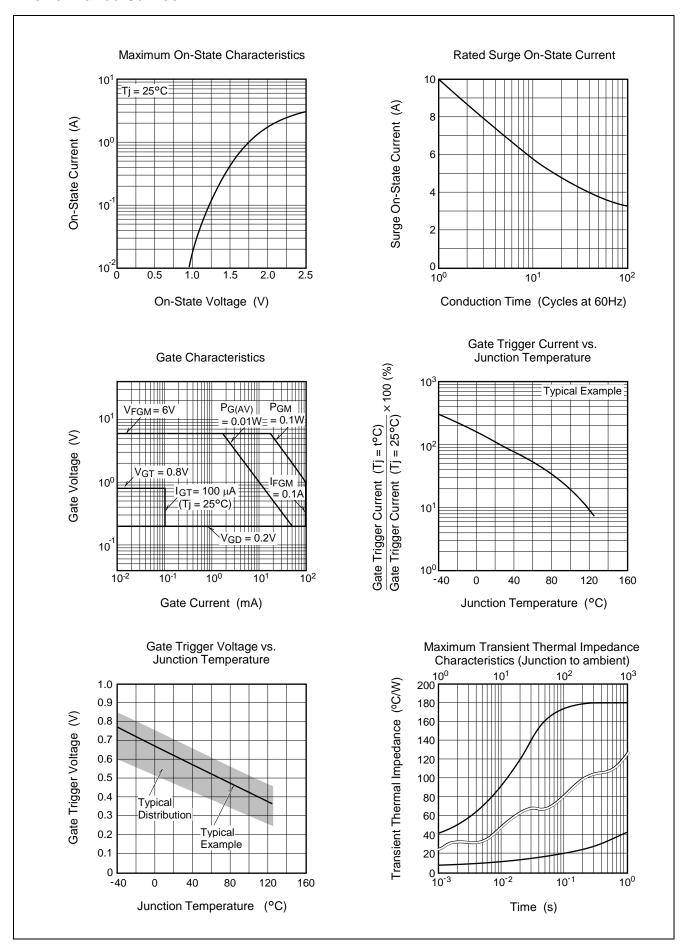
	Item	Α	В	С	D	E
Ī	I <sub>GT</sub> (μ <b>A</b> )	1 to 30	20 to 50	40 to 100	1 to 50	20 to 100

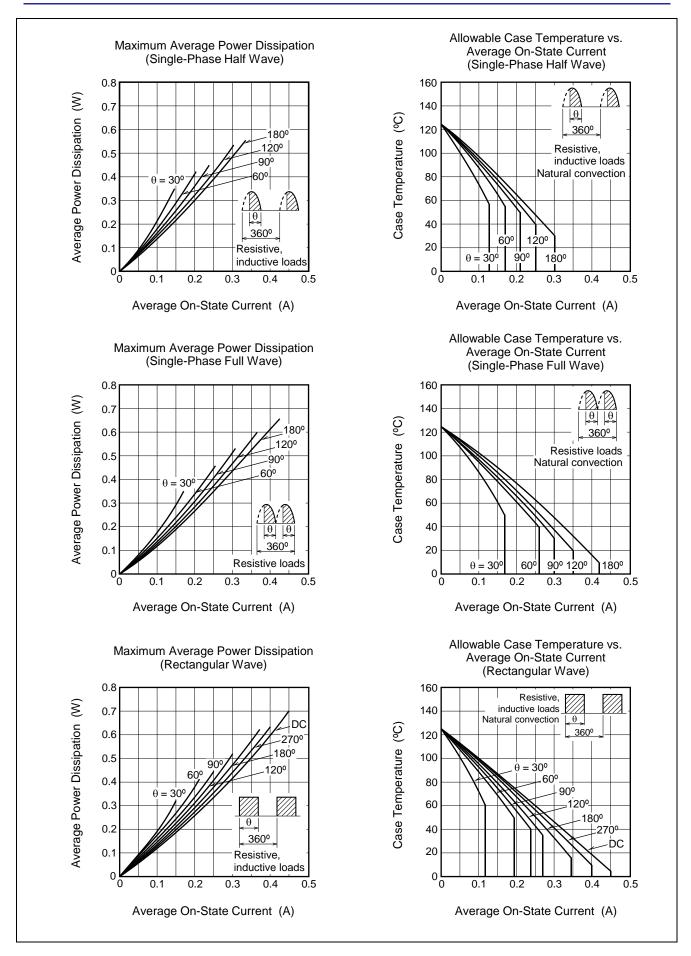
The above values do not include the current flowing through the 1  $k\Omega$  resistance between the gate and cathode.

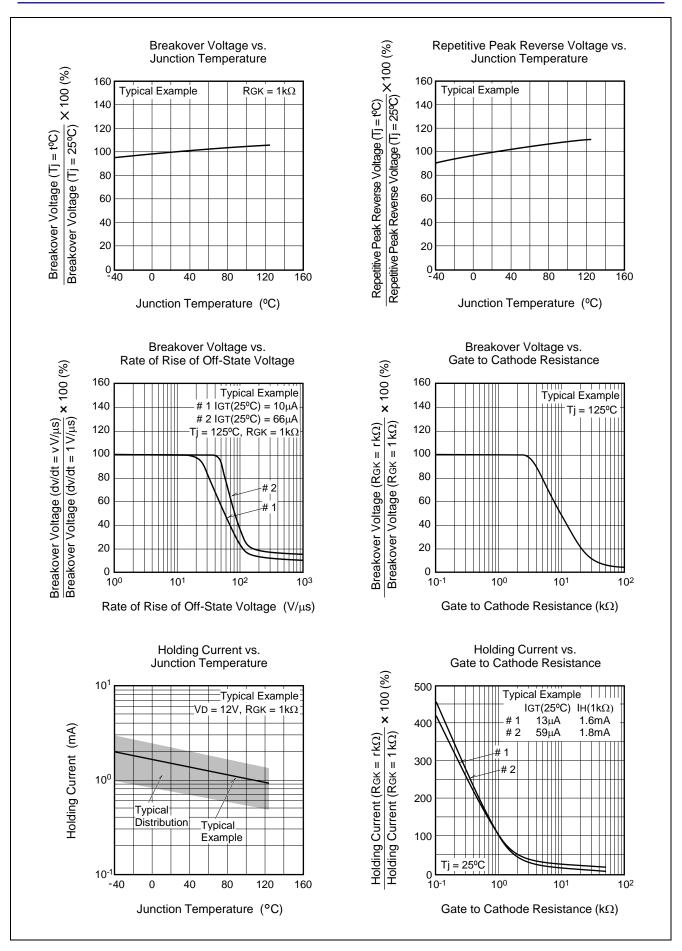
### 3. Igt, Vgt measurement circuit.

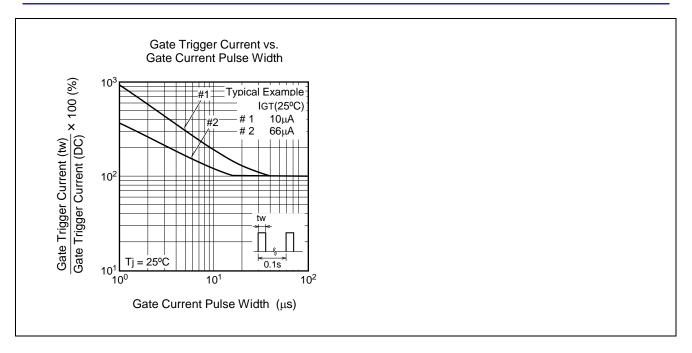


## **Performance Curves**



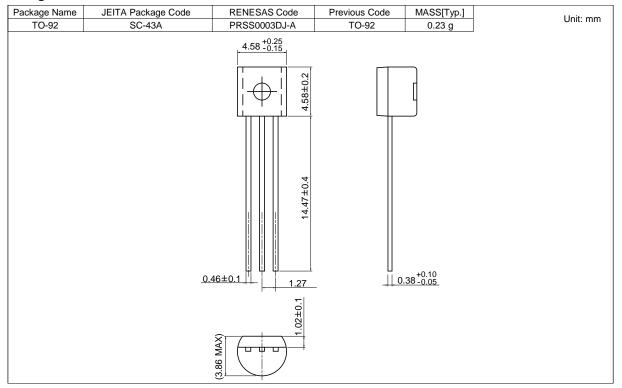




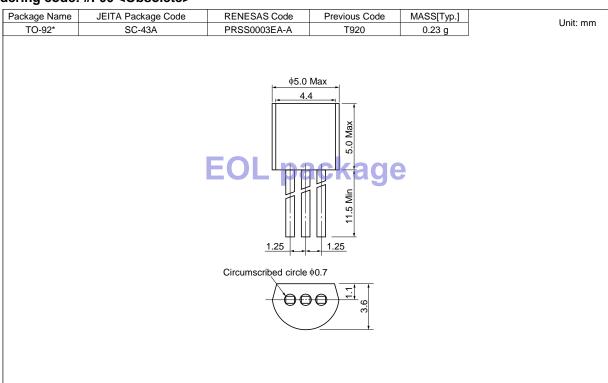


# **Package Dimensions**

## Ordering code: #BD0 <Active>



## Ordering code: #F00 <Obsolete>



# **Ordering Information**

Orderable Part Number	Package	Packing Note4	Quantity	Remark	Status
CR02AM-8#BD0	TO-92	Plastic Bag	1000 pcs.	Straight type	Active
CR02AM-8-□#BD0	TO-92	Plastic Bag	1000 pcs.	Straight type, □:I <sub>GT</sub> item	
CR02AM-8-A6#BD0	TO-92	Plastic Bag	1000 pcs.	A6 Lead form	
CR02AM-8-□A6#BD0	TO-92	Plastic Bag	1000 pcs.	A6 Lead form, □:I <sub>GT</sub> item	
CR02AM-8-TB#BD0	TO-92	Adhesive Tape	2000 pcs.	A8 Lead form	
CR02AM-8-□TB#BD0	TO-92	Adhesive Tape	2000 pcs.	A8 Lead form, □:I <sub>GT</sub> item	
CR02AM-8#F00	TO-92*	Plastic Bag	500 pcs.	Straight type	Obsolete
CR02AM-8-□#F00	TO-92*	Plastic Bag	500 pcs.	Straight type, □:I <sub>GT</sub> item	
CR02AM-8-A6#F00	TO-92*	Plastic Bag	500 pcs.	A6 Lead form	
CR02AM-8-□A6#F00	TO-92*	Plastic Bag	500 pcs.	A6 Lead form, □:I <sub>GT</sub> item	
CR02AM-8-TB#F00 TO-92*		Adhesive Tape	2000 pcs.	A8 Lead form	
CR02AM-8-□TB#F00	TO-92*	Adhesive Tape	2000 pcs.	A8 Lead form, □:I <sub>GT</sub> item	

Note: 4. Please confirm the specification about the shipping in detail.

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