

HAT2198R

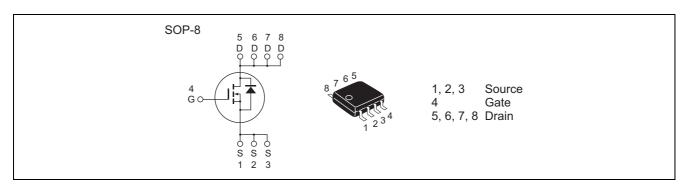
Silicon N Channel Power MOS FET Power Switching

REJ03G0062-020F Rev.2.0F Ù^].€8.20FÎ

Features

- High speed switching
- Capable of 4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance $R_{DS(on)} = 7.2 \ m\Omega \ typ. \ (at \ V_{GS} = 10 \ V)$

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	14	Α
Drain peak current	I _{D(pulse)} Note1	112	Α
Body-drain diode reverse drain current	I _{DR}	14	Α
Avalanche current	I _{AP} Note 2	14	Α
Avalanche energy	E _{AR} Note 2	19.6	mJ
Channel dissipation	Pch Note3	2.5	W
Channel to ambient thermal impedance	θch-a Note3	50	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

- 2. Value at Tch = 25°C, Rg \geq 50 Ω
- 3. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10s

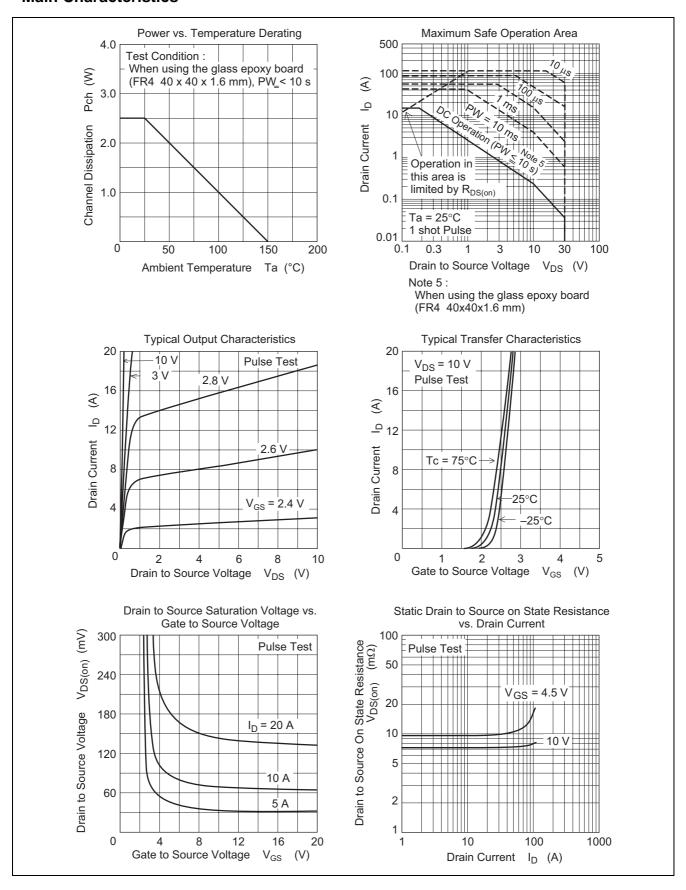
Electrical Characteristics

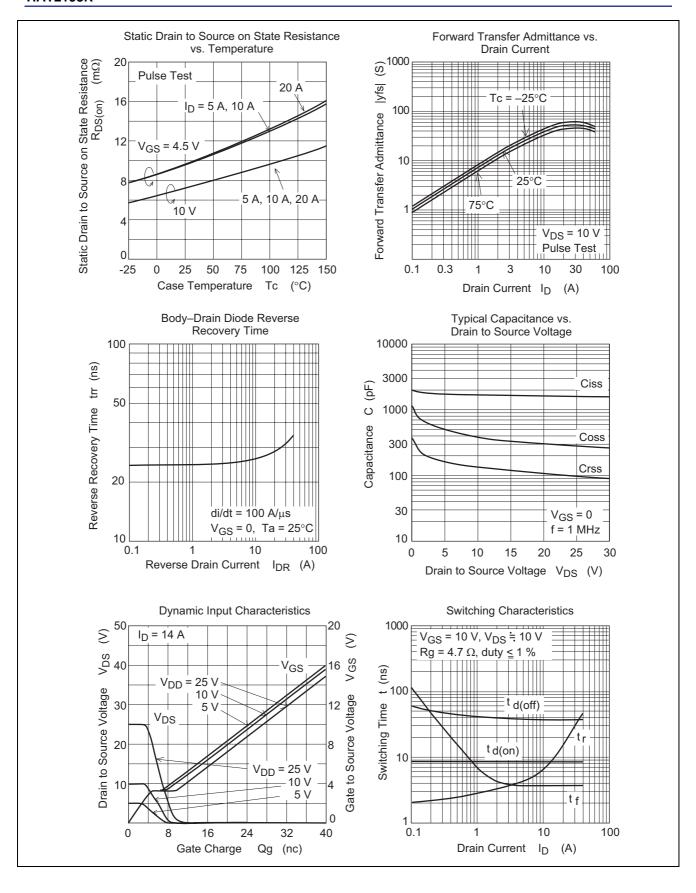
 $(Ta = 25^{\circ}C)$

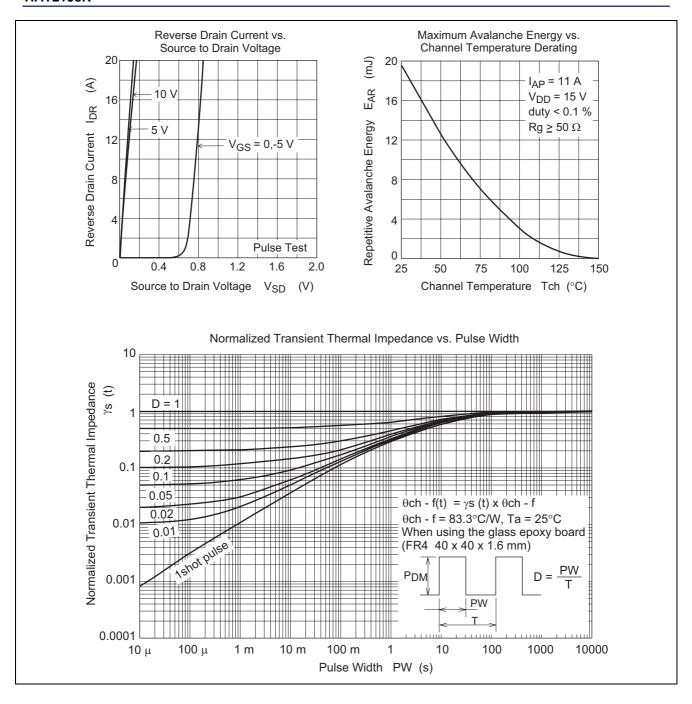
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	30	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I _{GSS}	_	_	± 0.1	μΑ	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	_	_	1	μΑ	$V_{DS} = 30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	_	2.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R _{DS(on)}	_	7.2	9.0	mΩ	$I_D = 7 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance	R _{DS(on)}	_	9.6	14.0	mΩ	$I_D = 7 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note4}}$
Forward transfer admittance	y _{fs}	18	30	_	S	$I_D = 7 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$
Input capacitance	Ciss	_	1650	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	390	_	pF	V _{GS} = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	135	_	pF	
Gate Resistance	Rg	_	0.55	_	Ω	
Total gate charge	Qg	_	11	_	nC	$V_{DD} = 10 \text{ V}$
Gate to source charge	Qgs	_	4.7	_	nC	V _{GS} = 4.5 V I _D = 14 A
Gate to drain charge	Qgd	_	2.5	_	nC	
Turn-on delay time	t _{d(on)}	_	8.5	_	ns	V _{GS} = 10 V, I _D = 7 A
Rise time	t _r	_	5	_	ns	$V_{DD} \cong 10 \text{ V}$ $R_L = 1.42 \Omega$ $Rg = 4.7 \Omega$
Turn-off delay time	t _{d(off)}	_	38	_	ns	
Fall time	t _f	_	3.8	_	ns	
Body-drain diode forward voltage	V_{DF}	_	0.80	1.04	V	IF = 14 A, V _{GS} = 0 Note4
Body-drain diode reverse recovery	t _{rr}	_	28	_	ns	IF = 14 A, V _{GS} = 0
time						$diF/dt = 100 A/\mu s$

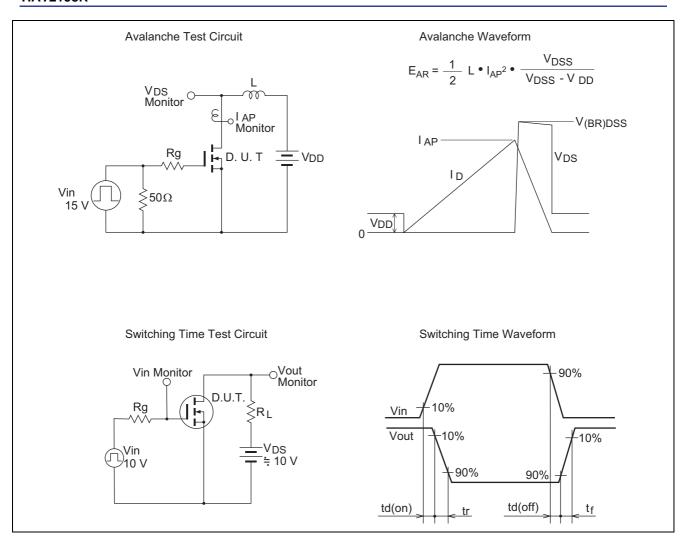
Notes: 4. Pulse test

Main Characteristics

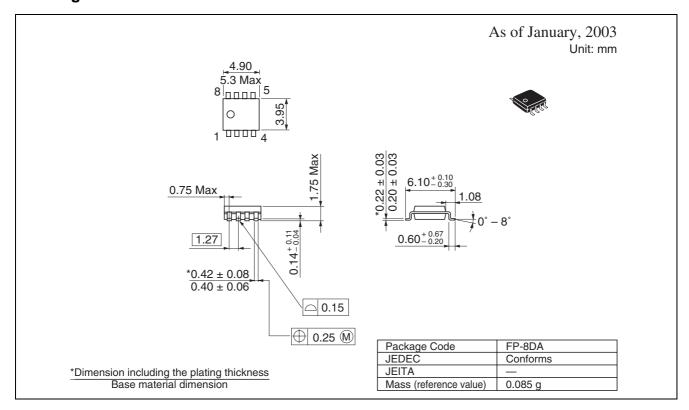








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT2198R-EL-E	2500 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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