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April 1st, 2010 Renesas Electronics Corporation

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RENESAS HD74ALVC2G126

Dual Bus Buffer with 3-state Output

REJ03D0172-0200Z (Previous ADE-205-622A (Z)) Rev.2.00 Dec.18.2003

Description

The HD74ALVC2G126 has dual bus buffer with 3-state output in an 8 pin package. Output is disabled when the associated output enable (OE) input is low. To ensure the high impedance state during power up or power down, OE should be connected to GND through a pull-down resistor; the minimum value of the resistor is determined by the current souring capability of the driver. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Supply voltage range : 1.2 to 3.6 V Operating temperature range: -40 to +85°C
- All inputs V_{IH} (Max.) = 3.6 V (@V_{CC} = 0 V to 3.6 V) All outputs V_O (Max.) = 3.6 V (@V_{CC} = 0 V)
- Output current $\pm 2 \text{ mA} (@V_{CC} = 1.2 \text{ V})$

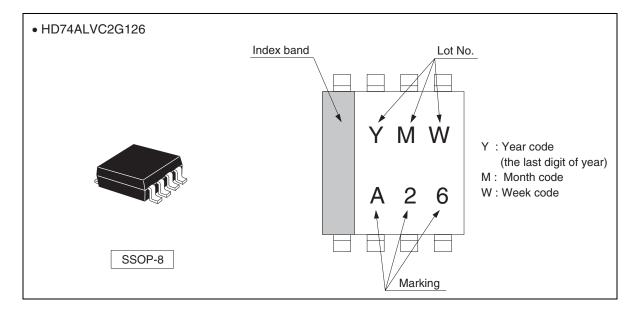
 $\pm 4 \text{ mA} (@V_{CC} = 1.2 \text{ V})$ $\pm 4 \text{ mA} (@V_{CC} = 1.4 \text{ V to } 1.6 \text{ V})$ $\pm 6 \text{ mA} (@V_{CC} = 1.65 \text{ V to } 1.95 \text{ V})$ $\pm 18 \text{ mA} (@V_{CC} = 2.3 \text{ V to } 2.7 \text{ V})$ $\pm 24 \text{ mA} (@V_{CC} = 3.0 \text{ V to } 3.6 \text{ V})$

Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74ALVC2G126USE	SSOP-8 pin	TTP-8DBV	US	E (3,000 pcs/reel)



Outline and Article Indication



Function Table

Inputs

L	
П	H
L	L
Х	Z
	L X

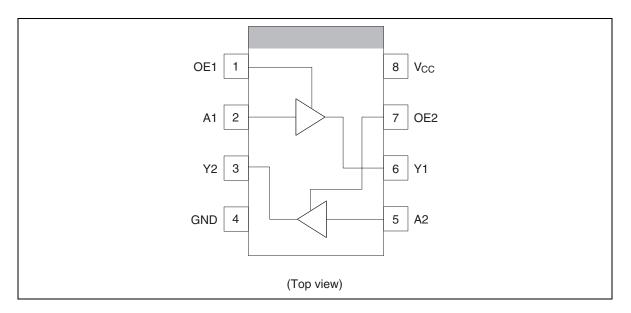
H: High level

L: Low level

X: Immaterial

Z: High impedance

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Conditions	
Supply voltage range	V _{CC}	-0.5 to 4.6	V		
Input voltage range *1	VI	-0.5 to 4.6	V		
Output voltage range *1, 2	Vo	–0.5 to V _{CC} +0.5	V	Output : H or L or Z	
		-0.5 to 4.6		V _{CC} : OFF	
Input clamp current	I _{IK}	-50	mA	V ₁ < 0	
Output clamp current	Ι _{ΟΚ}	±50	mA	$V_{\rm O}$ < 0 or $V_{\rm O}$ > $V_{\rm CC}$	
Continuous output current	lo	±50	mA	$V_{O} = 0$ to V_{CC}	
Continuous current through V _{CC} or GND	I_{CC} or I_{GND}	±100	mA		
Maximum power dissipation at Ta = 25°C (in still air) *3	P _T	200	mW		
Storage temperature	Tstg	-65 to 150	°C		

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

2. This value is limited to 4.6 V maximum.

3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Item	Symbol	Min	Max	Unit	Conditions
Supply voltage range	V _{CC}	1.2	3.6	V	
Input voltage range	VI	0	3.6	V	
Output voltage range	Vo	0	V _{cc}	V	
Output current	I _{OH}	—	-2	mA	V _{CC} = 1.2 V
			-4		V _{CC} = 1.4 V
			-6		V _{CC} = 1.65 V
			-18		V _{CC} = 2.3 V
			-24		V _{CC} = 3.0 V
	I _{OL}		2		V _{CC} = 1.2 V
			4		V _{CC} = 1.4 V
			6		V _{CC} = 1.65 V
			18		V _{CC} = 2.3 V
			24		V _{CC} = 3.0 V
Input transition rise or fall rate	Δt / Δv	0	20	ns / V	V_{CC} = 1.2 to 2.7 V
		0	10		V _{CC} = 3.3±0.3 V
Operating free-air temperature	Та	-40	85	°C	

Recommended Operating Conditions

Note: Unused or floating inputs must be held high or low.



Electrical Characteristics

$(Ta = -40 \text{ to } 85^{\circ}C)$

Item	Symbol	V_{cc} (V) *	Min	Тур	Max	Unit	Test conditions
Input voltage	VIH	1.2	V _{CC} ×0.75		_	V	
		1.4 to 1.6	V _{CC} ×0.7		—	-	
		1.65 to 1.95	V _{CC} ×0.7		—	-	
		2.3 to 2.7	1.7	—	_	-	
		3.0 to 3.6	2.0		—	-	
	V _{IL}	1.2			V _{CC} ×0.25	-	
		1.4 to 1.6	_		V _{CC} ×0.3	-	
		1.65 to 1.95			V _{CC} ×0.3	-	
		2.3 to 2.7			0.7	-	
		3.0 to 3.6	_	—	0.8	-	
Output voltage	V _{OH}	Min to Max	V _{CC} -0.2		—	V	$I_{OH} = -100 \ \mu A$
		1.2	0.9		—	-	$I_{OH} = -2 \text{ mA}$
		1.4	1.1	_	_	-	$I_{OH} = -4 \text{ mA}$
		1.65	1.2	_	_	-	$I_{OH} = -6 \text{ mA}$
		2.3	1.7	—	_	-	I _{OH} = -18 mA
		3.0	2.2	—	_	-	I _{OH} = -24 mA
	V _{OL}	Min to Max	_	—	0.2	-	I _{OL} = 100 μA
		1.2			0.3	-	$I_{OL} = 2 \text{ mA}$
		1.4		_	0.3	-	$I_{OL} = 4 \text{ mA}$
		1.65	_	—	0.3	-	$I_{OL} = 6 \text{ mA}$
		2.3	_		0.55	-	I _{OL} = 18 mA
		3.0	_	—	0.55	-	I _{OL} = 24 mA
Input current	I _{IN}	3.6	_	—	±5	μΑ	V_{IN} = 3.6 V or GND
Off state output current	I _{OZ}	3.6	_	_	±5	μA	$V_{O} = V_{CC}$ or GND
Quiescent supply current	I _{CC}	3.6	_		10	μA	$V_{IN} = V_{CC}$ or GND, $I_{O} = 0$
Output leakage current	I _{OFF}	0	—		5	μA	V _{IN} or V _O = 0 to 3.6 V
Input capacitance	CIN	3.3	_	4.5	_	pF	$V_{IN} = V_{CC} \text{ or } GND$

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.



Switching Characteristics

 $(Ta = -40 \text{ to } 85^{\circ}C)$

 $V_{CC} = 1.2 V$

Item	Symbol	Min	Тур	Мах	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}		5.5	_	ns	C _L = 15 pF	A	Y
Enable time	t _{ZH} t _{ZL}	_	6.5	_	ns	C _L = 15 pF	OE	Y
Disable time	t _{HZ} t _{LZ}	_	4.5	—	ns	C _L = 15 pF	OE	Y

 $V_{CC} = 1.5 \pm 0.1 \text{ V}$

Item	Symbol	Min	Тур	Max	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	2.0	—	7.0	ns	C _L = 15 pF	A	Y
Enable time	t _{ZH} t _{ZL}	2.0	—	7.0	ns	C _L = 15 pF	OE	Y
Disable time	t _{HZ} t _{LZ}	2.0	—	7.0	ns	C _L = 15 pF	OE	Y

 $V_{CC} = 1.8 \pm 0.15 \text{ V}$

Item	Symbol	Min	Тур	Мах	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	1.5	—	5.0	ns	C _L = 30 pF	A	Y
Enable time	t _{ZH} t _{ZL}	1.5	_	5.0	ns	C _L = 30 pF	OE	Y
Disable time	t _{HZ} t _{LZ}	1.5	_	5.0	ns	C _L = 30 pF	OE	Y

Switching Characteristics (cont)

 $V_{CC} = 2.5 \pm 0.2 \text{ V}$

Item	Symbol	Min	Тур	Мах	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	1.0	_	4.0	ns	C _L = 30 pF	A	Y
Enable time	t _{ZH} t _{ZL}	1.0	—	4.0	ns	C _L = 30 pF	OE	Y
Disable time	t _{HZ} t _{LZ}	1.0	—	4.0	ns	C _L = 30 pF	OE	Y

 $V_{CC} = 3.3 \pm 0.3 V$

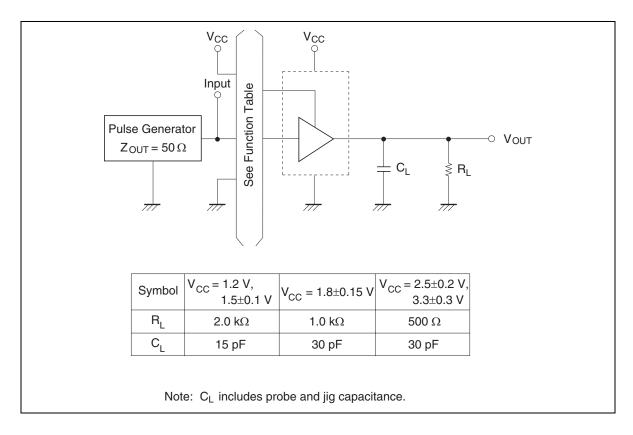
ltem	Symbol	Min	Тур	Мах	Unit	Test conditions	FROM (Input)	TO (Output)
Propagation delay time	t _{PLH} t _{PHL}	1.0	—	3.0	ns	C _L = 30 pF	A	Y
Enable time	t _{ZH} t _{ZL}	1.0	—	3.0	ns	C _L = 30 pF	OE	Y
Disable time	t _{HZ} t _{LZ}	1.0	—	3.0	ns	C _L = 30 pF	OE	Y

Operating Characteristics

 $(Ta = 25^{\circ}C)$

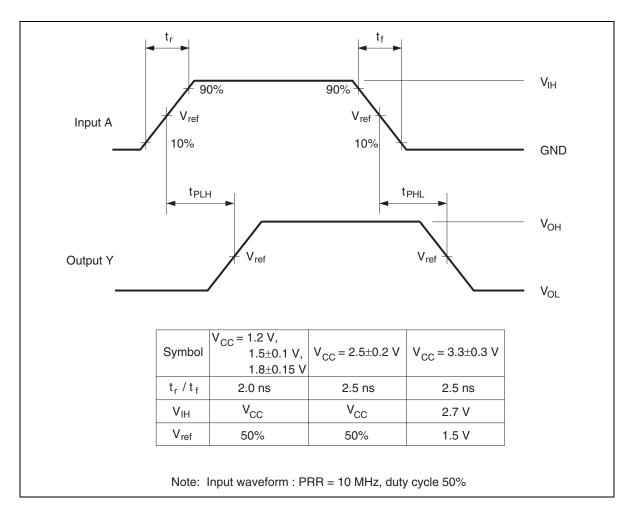
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Unit	Test conditions
Power dissipation	CPD	1.5	_	10.5	_	pF	f = 10 MHz
capacitance		1.8		10.5		_	
		2.5	_	11.0	_	_	
		3.3	_	13.0			

Test Circuit - 1



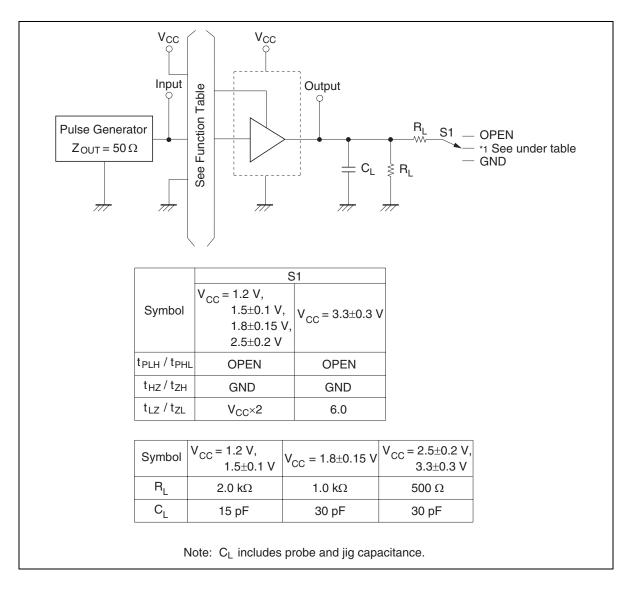


Waveforms - 1



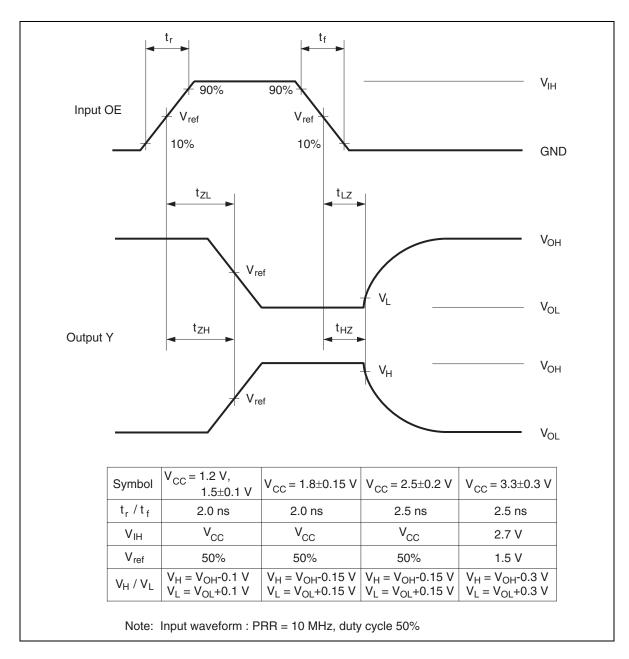


Test Circuit - 2

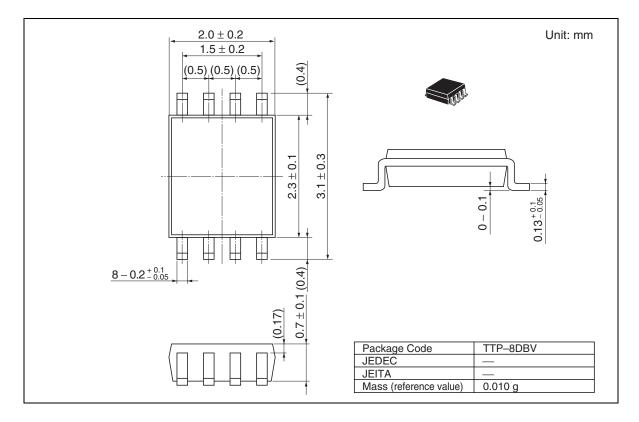




Waveforms - 2



Package Dimensions





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