Target Software: Renesas Flash Driver RL78 Type 02 (Here after “RFD RL78 Type 02”) 
EEPROM Emulation Software RL78 Type 02 (Here after “EES RL78 Type 02”)  
EES RL78 Type 02 will be released after April 2023.

- This list is a compilation of the target MCUs of the RL78 family RFD RL78 Type 02 and EES RL78 Type 02 for automotive MCUs. 
RFD RL78 Type 02 and EES RL78 Type 02 can be used with the target MCUs in this list. 
The items [R-x] ([R-1], [R-2], etc.) in the list are items used in RFD RL78 Type 02 and EES RL78 Type 02. 
For details, refer to the user's manual of the target software. 
- RL78 non-automotive MCUs cannot use RFD RL78 Type 02 and EES RL78 Type 02. 
For the RL78 non-automotive MCUs, see “Renesas Flash Driver and EEPROM Emulation Software Target MCU List for RL78 - General-purpose (To be released after May 2023)” (R20UT5228). 
- RL78/x1x (RL78/F1x, G1x, etc.) products cannot use RFD RL78 Type 0x and EES RL78 Type 0x. 
For the target flash libraries of RL78/x1x, see the self RAM List (R20UT2944). 

### Target MCUs

<table>
<thead>
<tr>
<th>MCU Group</th>
<th>Code Flash memory</th>
<th>User RAM</th>
<th>Data Flash memory</th>
<th>[R-1]</th>
<th>[R-2]</th>
<th>[R-3]</th>
<th>[R-4]</th>
<th>[R-5]</th>
<th>[R-6]</th>
<th>[R-7]</th>
<th>[R-8]</th>
<th>Target MCU name</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Size (bytes)</td>
<td>Start/End Address</td>
<td>Size (bytes)</td>
<td>Start/End Address</td>
<td>RAM Start Address</td>
<td>ROM End Address</td>
<td>OCD_ROM</td>
<td>Trace_RAM</td>
<td>Hot plug-in</td>
<td>END_BLOCK</td>
<td></td>
<td></td>
</tr>
<tr>
<td>RL78/F23</td>
<td>128K</td>
<td>0x00000 - 0x1FFFF</td>
<td>12K</td>
<td>0x0F000 - 0x0FFF0</td>
<td>0x0F000 - 0x0F2FF</td>
<td>0x0F000 - 0x0F2FF</td>
<td>0x0FFFF</td>
<td>0x0F2FF</td>
<td>0x1FE00</td>
<td>0xFD300</td>
<td>0xFD500</td>
<td>R7F123FxG (x = B, G, L, M)</td>
</tr>
<tr>
<td>RL78/F24</td>
<td>256K</td>
<td>0x00000 - 0x3FFFF</td>
<td>24K</td>
<td>0x0F000 - 0x0FFF</td>
<td>0x0F000 - 0x0F4FF</td>
<td>0x0F000 - 0x0F4FF</td>
<td>0x0FFFF</td>
<td>0x0F4FF</td>
<td>0x3FE00</td>
<td>0xFA300</td>
<td>0xFA500</td>
<td>R7F124FxJ (x = B, G, L, M, P)</td>
</tr>
</tbody>
</table>

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