

# **Product Change Notice (PCN)**

Subject: Product Improvement - Design Change for the listed Renesas ISL78600ANZ\* and

ISL78610ANZ\* Products
Publication Date: 8/6/2019
Effective Date: 2/6/2020

#### **Revision Description:**

Initial Release

## **Description of Change:**

This notice is to advise our customers of a design revision for the Renesas Electronics America products listed below. The change is to the analog front end (AFE) which will allow for simultaneous input sampling.

Products impacted by the change are:

ISL78600ANZ ISL78600ANZ-T ISL78610ANZ ISL78610ANZ-T

#### Reason for Change:

The change to the AFE will allow for sampling of all cell inputs simultaneously, therefore reducing timing related errors and improve the manufacturability of the product over all published temperature ranges.

### Impact on fit, form, function, quality & reliability:

The change will have no other impact on the form, fit, function, quality, reliability and environmental compliance of the devices.

#### **Product Identification:**

There will be no change in the external marking of the packaged parts. Product affected by this change is identifiable via Renesas internal traceability system.

Qualification status: Complete, see attached

Sample availability: 8/6/2019

Device material declaration: Available upon request

Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.

For additional information regarding this notice, please contact your regional change coordinator (below)							
Americas: PCN-US@RENESAS.COM	Europe: PCN-EU@RENESAS.COM	Japan: PCN-JP@RENESAS.COM	Asia Pac: PCN-APAC@RENESAS.COM				



# Appendix A - Qualification Results

Qualification Results							
Stress	Test Method	Product	Sample Size	# of Lots	Result		
High Temperature Operating Life (HTOL) 125°C, 1000 hours	JESD22-A108	ISL78600ANZ	80	1	Pass		
Electrical Characterization <sup>1</sup>	-	ISL78600ANZ	-	-	Pass		
Human Body Model (HBM)	AEC Q100-002	ISL78600ANZ	3	1	Level: 2		
Charged Device Model (CDM)	AEC Q100-011	ISL78600ANZ	3	1	Level: C6		
Latch-up (LU)	AEC Q100-004	ISL78600ANZ	6	1	Pass		

I. Electrical characterization over temperature was completed in accordance with REA internal procedures. Bench validation was also performed to verify the product meets published specifications.