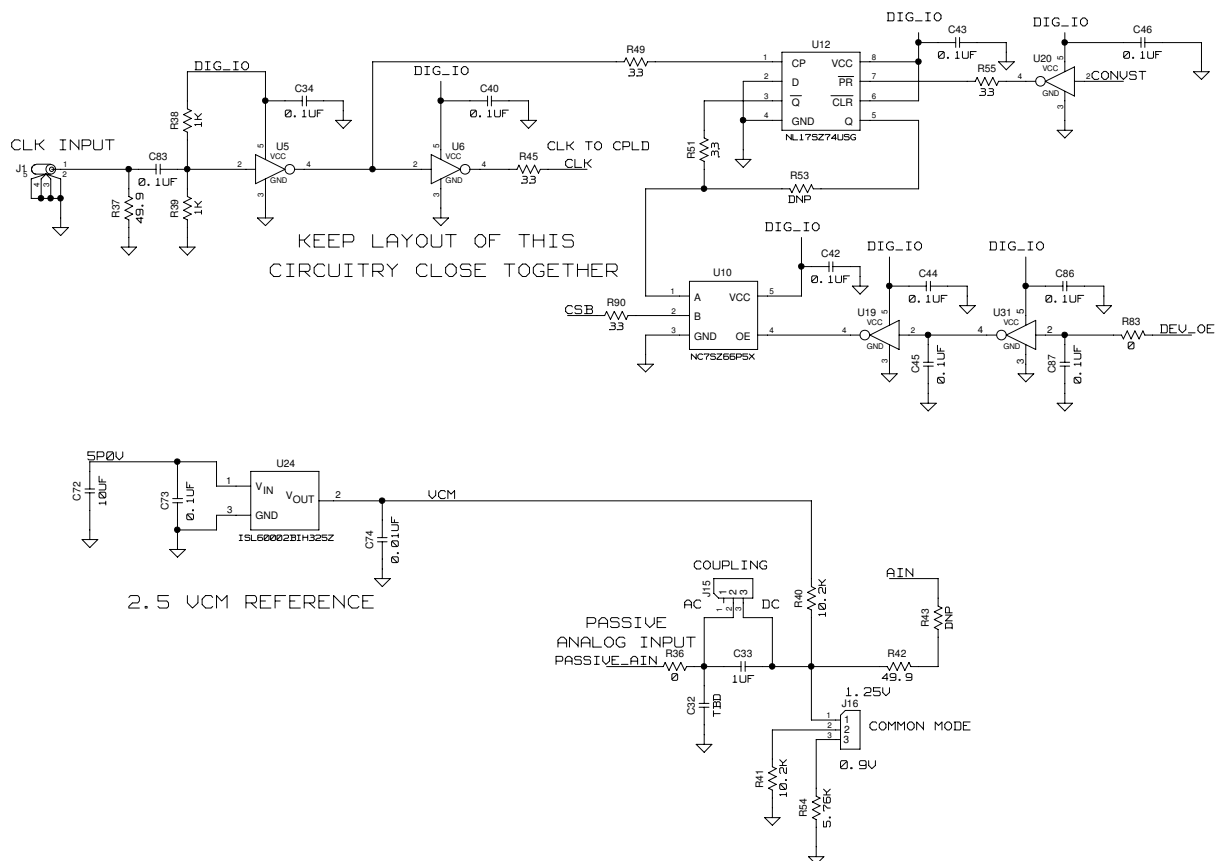


HRDWR ID ISL73148SEHEV1Z	REV. C	SHEET 2 OF 7
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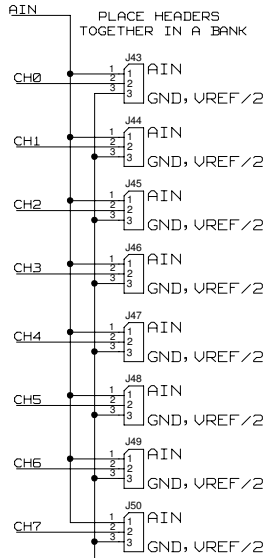
PASSIVE AIN, CLOCK, AND TEST POINT CIRCUITRY



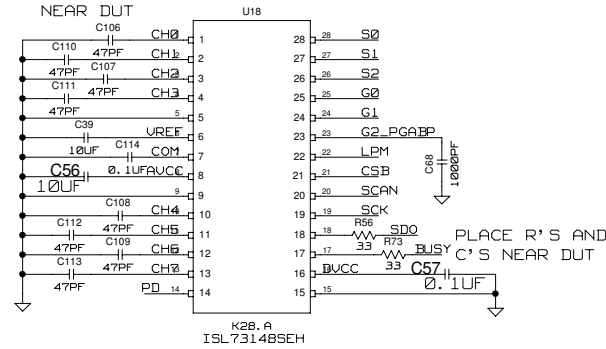
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ADC AND FORMAT/MODE SELECTION CIRCUITRY

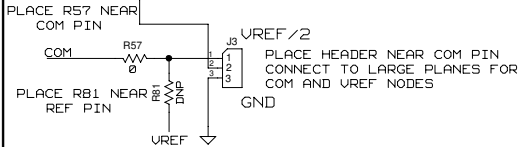
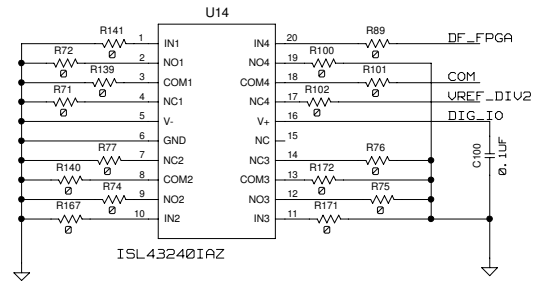
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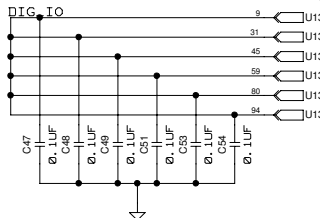
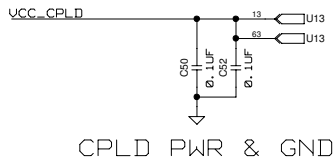
PLACE ALL C'S NEAR DUT



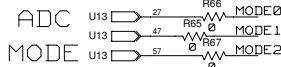
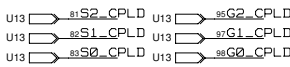
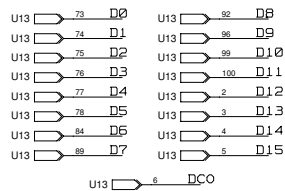
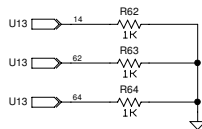
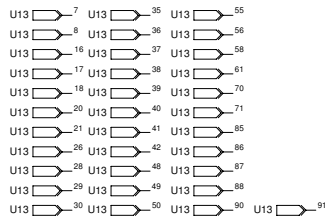
DATA FORMAT SELECT



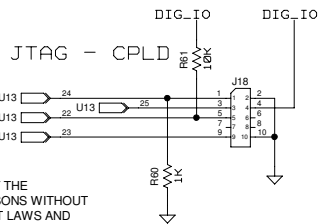
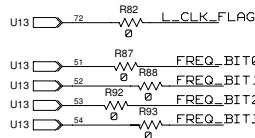
CPLD AND BOARD CONNECTORS CIRCUITRY



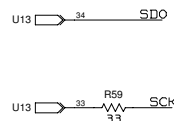
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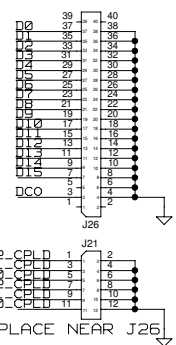
FREQ CTL PINS



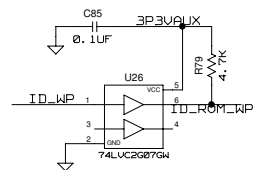
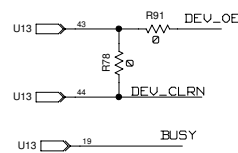
SERIAL DATA PINS



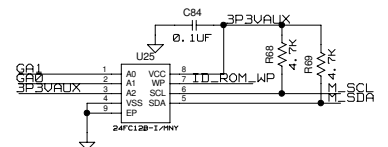
VERTICAL MOUNT PARALLEL CONNECTOR



GPIO PINS



ID EEPROM



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