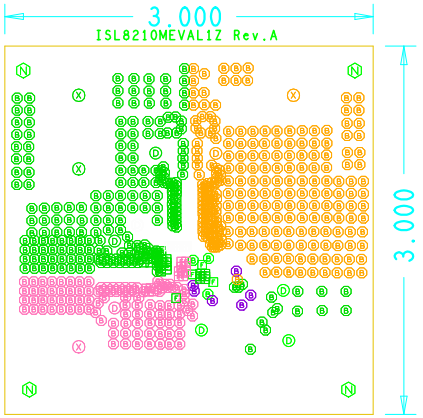
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

PHYSICAL BOARD DIMENSIONS & LAYER STRUCTURE

.062" +/- 10%

SILK TOP	silkt.art
MASK TOP	smaskt.art
COMPONENT	layer1.art
LAYER 2	layer2.art
LAYER 3	layer3.art
LAYER 4	layer4.art
LAYER 5	layer5.art
SOLDER	layer6.art
MASK BOTTOM	smaskb.art
SILK BOTTOM	silkb.art

DRILL CHART: TOP to BOTTOM ALL UNITS ARE IN MILS			
FIGURE	SIZE	PLATED	QTY
Ⓑ	11.0	PLATED	554
Ⓕ	12.0	PLATED	46
Ⓓ	40.0	PLATED	8
Ⓧ	155.0	PLATED	4
Ⓝ	128.0	NON-PLATED	4



DRILL
RENESAS CORPORATION
10-17-2018

NOTES:

1. THIS BOARD IS RoHS COMPLIANT.
2. PRINTED WIRING BOARD DESIGN AND ACCEPTANCE CRITERIA SHALL BE IAW WITH THE REQUIREMENTS OF IPC-D-275 AND IPC-A-600.
3. MATERIAL: FR4 (RoHS COMPLIANT), 2 OZ COPPER.
4. APPLY SOLDER MASK, BOTH SIDES (VIAS ARE TENTED), IAW IPC-SM-840. CLASS 2 (LPI) (BLUE MASK).
5. IMMERSION GOLD FINISH.
6. UNLESS OTHERWISE SPECIFIED ALL HOLE DIAMETERS ARE AFTER PLATING.
7. APPLY SILKSCREEN USING WHITE NON-CONDUCTIVE EPOXY BASED INK.
8. PWB MUST BE 100% ELECTRICALLY TESTED FOR SHORTS AND CONTINUITY. USE NETLIST PROVIDED ISL8210MEVAL1ZA_IPC.IPC IAW IPC-D-356.
9. MARK DATE CODE AND MANUFACTURES IDENIFICATION ON SOLDER SIDE PER IPC-6011 AND IPC-6012.
10. TOLERANCE ON ALL DRILL HOLES SHALL BE IAW IPC-D-2221 & 2222 UNLESS OTHERWISE SPECIFIED.
11. ALL VIA HOLES 12 MILS IN SIZE (VIA22C12P_FILLED) ARE TO BE FILLED WITH NON-CONDUCTIVE THERMAL EPOXY AND CAPPED.

PART TO BE FREE OF BURRS AND SHARP EDGES
COMMERCIAL TOLERANCES APPLY TO STOCK SIZES
MANUFACTURING TOLERANCES PER 900002
WORKMANSHIP PER 900260
SCREW THREADS PER ANSI Y14.6
ABBREVIATIONS PER MIL-STD-12
LOGIC SYMBOLS PER IEEE STD 91
DIMENSIONING AND TOLERANCING PER ANSI Y14.5
ELECTRICAL AND ELECTRONIC DIAGRAM PER ANSI Y14.15
GRAPHIC SYMBOLS FOR ELECTRICAL AND ELECTRONIC DIAGRAM PER IEEE STD 315
ELECTRICAL AND ELECTRONIC REFERENCE DESIGNATIONS PER IEEE STD 200
WELDING SYMBOLS PER ANSI/AWS A2.4
INTERPRET DRAWING IAW 900-STD-100

UNLESS OTHERWISE SPECIFIED
DIMENSION ARE IN INCHES
AND INCLUDE APPLIED FINISH
TOLERANCES
2 PLACE DIM. ± ---
3 PLACE DIM. ± ---
ANGLES ± ---

CONTRACT NO.	
DRN BY 10/18/2018 Tim Klemens	CHK BY
ENGR	ELEC ENGR
PROJ ENGR	MFG ENGR
RELIABILITY	CONFIGURATION
APPROVAL- DATE	QA
APPROVAL	SYS ENGR

RENESAS

ISL8210M
EVAL BOARD LAYOUT

SIZE	DWG NO.	REV
D	ISL8210MEVAL1Z	A
SCALE 2/1	SHEET 1 OF 1	