

Product Advisory (PA)

Subject: Correction to the Renesas ISL70001* Datasheet and DLA SMD

Publication Date: 9/16/2022

Effective Date: 9/16/2022

Revision Description:

Initial Release

Description of Change:

This notice is to inform you of datasheet and DLA SMD corrections to the Electrical Specifications as below;

1. Upper Device $r_{DS(ON)}$ Min & Max
2. Lower Device $r_{DS(ON)}$ Min & Max

Corrections are reflected in Appendix A of the notice. The SMD is revised as below;

Rev G = OLD revision

Rev H = NEW revision

Products Impacted by the change;

Renesas Part Number	Ordering Number	Renesas Part Number	Ordering Number	Renesas Part Number	Ordering Number
ISL70001ASEHVF	5962R0922503VXC	ISL70001ASEHEV1Z	N/A	ISL70001SRHVX	5962R0922501V9A
ISL70001ASEHVFE	5962R0922503VYC	ISL70001SEHVF	5962R0922502VXC	ISL70001SRHF/PROTO	N/A
ISL70001ASEHVX	5962R0922503V9A	ISL70001SEHVFE	5962R0922502VYC	ISL70001SEHFE/PROTO	N/A
ISL70001ASEHF/PROTO	N/A	ISL70001SEHVX	5962R0922502V9A	ISL70001SRHX/SAMPLE	N/A
ISL70001ASEHFE/PROTO	N/A	ISL70001SRHVF	5962R0922501VXC	ISL70001SRHEVAL1Z	N/A
ISL70001ASEHX/SAMPLE	N/A	ISL70001SRHQF	5962R0922501QXC		

Reason for Change:

Change corrects the datasheet and DLA SMD to reflect the actual product performance. Details regarding the change are contained within Appendix A, for an updated datasheet please contact your local sales or marketing representative.

Impact on fit, form, function, quality & reliability:

The change will have no impact on the form, fit, function, quality, reliability and environmental compliance of the devices.

Product Identification:

There have been no changes to the product, this is a documentation correction only. There will be no change in the external marking of the packaged products.

Qualification status: Not Applicable, correction only

Sample availability: 9/16/2022

Device material declaration: Available upon request

Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: PCN-US@Renesas.COM	Europe: PCN-EU@Renesas.COM	Japan: PCN-JP@Renesas.COM	Asia Pac: PCN-APAC@Renesas.COM

Appendix A:

Datasheet changes to Section 3.4 and DLA SMD Table 1A

FROM:

Upper device $r_{DS(ON)}$	$r_{DS(ON)}$	$V_{IN} = 3\text{ V}$, $\frac{4}{0.4\text{ A per power block, test mode}}$	4	01, 02, 03	153	277	$\text{m}\Omega$
			5,6		122	346	
Lower device $r_{DS(ON)}$	$r_{DS(ON)}$	$V_{IN} = 3\text{ V}$, $\frac{4}{0.4\text{ A per power block, test mode}}$	4	01, 02, 03	108	184	$\text{m}\Omega$
			5,6		77	236	

^{4/} Limits established by characterization or analysis and are not production tested

TO:

Removed Note 4 regarding limits and not production tested, broke out limits by temp / subgroup, and changed min max limits to align with historical ATE software. The parameters are tested.

Upper device $r_{DS(ON)}$	$r_{DS(ON)}$	$V_{IN} = 3\text{ V}$, 0.4 A per power block, test mode	4	01, 02, 03	180	281	$\text{m}\Omega$
			5		225	395	
			6		125	210	
Lower device $r_{DS(ON)}$	$r_{DS(ON)}$	$V_{IN} = 3\text{ V}$, 0.4 A per power block, test mode	4	01, 02, 03	120	195	$\text{m}\Omega$
			5		165	275	
			6		82	145	