RENESAS

Separate Sheet

Product Specifications of the RZ/N Series of Microprocessors

	RZ/N1D Group	RZ/N1S Group	RZ/N1L Group
Application CPU	Dual ARM® Cortex®-A7 @ up to 500 MHz	Single ARM Cortex-A7 @ up to 500 MHz	N/A
Communication CPU	ARM® Cortex®-M3 @ up to 125 MHz		
R-IN Engine	R-IN Engine (HW-RTOS + HW Ethernet Accelerator)		
DDR Interface	*16-bit DDR2 and DDR3*	N/A	
Integrated SRAM	2 MB SRAM with ECC	6 MB SRAM with ECC	
Industrial Real-Time Networks	PROFINET®, Ethernet POWERLINK®, EtherCAT®, EtherNet/IP™, Sercos®		
Redundancy Protocols	(option) PRP, HSR	(option) PRP	N/A
Security	(option) Cryptographic engine		N/A
Package	400-pin BGA (17mm x 17mm) or 324-pin BGA (15mm x 15mm)	324-pin BGA (15mm x 15mm) or 196-pin BGA (12mm x 12mm)	196 BGA (12mm x 12mm)

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