

RENESAS TECHNICAL UPDATE

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Product Category	MPU & MCU		Document No.	TN-RX*-A071A/E	Rev.	1.00
Title	Note on Event Link Output in the Serial Peripheral Interface		Information Category	Technical Notification		
Applicable Product	RX210 Group, RX220 Group, RX21A Group	Lot No.	Reference Document	RX210 Group User's Manual: Hardware Rev.1.40 (R01UH0037EJ0140) RX220 Group User's Manual: Hardware Rev.1.00 (R01UH0292EJ0100) RX21A Group User's Manual: Hardware Rev.1.00 (R01UH0251EJ0100)		
		All				

This document describes note on mode fault, overrun, or parity error event output in the serial peripheral interface (RSPI) of the above applicable products.

1. Note

If setting the ELSRn register to 52h (RSPI0 error (mode fault, overrun, or parity error) signal) while the SPCR.SPMS bit is 0 (SPI operation), the SPCR.MSTR bit is 1 (master mode), and the SPCR.MODFEN bit is 1 (enables the detection of mode fault error), a mode fault may cause malfunctions of the linked modules.

When the SPCR.SPMS bit is 1 (clock synchronous operation), the SPCR.MSTR bit is 0 (slave mode), or the SPCR.MODFEN bit is 0 (disables the detection of mode fault error), the linked modules operate normally.

2. Measure

Do not use mode fault, overrun, or parity error event output when a multi-master configuration, SPI operation, and master mode are selected for the RSPI (do not set the ELSRn register to 52h when the SPCR.SPMS bit is 0, the SPCR.MSTR bit is 1, and SPCR.MODFEN bit is 1).