Renesas Electronics (China) Co., Ltd. Renesas Electronics (Shanghai) Co., Ltd. Renesas Electronics Hong Kong Limited
Precaution of using REAL-TIME COUNTER
MCCY-ZB-15-0006-1
Jun. / 30 / 2015
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Precaution described below is added to the following products in the User's Manual.

1. Applicable Product

R7F0C001G2DFB	UPD79F7020GB-8ET-A
R7F0C001L2DFB	UPD79F7020GB-UET-AT
R7F0C002G2DFB	UPD79F8433GB-8ET-A
R7F0C002L2DFB	UPD79F8433GB-UET-AT
R7F0C003M2DFB-C	UPD79F8451GK-GAJ-AX
R7F0C004M2DFB-C	UPD79F8452GK-GAJ-AX
R7F0C014L2DFA-C	UPD79F8453GK-GAJ-AX
R7F0C014L2DFB-C	UPD79F9211GB-8ES-A
R7F0C019L2DFB-C	UPD79F9211GB-GAF-AX
	UPD79F9211GB-XXX-8ES-A
	UPD79F9211GB-XXX-GAF-AX

Reference Document

Latest user's manual of applicable product.

2. Precaution regarding the use of REAL-TIME COUNTER

RWAIT bit is bit 0 of Real-time counter Control Register 1 (RTCC1).
For the description of RWAIT bit, it'll add the following Note1, because after setting to RWAIT = 1, the time required to until RWST = 1 might be longer than one clock time of the operation clock (f_{RTC}).

RWAIT	Wait control of real-time clock
0	Sets counter operation.
1	Stops SEC to YEAR counters. Mode to read or write counter value
This bit controls the operation of the counter.	
Be sure to write "1" to it to read or write the counter value.	
Because RSUBC continues operation, complete reading or writing of it in 1 second, and clear this bit back to 0.	
When RWAIT = 1, it takes up to 1 clock (32.768 kHz)until the counter value can be read or written. [Note 1]	
If RSUBC overflows when RWAIT = 1, it counts up after RWAIT = 0. If the second count register is written, however, it does not count up because RSUBC is cleared.	

Note1. When setting RWAIT=1 during 1 operating clock (f_{RTC}), after setting RTCE=1, it may take two clock time of the operation clock (f_{RTC}), until RWST bit is set to "1".