

RENESAS TECHNICAL UPDATE

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Product Category	MPU/MCU		Document No.	TN-RL*-A078A/E	Rev.	1.00
Title	Regarding the precaution addition of 32-bit multiply-accumulator interrupt operation		Information Category	Technical Notification		
Applicable Product	RL78/I1C R5F10Nxx	Lot No.	Reference Document	RL78/I1C User's Manual: Hardware Rev. 1.00 R01UH0587EJ0100 May 27, 2016		
		All lots				

The following precaution is added to the above applicable product user's manual. The precaution will be made for the next revision of the user's manual.

1. Regarding the precaution addition of 32-bit multiply-accumulator interrupt operation

In "39.4.4 multiplication operation" on page 1082 of "chapter 39 32-bit multiply-accumulator" in the RL78/I1C User's Manual: Hardware Rev. 1.00 R01UH0587EJ0100 (May 27, 2016) , the following precaution is added.

[Precaution]

In the case that multiplication mode (signed) is selected and the value of multiplication result register (MULR3-MULR0) is minus before the operation, the multiply-accumulation operation overflow/underflow interrupt (INTMACLOF) may occur. If multiplication mode (signed) is selected, before setting the multiplication data into the MULBH to start operation, please set "1" to interrupt mask flag register (MACMK) to disable INTMACLOF, or please set "0000H" or plus multiplication value to multiplication result register (MULR3).