Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

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April 1st, 2010 Renesas Electronics Corporation

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HITACHI MICROCOMPUTER TECHNICAL UPDATE

DATE	14 November 2001	No.	TN-SH7-378A/E
ТНЕМЕ	SH3/SH3DSP usage notice on RESET bit of RTC		
CLASSIFICATION	☐ Spec. change ☐ Limitation on Use ☐ Supplement of Documents		
PRODUCT NAME	HD6417708,HD6417708S,HD6417708R,HD6417718R,HD6417707,HD6417709,HD6417709A HD6417729,HD6417729R,HD6417709S,HD6417727,HD6417706		
REFERENCE DOCUMENTS	SH7708 series Hardware manual, SH7707 Hardware manual, SH7709 Hardware manual, SH7709A Hardware manual, SH7709S Hardware SH7729 Hardware manual, SH7729R Hardware manual, SH7727 Hardware SH7706 Hardware	nanual,	Effective DatE Eternity From

HD6417708, HD6417708S, HD6417708R, HD6417718R, HD6417707, HD6417709, HD6417709A, HD6417729R, HD6417729R, HD6417709S, HD6417727 and HD6417706 have followed usage notice.

1.Condition

(1) Timing of initializing R64CNT

The maximum time from 1) to 2) is 107us (3.5 times of clock period on EXTAL2 pin).

- 1) Write 1 to RCR2.RESET.
- 2) The 64Hz counter register (R64CNT) read value has initialized.

The RTC divider circuit itself has done initialization at the same time when RCR2.RESET bit is set to 1.

(2) Increasing RSECCNT by initializing R64CNT

If START bit is wrote to 1 within 107us from RESET bit of RCR2 register is wrote to 1, the value of RSECCNT register will be increased by 1.

2. Note on usage

(1) Timing of initializing R64CNT

When it is needed to read initialized value of R64CNT after initializing R64CNT by writting 1 to RESET bit, please read R64CNT after 107us from setting 1 to RESET bit.

(2) Increasing RSECCNT by initializing R64CNT

Please use one of following method which is shown in figure 1 and figure 2.

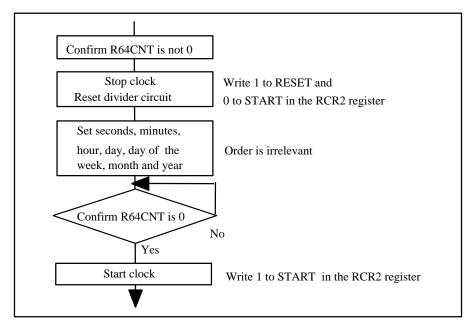


Figure 1 Setting the Time (a)

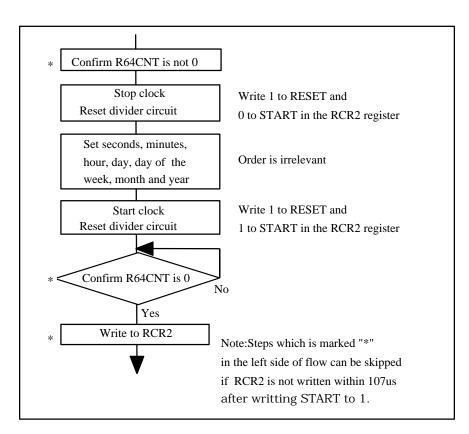


Figure 2 Setting the Time (b)