

Peripheral Driver Generator Revised to V.2.08

We have revised Peripheral Driver Generator from V.2.07 to V.2.08. This utility program aids to create on-chip peripheral I/O drivers. Peripheral Driver Generator V.2 is specific to the RX family of MCUs. For an overview of Peripheral Driver Generator, see:

<https://www.renesas.com/pdg>

The above URL is one of our global sites.

1. Descriptions of Revision

1.1 Supported MCUs Increased

In the RX63T and RX210 groups of MCUs, supported MCUs have been added. The type names of these MCUs are as follows:

(1) RX63T group

Type names:

- R5F563TEADFB, R5F563TEADFA, R5F563TEADFH, R5F563TEADFP,
R5F563TCADFB, R5F563TCADFA, R5F563TCADFH, R5F563TCADFP,
R5F563TBADFB, R5F563TBADFA, R5F563TBADFH, R5F563TBADFP,
R5F563TEDDFB, R5F563TEDDFA, R5F563TEDDFH, R5F563TEDDFP,
R5F563TCDDFB, R5F563TCDDFA, R5F563TCDDFH, R5F563TCDDFP,
R5F563TBDDFB, R5F563TBDDFA, R5F563TBDDFH, R5F563TBDDFP,
R5F563TEBDFB, R5F563TEBDFA, R5F563TEBDFH, R5F563TEBDFP,
R5F563TCBDFB, R5F563TCBDFA, R5F563TCBDFH, R5F563TCBDFP,
R5F563TBBDFB, R5F563TBBDFA, R5F563TBBDFH, R5F563TBBDFP,
R5F563TEEDFB, R5F563TEEDFA, R5F563TEEDFH, R5F563TEEDFP,
R5F563TCEDFB, R5F563TCEDFA, R5F563TCEDFH, R5F563TCEDFP,
R5F563TBEDFB, R5F563TBEDFA, R5F563TBEDFH, and R5F563TBEDFP

(2) RX210 group

Type names:

- R5F5210BBxLB, R5F5210BBxLK, R5F5210BBxLP, R5F5210BBxLJ,
R5F5210ABxLB, R5F5210ABxLK, R5F5210ABxLP, R5F5210ABxLJ,
R5F52108BxLB, R5F52108BxLK, R5F52107BxLB, R5F52107BxLK,
R5F52106BxLB, R5F52106BxLK, R5F52106BxLP, R5F52106BxLN,
R5F52106BxLM, R5F52106BxLL, R5F52106BxLJ, R5F52106BxLA,
R5F52106BxFF, R5F52106BxFFK, R5F52106BxLH, R5F52106BxBM,

R5F52105BxFB, R5F52105BxLK, R5F52105BxFP, R5F52105BxFN, R5F52105BxFM, R5F52105BxFL, R5F52105BxLJ, R5F52105BxLA, R5F52105BxFF, R5F52105BxFK, R5F52105BxLH, R5F52105BxBM, R5F52104BxFM, R5F52104BxFL, R5F52104BxFF, R5F52104BxLH, R5F52103BxFM, R5F52103BxFL, R5F52103BxFF, R5F52103BxLH, R5F52108CxFP, R5F52108CxFN, R5F52108CxFM, R5F52108CxLJ, R5F52108CxFF, R5F52108CxFK, R5F52107CxFP, R5F52107CxFN, R5F52107CxFM, R5F52107CxLJ, R5F52107CxFF, and R5F52107CxFK

1.2 Problems Fixed

The following problems have been fixed.

- (1) With using a callback function for the I2C bus interface (RIIC) to send a slave address

For details of the problem, see RENESAS TOOL NEWS Document No. 130701/tn6 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=130701tn6>

- (2) With setting up the clock generation circuit when using the RX62T group of MCUs

For details of the problem, see RENESAS TOOL NEWS Document No. 130601/tn1 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=130601tn1>

- (3) With making changes to alarm setting of real-time clock (RTC) (see NOTE)

For details of the problem, see RENESAS TOOL NEWS Document No. 130401/tn2 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=130401tn2>

We are sorry that we announced this problem concerns the RX210, RX630 and RX62N groups of MCUs in Peripheral Driver Generator.

Note, however, that the RX62N group of MUCs is not concerned.

NOTE:

This problem has been fixed in only the RX210 group of MCUs in Peripheral Driver Generator. For the RX630 group of MCUs, it has not been fixed yet.

- (4) With using serial communication interface (SCIa) of RX62N/RX621 groups

For details of the problem, see RENESAS TOOL NEWS Document No. 130301/tn5 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn5>

- (5) With using real-time clock (RTC) of RX62N/RX621 groups

For details of the problem, see RENESAS TOOL NEWS Document

No. 130301/tn4 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn4>

(6) With Using EXDMA Controller (EXDMAC) of RX62N/RX621 Groups

For details of the problem, see RENESAS TOOL NEWS Document

No. 130301/tn3 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn3>

(7) With using I2C-bus interface (RIIC) of RX62N/RX621 groups

For details of the problem, see RENESAS TOOL NEWS Document

No. 130301/tn2 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=130301tn2>

(8) With setting programmable pulse generators (PPGs) (see NOTE)

For details of the problem, see RENESAS TOOL NEWS Document

No. 120916/tn2 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=120916tn2>

NOTE:

This problem has been fixed in only the RX62N group of MCUs in Peripheral Driver Generator. For the RX63N, RX631 and RX630 groups of MCUs, it has not been fixed yet.

(9) With creating the codes for setting the complementary PWM mode of the multi-function timer pulse unit 3 (MTU3) in the RX62T group of MCUs

For details of the problem, see RENESAS TOOL NEWS Document

No. 120601/tn8 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=120601tn8>

(10) With data transmission through the serial peripheral interface (RSPI)

For details of the problem, see RENESAS TOOL NEWS Document

No. 120601/tn7 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=120601tn7>

(11) With entering the information of the external bus of the RX62N group of MCUs

For details of the problem, see RENESAS TOOL NEWS Document

No. 120601/tn6 at:

<https://www.renesas.com/search/keyword-search.html#genre=document&q=120601tn6>

1.3 Restrictions Lifted

The following restrictions have been lifted:

(1) A Note on Using MTU Pulse Output Pins
(RX210, RX220, RX630, RX63N, and RX631)
For details of the restriction, see Section 2.1 in the Peripheral Driver Generator V.2.07 Release Note from [HERE](#).

(2) A Note on Reading Time in RTC
(RX210, RX220, RX630, RX63N, and RX631)
For details of the restriction, see Section 2.2 in the Peripheral Driver Generator V.2.07 Release Note from [HERE](#).

(3) A Note on Reading the Hour Mode in RTC
(RX210, RX220, RX630, RX63N, and RX631)
For details of the restriction, see Section 2.3 in the Peripheral Driver Generator V.2.07 Release Note from [HERE](#).

(4) A Note on Using P27 for Peripheral Function (RX210)
For details of the restriction, see Section 2.12 in the Peripheral Driver Generator V.2.07 Release Note from [HERE](#).

1.4 Renesas Peripheral Driver Library Updated

We have updated to the following versions of the Renesas Peripheral Driver Library included with the Peripheral Driver Generator.

- RX62N Group Renesas Peripheral Driver Library V.1.10
- RX62T Group Renesas Peripheral Driver Library V.1.01
- RX210 Group Renesas Peripheral Driver Library V.2.00
- RX63T Group Renesas Peripheral Driver Library V.2.00

1.5 Options Added

- (1) Big-endian in the RX62N and RX62T groups of MCUs has been supported.
- (2) The address mode selection option has been added to the RIIC master transmission and reception functions.

2. Host System Requirements

Computer: IBM PC/AT or compatible

OS: Windows 7, Windows XP, Windows Vista, or Windows 8

All other necessary software environments:

- .NET Framework 3.5 SP1 (Windows 8 excluded.)
- Microsoft Visual C++ 2008 SP1 runtime library

3. Precaution

Some known problems reside in Peripheral Driver Generator V.2.08.

You can refer to the details of them that have not been fixed yet on the following Web page:

<https://www.renesas.com/search/keyword-search.html#genre=document&documenttype=531&toollayer=300616>

In addition, please read through the release note.

4. How to Update Your Product

Update in either of the following ways:

(1) Use Auto Update Utility. This service will be available on and after December 25.

(2) Download the installer of the revised product from:

https://www.renesas.com/pdg_download

Then execute it. The installer will be published on the Web site on December 20.

The above URL is one of our global sites.

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