RENESAS TOOL NEWS

[Released on the Web]

Smart Configurator for RZ/A1L and RZ/A1LU Products

R20TS0094EJ0100 Rev.1.00 Nov. 16, 2016

Outline

We are releasing the Smart Configurator for RZ/A1L and RZ/A1LU products of the RZ family.

1. Features

The Smart Configurator is a tool for checking pin settings and the state of settings of microcontrollers when designing hardware specifications.

You can set pins of the peripheral functions from the GUI of the Smart Configurator, and check for contention between multiplexed functions in pin configuration figures and pin configuration tables. Output of the setting information makes it easy for you to transfer correct hardware specifications to software designers.

rza1.scfg 🛛				
ns Configuration				Type pin function
ardware Resource $\boxplus \boxminus \downarrow_{\mathbb{Z}}^{a}$	Pin Functi	on	3 🖬 한 전	
Type filter text	Type pin f	function		2 (2) (1) (1) (2) (2) (2) (2) (2) (2) (2) (2) (2) (2
BSC5 A	Enabled	Function	Assignment	T [2] [2] [10] [10] [10] [10] [10] [10] [10] [10
and Direct memory access co		TCLKA	Not assigned	F F F R R R R R R R R R R R R R R R R R
🗆 🖏 Multi-function timer pulse	V	TCLKB	P6_1/D17/LCD0_DATA9/SSL00/T	z (70) (70) (80) 800 800 800 800 800 800 800 800
💣 MTU 💡		TCLKC	Not assigned	
💣 МТОО		TCLKD	Not assigned =	RENESAS
S MTU1	V	TIOCOA	P2_8/RD#/SSITxD3/TIOC0A/CAN	• (see the form we RZAIL
MTU2	V	TIOC0C	P2_2/CAS#/CAN1RX/TIOC0C/IRC	a 100 0 00 00 00 00 00 00 00 00 00 00 00
📦 MTU3	1	TIOCOD	P2_3/CKE/CAN1TX/TIOC0D	R7S721020VCBG
MTU4		TIOC1A	Not assigned	
🖏 Realtime clock		TIOC1B	Not assigned	
🖃 📲 Serial communication inte		TIOC2A	Not assigned	
SCIF0		TIOC2B	Not assigned	N 018 011 1400, W DW 02,00 000 00,00 00,00 00,00 00,00 000 0
SCIF1		TIOC3A	Not assigned	
SCIF2 -		TIOC3B	Not assigned	P Em im 94 E3 E3 E3 10 10 10 10 10 00 00 00 im 40
	•	m	•	x
in Function Pin Number				
pard Pins		Legend		
コンフィグレーションチェック 🛛				
errors, 0 warnings, 0 others				
escription				Туре
😣 Pin (2 items)				
8 E05000010: Pin L14 cannot be u	used multiple	n Allocator. Pin		
8 E05000010: Pin L15 cannot be u	used multiple	Allocator. Pin		

The pins set to be used by each peripheral function can be displayed in the [Pins Configuration] window. In the [MCU Package] window, the used pins, unused pins, and pins for which multiplexed functions are in contention are indicated by different colors, making the allocation easy to check.



2. Supported Devices

RZ family

RZ/A1L group: R7S721020VCBP, R7S721020VCFP, R7S721020VLFP, R7S721021VCFP, and R7S721021VLFP RZ/A1LU group: R7S721030VCBG, R7S721030VCFP, R7S721030VLFP, R7S721031VCFP, and R7S721031VLFP

3. Operating Environments

IBM PC/AT compatible

Windows® 10 (32-bit edition, 64-bit edition)

Windows® 8.1 (32-bit edition, 64-bit edition)

Windows® 7 (32-bit edition, 64-bit edition)

Windows Vista® (32-bit edition, 64-bit edition)

Processor: At least 1 GHz (hyper-threading and multicore CPUs are supported)

Memory: We recommend at least 2 GB. At least 1 GB (or at least 2 GB for Windows 10 and 64-bit editions) is required.

Hard disk: At least 1 GB of available space

Display: Resolution should be at least 1024 x 768 with at least 65536 colors

4. Obtaining the Product

Download the installer from the URL below to install the product.

The installer is scheduled for posting on November 21. <u>https://www.renesas.com/ja-jp/products/microcontrollers-microprocessors/rz/rza/rza11.html#tools</u>



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Revision History

			Description		
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