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瑞萨电子公司网址：<http://www.renesas.com>

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瑞萨电子公司

【发行】瑞萨电子公司（<http://www.renesas.com>）

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地址重叠错误

Trouble

用户反映，基于 Renesas M16C 内核的 R8C MCU 编译时发生地址重叠错误，导致不能正常仿真调试。

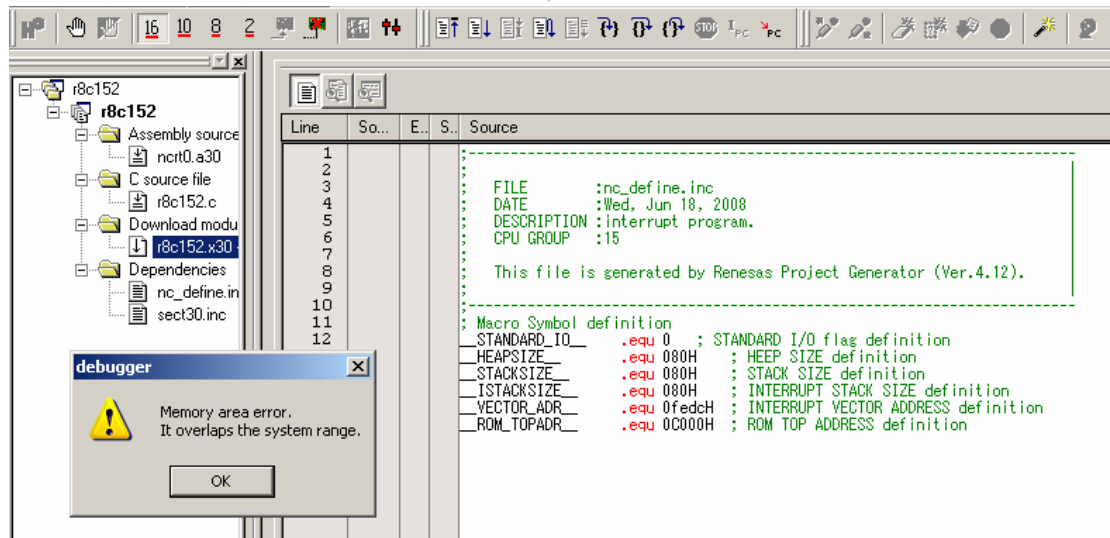
Analyze

检查程序,发现用户在编译时出现地址重叠错误

其过程如下:

当下载程序到 E8 时,出现如图所示的地址重叠错误

其原因是 E8 占用 MCU 的 2K 的 ROM 资源



Do

修改启动文件的 nc.define.inc ROM_TOPADR 的起始地址。

RTSHCMCAN0005-0100 Rev.1.00

```

Line   So...  E..  S..  Source
-----
1      |      |     |     |
2      |      |     |     |
3      |      |     |     |
4      |      |     |     | FILE      :nc_define.inc
5      |      |     |     | DATE      :Wed, Jun 18, 2008
6      |      |     |     | DESCRIPTION: interrupt program.
7      |      |     |     | CPU GROUP :15
8      |      |     |     |
9      |      |     |     | This file is generated by Renesas Project Generator (Ver.4.12).
10     |      |     |     |
11     |      |     |     | ; Macro Symbol definition
12     |      |     |     | STANDARD_I/O_ .equ 0 ; STANDARD I/O flag definition
13     |      |     |     | HEAPSIZE_     .equ 080H ; HEAP SIZE definition
14     |      |     |     | STACKSIZE_    .equ 080H ; STACK SIZE definition
15     |      |     |     | ISTACKSIZE_  .equ 080H ; INTERRUPT STACK SIZE definition
16     |      |     |     | VECTOR_ADR_  .equ 0fedcH ; INTERRUPT VECTOR ADDRESS definition
17     |      |     |     | _ROM_TOPADR_ .equ 0C000H ; ROM TOP ADDRESS definition
18     |      |     |     |
  
```

把上图的 `__ROM_TOPADR__ .equ 0C000H ; ROM TOP ADDRESS definition` 改成 `__ROM_TOPADR__ .equ 0C800H ; ROM TOP ADDRESS definition` 如下图所示:

```

Line   So...  E..  S..  Source
-----
1      |      |     |     |
2      |      |     |     |
3      |      |     |     |
4      |      |     |     | FILE      :nc_define.inc
5      |      |     |     | DATE      :Wed, Jun 18, 2008
6      |      |     |     | DESCRIPTION: interrupt program.
7      |      |     |     | CPU GROUP :15
8      |      |     |     |
9      |      |     |     | This file is generated by Renesas Project Generator (Ver.4.12).
10     |      |     |     |
11     |      |     |     | ; Macro Symbol definition
12     |      |     |     | STANDARD_I/O_ .equ 0 ; STANDARD I/O flag definition
13     |      |     |     | HEAPSIZE_     .equ 080H ; HEAP SIZE definition
14     |      |     |     | STACKSIZE_    .equ 080H ; STACK SIZE definition
15     |      |     |     | ISTACKSIZE_  .equ 080H ; INTERRUPT STACK SIZE definition
16     |      |     |     | VECTOR_ADR_  .equ 0fedcH ; INTERRUPT VECTOR ADDRESS definition
17     |      |     |     | _ROM_TOPADR_ .equ 0C800H ; ROM TOP ADDRESS definition
18     |      |     |     |
  
```

MCU

文档适用于基于 Renesan M16C 内核的 MCU。