

RJE0617JSP

-60V, -1.5A, P Channel Thermal FET Power Switching R07DS1070EJ0500 Rev.5.00 Jan 31, 2020

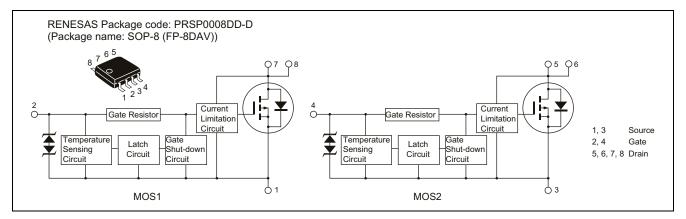
Description

This FET has the over temperature shut-down capability sensing to the junction temperature. This FET has the built-in over temperature shut-down circuit in the gate area. And this circuit operation to shut-down the gate voltage in case of high junction temperature like applying over power consumption, over current etc..

Features

- Logic level operation (3 V Gate drive).
- Built-in the over temperature shut-down circuit.
- High endurance capability against to the short circuit.
- Hysteresis type shut down operation.
- High density mounting.
- Built-in the current limitation circuit.
- Power supply voltage applies 12 V.
- AEC-Q101compliant.

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-60	V
Gate to source voltage	Vgss	-16	V
Gate to source voltage	Vgss	2.5	V
Drain current	ID Note4	-1.5	А
Body-drain diode reverse drain current	I _{DR}	-1.5	А
Avalanche current	AP Note 3	-1.5	А
Avalanche energy	EAR Note 3	9.6	mJ
Channel dissipation	Pch Note 1	1	W
Channel dissipation	Pch Note 2	1.5	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. 1 Drive operation : When using the glass epoxy board (FR4 $40 \times 40 \times 1.6$ mm), PW ≤ 10 s

2. 2 Drive operation : When using the glass epoxy board (FR4 40 \times 40 \times 1.6 mm), PW \leq 10 s

3. Tch = 25°C, Rg \geq 50 Ω

4. It provides by the current limitation lower bound value.



Typical Operation Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Input voltage	VIH	-3	_	_	V	
	VIL		_	-1.2	V	
Input current	Іін1	_	_	-100	μA	$Vi = -8 V, V_{DS} = 0$
(Gate non shut down)	I _{IH2}	_	_	-50	μA	$Vi = -3.5 V, V_{DS} = 0$
	IIL		_	-10	μA	$Vi = -1.2 V, V_{DS} = 0$
Input current	IIH(sd)1	_	-0.8	_	mA	$Vi = -8 V, V_{DS} = 0$
(Gate shut down)	IIH(sd)2		-0.35	_	mA	$Vi = -3.5 V, V_{DS} = 0$
Shut down temperature	Tsd		175		°C	Channel temperature
Return temperature	Thr	_	105	_	°C	Channel temperature
Gate operation voltage	Vop	-3	_	-12	V	
Drain current	I _{D limt}	-1.5			Α	$V_{GS} = -12 \text{ V}, V_{DS} = -10 \text{ V}^{\text{Note 5}}$
(Current limitation value)		-1.5				

Notes; 5. Pulse test

Electrical Characteristics

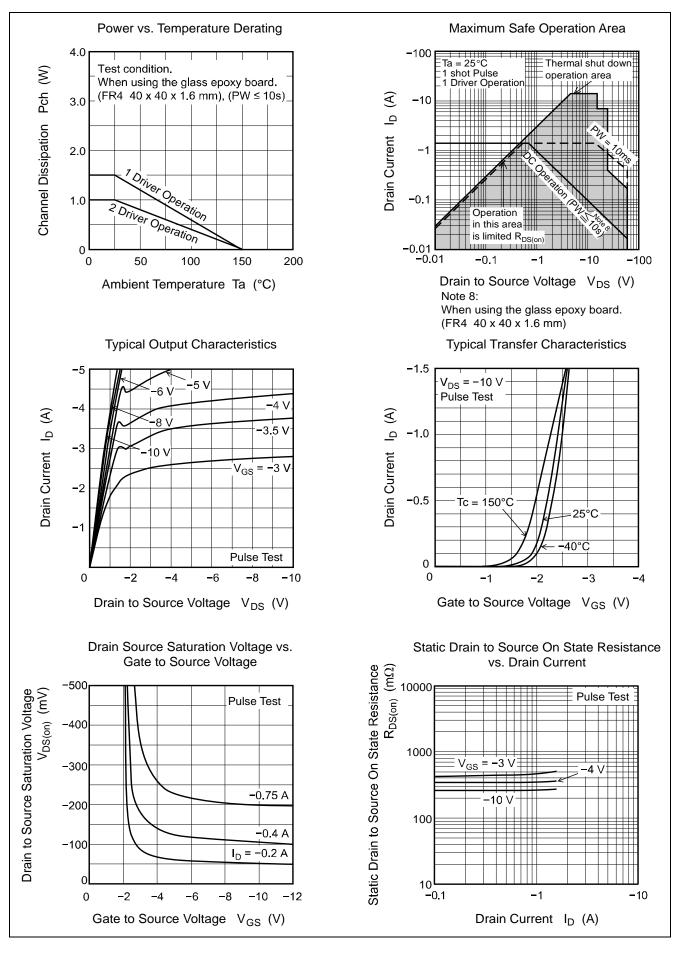
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain current		-1.5		-12	A	$V_{GS} = -3.5 \text{ V}, V_{DS} = -10 \text{ V}^{\text{Note 6}}$
Drain current	lo Ip	-1.5		-12 -40	mA	$V_{GS} = -3.5 \text{ V}, \text{ V}_{DS} = -10 \text{ V}$ $V_{GS} = -1.2 \text{ V}, \text{ V}_{DS} = -10 \text{ V}$
	_		<u> </u>		A	$V_{GS} = -12 \text{ V}, \text{ V}_{DS} = -10 \text{ V}^{\text{Note 6}}$
		-0.8			A	$V_{GS} = -3 V, V_{DS} = -10 V^{Note 6}$
Drain to source breakdown voltage	V(BR)DSS	-60	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown	V _{(BR)GSS}	-16	—	-	V	$I_G = -800 \ \mu A, \ V_{DS} = 0$
voltage	V _{(BR)GSS}	2.5	_	_	V	I _G = 100 μA, V _{DS} = 0
Gate to source leak current	Igss		_	-100	μA	$V_{GS} = -8 V, V_{DS} = 0$
	lgss	_	_	-50	μA	$V_{GS} = -3.5 \text{ V}, V_{DS} = 0$
	Igss	_	_	-10	μA	$V_{GS} = -1.2 V, V_{DS} = 0$
	lgss		_	100	μA	$V_{GS} = 2.4 V, V_{DS} = 0$
Input current (shut down)	I _{GS(OP)}		-0.8		mA	$V_{GS} = -8 V, V_{DS} = 0$
	IGS(OP)		-0.35	_	mA	$V_{GS} = -3.5 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	IDSS		_	-10	μA	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0$
	I _{DSS}		_	-10	μA	$V_{DS} = -48 \text{ V}, \text{ V}_{GS} = 0$
						Ta = 125°C
Gate to source cutoff voltage	V _{GS(off)}	-0.9	_	-2.1	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$
Forward transfer admittance	y _{fs}	1.5	2.7	_	S	$I_D = -0.75 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 6}}$
Static drain to source on state	R _{DS(on)}	_	445	800	mΩ	$I_D = -0.4 \text{ A}, V_{GS} = -3V^{\text{Note 6}}$
resistance	R _{DS(on)}	_	363	425	mΩ	$I_D = -0.75 \text{ A}, V_{GS} = -4 \text{ V}^{\text{Note 6}}$
	R _{DS(on)}	_	272	350	mΩ	$I_D = -0.75 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note 6}}$
Output capacitance	Coss	_	213	_	pF	$V_{DS} = -10 V, V_{GS} = 0,$
						f = 1MHz
Turn-on delay time	t _{d(on)}	_	0.9	_	μS	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -0.75 \text{ A},$
Rise time	tr	_	3.4	_	μS	R _L = 40 Ω
Turn-off delay time	t _{d(off)}	_	3.2		μs]
Fall time	tf		6.3	_	μS]
Body-drain diode forward voltage	V _{DF}		-0.8	_	V	$I_F = -1.5 \text{ A}, V_{GS} = 0$
Body-drain diode reverse	t _{rr}		70	_	ns	$I_F = -1.5 \text{ A}, V_{GS} = 0$
recovery time						di _F /dt = 50 A/µs
Over load shut down operation time Note 7	t _{os}	—	5.4		ms	$V_{GS} = -5 V, V_{DD} = -16 V$

Notes: 6. Pulse test

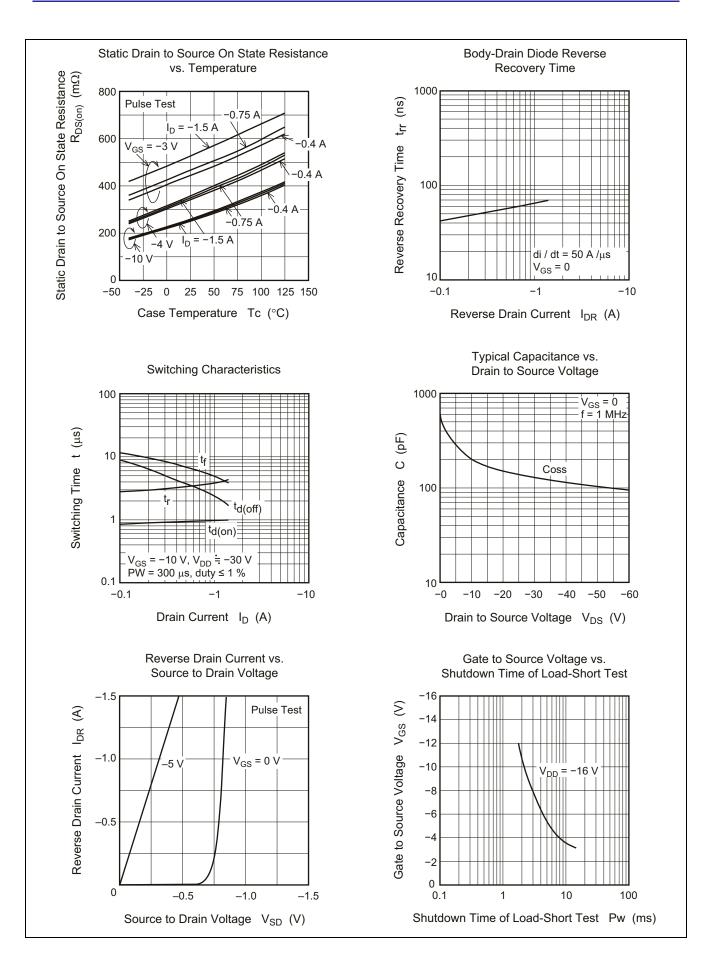
7. Including the junction temperature rise of the over loaded condition.



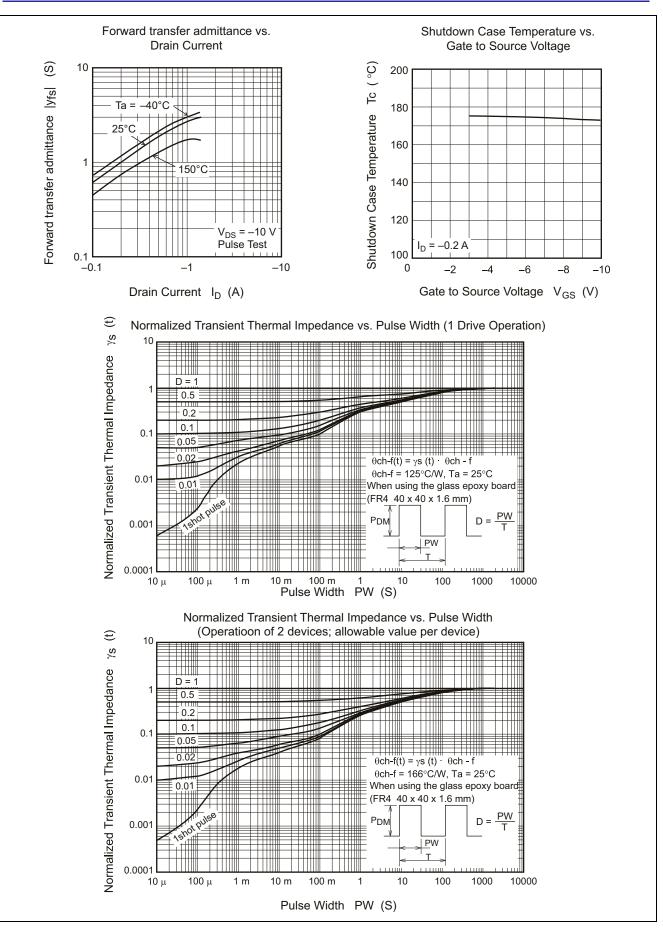
Main Characteristics

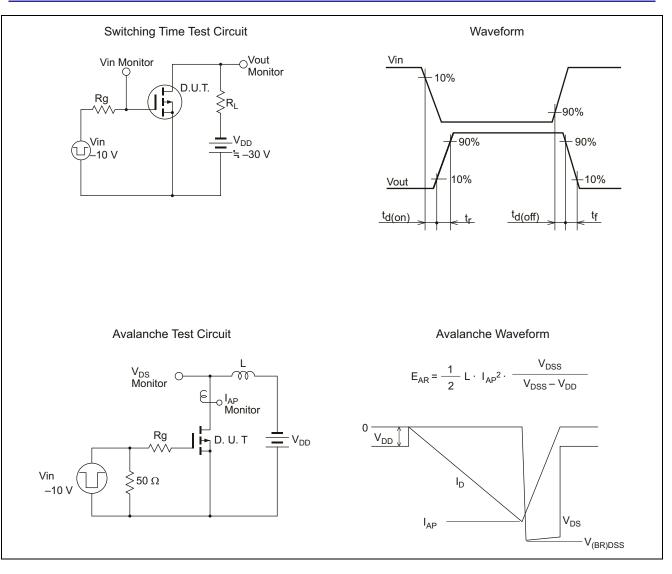






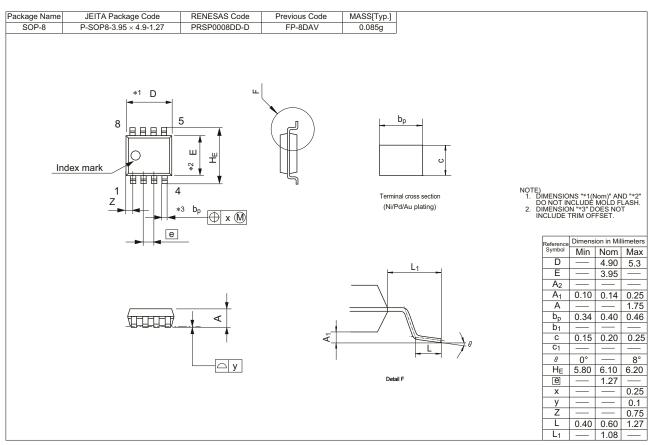








Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RJE0617JSP-00-J0	2500 pcs/reel	Taping

Note: The symbol of 2nd "-" is occasionally presented as "#".



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