

June 6, 2011

## Product Specifications of the SH7734 MCU

Item	Specifications
Group name	SH7734
Product name	R8A77343PA0FBG
Input voltage	Core: 1.25 V, I/O: 3.3 V Memory: 1.5 V or 1.8 V
Operating frequency	533 MHz
Temperature	-40 to +85°C
CPU core	SH-4A
Number of instructions	103
Internal RAM	Fast speed internal memory: ILRAM 16 KB, OLRAM 16 KB
Cache memory	64 KB (Instruction 32 K/data 32 K, 4 way-set-associative)
External memory bus (extension bus)	Bus clock: (Max) 50 MHz
	The bus state controller supports SRAM, Burst ROM or ATA interfaces.
	Address space: (Max) 7 areas
	Data bus width: 8- or 16-bit
External memory (main memory)	Bus clock: (Max) 300 MHz
	The DDR controller supports both DDR2-SDRAM and DDR3-SDRAM.
	Data bus width: 16-bit
Peripheral modules	2D graphics accelerator supporting OpenVG1.1
	Display unit
	Gigabit Ethernet controller (Built-in dedicated DMAC)
	Video in module (BT656)
	Serial sound interface (Max 4 ch)
	Sampling rate converter
	USB 2.0 (High-speed) host or function
	Multi-function 16-bit timer (MTU2)

Item	Specifications
	I <sup>2</sup> C bus interface (Max 2 ch)
	Serial communication interface with FIFO
	Host interface
	NAND flash interface
	A/D converter (10-bit) (max 8 ch)
	Watchdog timer
	Real time clock
	Direct memory access controller
	MMC host interface
	SD host interface
	Stream interface
	RCAN
	Low power modes
Software standby mode	
Deep standby mode	
Module standby mode	
Package	440-pin BGA (21 mm × 21 mm) 0.8 mm pitch

(Remarks)

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