[Notes]

CS+ Integrated Development Environment

R20TS0091EJ0100 Rev.1.00 Nov. 01, 2016

Outline

When using the CS+ IDE, take note of the problem described in this note regarding the following point.

1. The property setting of the V850 compiler (CA850) for the address where the programmable I/O area starts

1. The Property Setting of the V850 Compiler (CA850) for the Address Where the Programmable I/O Area Starts

1.1 Applicable Products

- CubeSuite V1.20 to V1.40
- $\blacktriangleright CubeSuite+ V1.00.00 to CS+ for CA, CX V3.03.00$

1.2 Applicable MCU

V850 family

1.3 Details

The address where the programmable I/O area of the target device starts is not specifiable with the below property of the CA850 build tool. A dialog box with the message "Property value is not valid." is displayed if specification in this way is attempted.

Property: [Common Options] tabbed page -> [Device] category -> [Programmable I/O area start address]

Note that this property setting is not displayed for devices which do not have a programmable I/O function.

1.4 Workarounds

Specify the CA850 option directly in the property settings below.

(a) In the case of access to programmable I/O from a C source file

Property: [Compile Options] tabbed page -> [Others] category -> [Other additional options]

Compiler option: Specify "-Xbpc=num".

(b) In the case of access to programmable I/O from an assembler file

Property: [Assemble Options] tabbed page -> [Others] category -> [Other additional options]

Assembler option: Specify "-bpc=num".

For details of the format for the specification* of these options, refer to the following user's manual.

https://www.renesas.com/search/keyword-search.html#genre=document&q=r20ut0557

CubeSuite+ V1.00.00 Integrated Development Environment User's Manual: V850 Build Appendix B: Command Reference

*: The formats for the specification of the options are the same in CubeSuite, CubeSuite+, and CS+.

1.5 Schedule for Fixing the Problem

This problem will be fixed in the next version.



Revision History

		Description	
Rev.	Date	Page	Summary
1.00	Nov. 01, 2016	-	First edition issued

TOYOSU FORESIA, 3-2-24 Toyosu, Koto-ku, Tokyo 135-0061 Japan

Renesas Electronics Corporation

Inquiry

http://www.renesas.com/contact/

Renesas Electronics has used reasonable care in preparing the information included in this document, but Renesas Electronics does not warrant that such information is error free. Renesas Electronics assumes no liability whatsoever for any damages incurred by you resulting from errors in or omissions from the information included herein.

The past news contents have been based on information at the time of publication.

Now changed or invalid information may be included. The URLs in the Tool News also may be subject to change or become invalid without prior notice.

All trademarks and registered trademarks are the property of their respective owners.

