APPLICATION NOTE



AN1145 Rev 1.00

A Compendium of Application Circuits for Intersil Digitally-Controlled (XDCP) Potentiometers

Jan 9, 2013

Introduction

This application note lists a number of application circuits for Intersil's digitally-controlled (XDCP) potentiometers. The application circuits illustrate the wide variety of possible functions which can be implemented using the variability of the potentiometer in conjunction with standard active devices like operational amplifiers and comparators. The types of circuits include control circuits, converters, filters, signal processing circuits, regulators, wave shapers, analog computing circuits and signal sources. The circuits are shown in basic form and do not include supply decoupling or proper grounding techniques. The user must account for these in the final design.

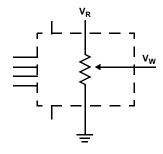
Intersil's potentiometers are controlled through the 2-wire, I²C, 3-wire, or SPI computer serial-interfaces or buses. For front panel, push button type applications, Intersil's push pots are recommended.

Electronic digitally-controlled (XDCP) potentiometers provide three powerful application advantages:

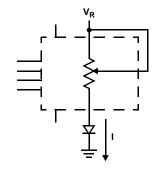
- 1. The variability and reliability of a solid-state potentiometer.
- 2. The flexibility of computer-based digital controls.
- 3. The retentivity of nonvolatile memory used for the storage of multiple potentiometer settings or data.

In addition, the packages of the potentiometers are completely compatible with other electronic components and hence reduce manufacturing assembly costs.

Applications



THREE TERMINAL POTENTIOMETER; VARIABLE VOLTAGE DIVIDER



TWO TERMINAL VARIABLE RESISTOR; VARIABLE CURRENT

FIGURE 1. BASIC CONFIGURATIONS OF ELECTRONIC POTENTIOMETERS

Application Circuits

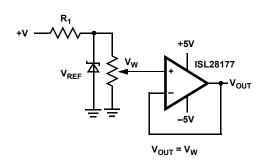


FIGURE 2. BUFFERED REFERENCE VOLTAGE

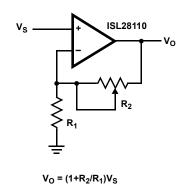


FIGURE 4. NONINVERTING AMPLIFIER

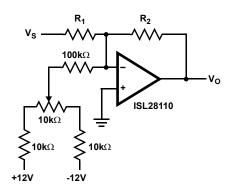


FIGURE 6. OFFSET VOLTAGE ADJUSTMENT

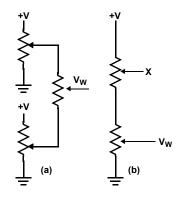
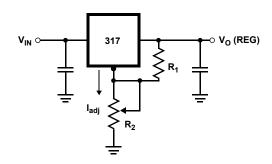
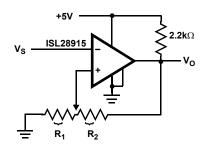


FIGURE 3. CASCADING TECHNIQUES



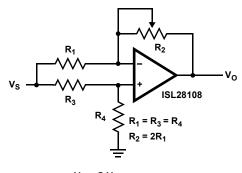
 $V_O(REG) = 1.25V(1+R_2/R_1)+I_{adj}R_2$

FIGURE 5. VOLTAGE REGULATOR



 $\begin{aligned} &V_{UL} = \{R_1/(R_1 + R_2)\} \ V_O(MAX) \\ &V_{LL} = \{R_1/(R_1 + R_2)\} \ V_O(MIN) \end{aligned}$

FIGURE 7. COMPARATOR WITH HYSTERISIS



 $V_O = G V_S$ $-1/2 \le G \le +1/2$

FIGURE 8. ATTENUATOR

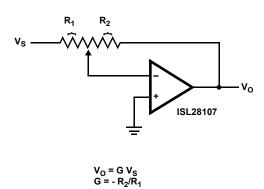


FIGURE 10. INVERTING AMPLIFIER

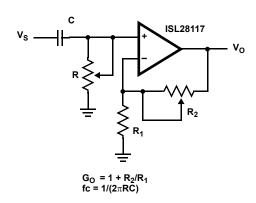
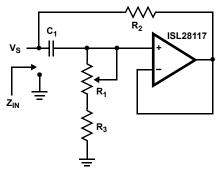


FIGURE 9. FILTER



 $Z_{IN} = R_2 + s R_2 (R_1 + R_3) C_1 = R_2 + s Leq$ $(R_1 + R_3) >> R_2$

FIGURE 11. EQUIVALENT L-R CIRCUIT

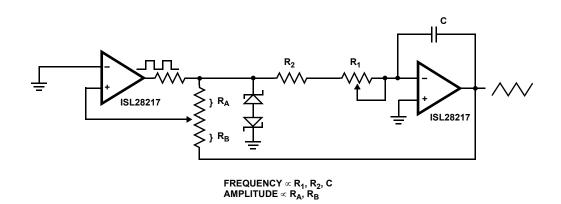
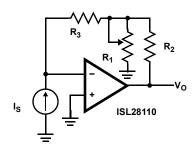
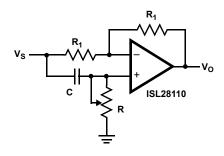


FIGURE 12. FUNCTION GENERATOR



 $V_0 / I_S = -R_3(1+R_2/R_1) + R_2$

FIGURE 13. I TO V CONVERTER



 \angle V_O/V_S = 180° – 2tan⁻¹ ω RC

FIGURE 15. PHASE SHIFTER

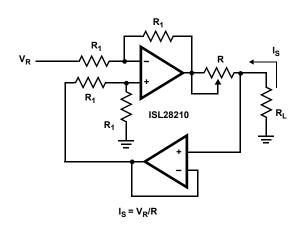
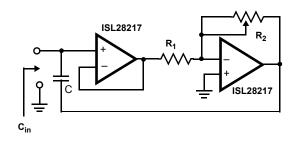


FIGURE 14. CURRENT SOURCE



 $C_{IN} = C (1 + R_2/R_1)$

FIGURE 16. CAPACITANCEV MULTIPLIER

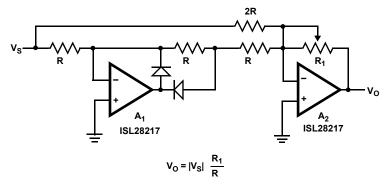
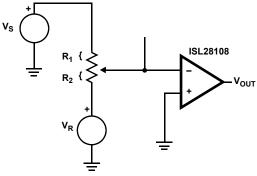


FIGURE 17. ABSOLUTE VALUE AMPLIFIER WITH GAIN

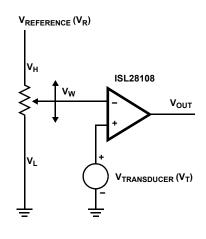


$$V_{OUT} = HIGH FOR V_S \le \frac{R_1}{R_2} V_R$$

$$V_{OUT} = LOW FOR V_S \ge \frac{R_1}{R_2} V_R$$

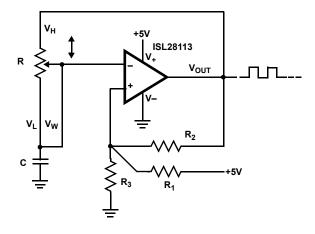
$$R_1 + R_2 = R_{POT}$$

FIGURE 18. LEVEL DETECTOR



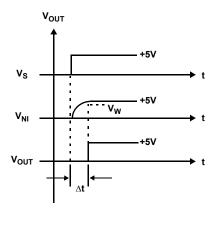
$$V_T > V_W$$
, $V_{OUT} = HIGH$
 $V_T < V_W$, $V_{OUT} = LOW$

FIGURE 19. LEVEL DETECTOR



Frequency \propto R, C Duty Cycle \propto R₁, R₂, R₃

FIGURE 20. OSCILLATOR



$$\Delta t = RCIn \qquad \left(\frac{5V}{5V - V_W}\right)$$

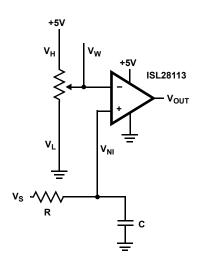


FIGURE 21. TIME DELAY

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Renesas Electronics America Inc. 1001 Murphy Ranch Road, Milpitas, CA 95035, U.S.A. Tel: +1-408-432-8888, Fax: +1-408-434-5351

Renesas Electronics Canada Limited 9251 Yonge Street, Suite 8309 Richmond Hill, Ontario Canada L4C 9T3 Tel: +1-905-237-2004

Renesas Electronics Europe Limited Dukes Meadow, Milliboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tei: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, German Tel: +49-211-6503-0, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
Room 1709 Quantum Plaza, No.27 ZhichunLu, Haidian District, Beijing, 100191 P. R. China Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 301, Tower A, Central Towers, 555 Langao Road, Putuo District, Shanghai, 200333 P. R. China Tel: +86-21-2226-0898, Fax: +86-21-2226-0999

Renesas Electronics Hong Kong Limited

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Renesas Electronics Taiwan Co., Ltd.

13F, No. 363, Fu Shing North Road, Taipei 10543, Taiwan Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre, Singapore 339949 Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd. Unit 1207, Block B, Menara Amcorp, Amco Amcorp Trade Centre, No. 18, Jin Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia

Unit 1207, Block B, Menara Amcorp, Amcorp Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics India Pvt. Ltd. No.777C, 100 Feet Road, HAL 2nd Stage, Indiranagar, Bangalore 560 038, India Tel: +91-80-67208700, Fax: +91-80-67208777

Renesas Electronics Korea Co., Ltd. 17F, KAMCO Yangiae Tower, 262, Gangnam-daero, Gangnam-gu, Seoul, 06265 Korea Tel: +82-2-558-3737, Fax: +82-2-558-5338