Renesas RA Family

Getting Started with the Graphics Application

Introduction

This application note describes the creation of an application that uses Graphical User Interfaces with an EK-RA6M3G kit, referred to as a 'graphics application'. This application is geared towards providing a reference for developing complex multi-threaded applications with a touchscreen graphical Human Machine Interface (HMI) by using the Renesas Flexible Software Package (FSP) and SEGGER AppWizard.



Figure 1. Weather Panel of the Graphics Application on Renesas EK-RA6M3G

This application is developed using the Renesas RA Flexible Software Package (FSP), which provides a quick and versatile way to build secure connected Internet of Things (IoT) devices using the Renesas RA family of Arm[®]-based microcontrollers (MCUs). RA FSP provides production-ready peripheral drivers to take advantage of the RA FSP ecosystem along with the SEGGER emWin library and FreeRTOS. In addition, Ethernet, USB, and file system stack support are also available. This powerful suite of tools provides a comprehensive, integrated framework for the rapid development of complex embedded applications.

This application note assumes that you are familiar with the concepts associated with writing multi-threaded applications under a Real-Time Operating System (RTOS) environment, such as FreeRTOS. This application note makes use of RTOS features such as threads and semaphores. Knowledge of operating these with FreeRTOS can help in understanding the supplied application project in the source. For more detailed information on FreeRTOS features, refer to the FreeRTOS User Manual.

The graphics application is developed using the Renesas e² studio Integrated Development Environment (IDE). This e² studio is a free application that you can download from the Renesas website. While building applications under the Renesas FSP Platform is considerably faster than developing similar applications in other environments, there is still a learning curve to understand the steps necessary to construct complex multi-threaded HMI applications quickly. This application note walks you through all the steps necessary, including the following:

- Board setup.
- Application overview.
- Detailed explanation of uses of the graphical screens.
- SEGGER AppWizard project integration.
- SEGGER AppWizard interactions setup.
- Adding an emWin widget that is not yet available in AppWizard.
- FSP configuration.
- Application design highlights.
- Using the General-Purpose Timer to drive a PWM backlight control signal.
- Importing, loading, and running the project.



Required Resources

Development tools and software

- e² studio v2025-04.1
 e² studio | Renesas
- Renesas Flexible Software Package (FSP) v6.0.0
 RA Flexible Software Package (FSP) | Renesas
- AppWizard V1.56_6.48 <u>SEGGER emWin GUI Library for Renesas RA Products | Renesas</u> Note: The version emWin in FSP must match the emWin version in the Segger AppWizard. There is a limitation of our FSP versioning system; for example, the emWin version 6.44.2 in FSP is equivalent to 6.44b in the AppWizard V1.52_6.44b, and so on.

Hardware

 Renesas EK-RA6M3G kit (RA6M3 MCU Group) (<u>https://www.renesas.com/us/en/products/software-tools/boards-and-kits/eval-kits/ek-ra6m3g.html</u>)

Reference Manuals

- RA Flexible Software Package Documentation Release v6.0.0
- AppWizard User Guide & Reference Manual Version 1.56
- emWin User Guide & Reference Manual Version 6.48
- Renesas RA6M3 Group User's Manual Rev.1.20
- EK-RA6M3G-v1.0 Schematics



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1. Board Setup

The EK-RA6M3G kit contains a few switch settings that must be configured before running the application associated with this application note. In addition to these switch settings, the boards also contain a USB debug port and connectors to access the J-Link[®] programming interface.

Table 1.	Switch	Settings	for	EK-RA6M3G

Switch	Setting
J8	Jumper on pins 1-2
J9	Open



Figure 2. J8 and J9 on EK-RA6M3

The EK-RA6M3G kit consists of two boards: the EK-RA6M3 board featuring the RA6M3 MCU with an onchip Graphics LCD Controller and a Graphics Expansion Board featuring a 4.3-inch 480 x 272-pixel TFT color LCD with capacitive touch overlay. The GPIO port pin driving the backlight controller is capable of PWM output using a timer peripheral in the MCU. As a result, the intensity of the LED backlight can be adjusted by the RA6M3 MCU.



Figure 3. EK-RA6M3G Kit



2. Application Overview

One of the key goals of the provided graphics application is to demonstrate how to build applications that require complex HMI screens using SEGGER AppWizard and emWin library. The following list highlights all the key features of the graphics application:

- Complex HMI design using AppWizard.
- Multi-threaded applications using FreeRTOS

 Semaphore object.
- GLCDC configuration
 - Framebuffer configuration.
 - TCON configuration.
- Touch Panel, I2C touch controller driver ft5x06.
 - External IRQ mapping is required.

There are many ways to solve the same problem in any software design. The solution given in this application note is one approach.

2.1 RA6M3 MCU Peripherals Used by the Graphics Application

The graphics application is complex, and it uses the Renesas RA6M3 MCU. This MCU is built around an Arm[®] Cortex[®]-M4 device. Developing complex microcontroller-based applications is usually a multi-step process:

- 1. The first step usually involves gathering the application requirements and performing a high-level system design that maps the requirements onto the set of hardware components. The components necessary to fulfill those requirements include the target MCU used in the design, the tool chains required to build/debug the applications, and so forth.
- 2. The next step usually determines which on-board peripherals of the target MCU are used. In this step, it is often necessary to spend considerable time understanding the onboard peripherals' register map and writing the lower-level driver code necessary to expose the peripheral to the upper-level application code. Most of this work has already been done in the FSP, considerably streamlining application development.
- 3. Besides the on-board peripherals of the target MCU, the design often encompasses external hardware and how it is controlled. For example, the EK-RA6M3G has a Graphics Expansion board, which is controlled directly by the on-chip Graphics LCD Controller (GLCDC) of the RA6M3 MCU.
- 4. The last step usually details how an application will be structured on top of the selected hardware to accomplish the initial requirements.

The graphics application requirements were first mapped to the onboard peripherals of the EK-RA6M3G kit. Figure 4 shows all the internal hardware peripherals used by the graphics application. This application note describes how each of these peripherals is configured using the FSP and the considerations that were used for each peripheral as the application is being developed.





Figure 4. RA6M3 MCU Peripherals Used in the Graphics Application

2.2 Human-Machine Interface (HMI)

In many HMI applications, the most daunting task may be the GUI (Graphical User Interface) itself. In applications requiring a graphical HMI, it is generally considered best practice to separate the business logic from the presentation. This abstracts the GUI from making decisions on what to display. Instead, it is now only concerned about how to display it. It relies on external logic to tell it what to display and when to display it.

Once you have gathered the requirements, achieved a top-level design, and identified the hardware necessary to implement that design, it is often beneficial to construct a GUI to help quickly communicate the look and feel of the system to others. This is where the SEGGER AppWizard comes into play.

The FSP natively supports the use of AppWizard and emWin library from SEGGER. You may choose to use emWin primitive calls directly in your application or choose to use the AppWizard to design your screens. AppWizard is a stand-alone tool that provides a point-and-click environment for generating all the screens necessary for your embedded application. Once designed, the tool outputs .c and .h files, which you then include in your application. All the application screens in the graphics application were built using the AppWizard.



2.3 Graphics Application Panels

The graphics application consists of two graphical panels: a **Weather Panel** and a **Logging Panel**. In this application, we build separate static display designs for these two panels. The screen resolution on the EK-RA6M3G kit is 480 x 272 pixels.





Weather Panel This is the first screen that appears on the kit when booting up. It shows Weather forecasts by selecting days or increasing/decreasing Temperatures.

Logging Panel This panel shows events that occur in the Weather Panel and adjusts the LCD backlight or text color and background color of the Logging Editor.

3. AppWizard Overview

This section provides an overview of how graphical screens are designed and integrated into an FSP application using the AppWizard and emWin library. It is not meant to replace the AppWizard or emWin documentation. When designing graphical interfaces for the Renesas FSP platform, you are encouraged to refer to the documentation for the AppWizard and emWin library.

The AppWizard presents a graphical point-and-click environment that allows you to quickly create all the screens needed for your embedded application. You can specify the screen resolution, color depth, and various other parameters such that what you see in the AppWizard that is running on your PC is what you will get on your embedded screens.

The AppWizard comes as a standard with some fonts and basic graphics for interfaces such as image, text, button, rotary, slider, and so forth. During your screen creation phase, you may provide the AppWizard with your own external images and font files to make your displays as fancy as needed.



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Figure 6. Screenshot of the Weather Panel being designed in the AppWizard

The organization of the AppWizard is straightforward. The top center window, known as the **Editor** window, contains the screen being designed. In the upper left corner, you will find the **Add objects** window. This window shows the supported window objects in the AppWizard. It allows you to click on the object icons and drag and place them in the Editor window. On the center-left is the **Hierarchic tree** window. The order in which you add items in the same level/parent determines the order in which they are drawn in the final screens, so some planning is necessary. However, you still can change the order by using drag and drop or the **Move Up** and **Move Down** buttons. As is the case with most graphical design environments, screens are laid out in a hierarchy where the main window is usually the parent, and all graphical objects contained in the window are children of that parent. The **Properties** window on the right side displays properties associated with a selected object. You may select objects from the **Hierarchic tree** window or from the **Editor** window.

The bottom left of the AppWizard screen contains **Quick Access Buttons** for managing resources such as Texts, Fonts, Images, Animations and Variables that you use to create and interact with the screens. AppWizard supports multi-language designs as well.

The key to making any graphical design interactive is to associate events like button touches with the event handling code that implements the appropriate functionality. The **Interactions** window at the bottom center makes it easy for you to define the application's behavior regarding certain actions. These interactions can be done without any extra code, but AppWizard allows you to add your code to handle these actions and respond to GUI events.



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x :	1	-		ID_BUTTON_SUN	RELEASED	SETVIS	ID_IMAGE_SUN_PRESSED	Toggle visibility
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Figure 7. AppWizard Interactions Window



3.1 Create a New Project Using the AppWizard

The **Create New Project** dialog box is shown in Figure 8. This dialog box is where you specify the projectspecific information, such as the basic display settings, as well as the path information for where AppWizard locates the files that result from the **Export & Save** process. The AppWizard also generates a simulation project in the folder \Simulation located in the project folder.

When you perform **Export & Save**, the AppWizard creates .c and .h files that contain all the information necessary to render the screens you built with AppWizard on the LCD in your embedded application. The **Project Path** is where you specify the default output directory for the Source, Header, and Resource files.

Project Project path: \Graphics_App_EK_RA6M	3G\AppWizard	Browse
Project name: AppWizard		
BSP		
Selected BSP: RA6M3G_E2S		Select BSP
Color scheme and display options		
Display size x:	480	\$
Display size y:		\$
Color format:	16 Bit, GUICC_M565	
Enable Multibuffering:	✓	
Text		
Show text from SD-card:		
Show missing characters:		
Enable bi-directional text:		
Enable Thai support:		
Focus		
Enable focus support:		
Select focus color:		
Set focus radius:	0	\$
Set focus width:		\$
Animations		
Minimum time per frame:	40	\$
Scroller	_	
Enable scroller support:		
Simulation		
Enable simulation:	⊻	
Stay alive loop		
Generate loop in MainTask():		
Use of static memory devices Enable use of static memory devices:	•	
Run script		
Script to be executed after export:		

Figure 8. Create a New Project Dialog Box



It is a good practice to save the Source, Header, and Resource files relative to the e² studio location. This makes it easy to move projects from one location to another or from one PC to another. In the case of the graphics application, you can see that all the directories are located under the AppWizard folder in the project directory created by e² studio. We recommend creating the e² studio project first, then creating the AppWizard folder as an e² studio source folder before creating an AppWizard project named AppWizard under the e² studio project folder.

After generating the AppWizard, you should exclude the Simulation and Target folders from Build before building the e² studio project. All the necessary library and header files for the target board are generated after you finish adding the emWin stack to your e² studio project.



Figure 9. AppWizard Project File View in the Graphics Application Folder

Go to **Project > Properties > C/C++ Build > Settings > GNU ARM Cross C Compiler > Includes** to add the newly created AppWizard folder and its subfolders to the e^2 studio project; include the path as shown in Figure 10.



🔁 Add dire	ctory path			×
Directory:				
\${workspa	ce_loc:/\${ProjNa	me}/AppWizard}		
Add sub	directories			
	OK	Cancel	Workspace	File system

Figure 10. Adding the AppWizard Folder to the e² studio Project Includes Path

3.2 Design Weather Panel Buttons Using AppWizard

The AppWizard User Manual and Quick Start Guide cover basic designs. The **Weather Panel** buttons, on the other hand, are more complex and are the target of this application note. These buttons are grouped in a Window widget that includes multiple objects. For example, the window ID_WINDOW_SUN consists of:

- ID_WINDOW_SUN
 - Window widget. The placeholder to group the other widgets.
- ID_IMAGE_SUN_PRESSED
 - Image widget. Visible when the ID_BUTTON_SUN is pressed, invisible when the ID_BUTTON_SUN is released. Set bitmap using bottom_button_trans_pressed.png.



Figure 11. ID_IMAGE_SUN_PRESSED Bitmap Setting

- ID_IMAGE_SUN
 - Image widget. Invisible when the ID_BUTTON_SUN is pressed, visible when the ID_BUTTON_SUN is released. Set bitmap using bottom_button_trans.png.



Figure 12. ID_IMAGE_SUN Bitmap Setting



• ID_IMAGE_SUNNY_SUN

- Image widget. Sunny icon. Set bitmap using icon_sunny.png.



Figure 13. ID_IMAGE_SUNNY_SUN Bitmap Setting

ID_TEXT_SUN

- Text widget. The "SUN" text.

- ID_TEXT_SUN_RANGE
 - Text widget. Shows temperature range.
- ID_BUTTON_SUN
 - Button widget. A transparent button without a bitmap image is placed on top of the other widgets.
 Some AppWizard interaction setups must be in place to create button-pressed/release impressions.



Figure 14. Design of the SUN Button Group

3.3 Setup AppWizard Interactions

Set the following interaction for the ID_BUTTON_SUN to create the button pressed/released as mentioned earlier in the Weather Panel Button Design section:

- The ID_IMAGE_SUN_PRESSED widget is invisible, toggling from invisible to visible when the transparent ID_BUTTON_SUN is pressed.
- The ID_IMAGE_SUN widget is visible, toggling from visible to invisible when the transparent ID_BUTTON_SUN is released.



Set interactio	ion parameters X
Set visibility:	Toggle
Slot:	D_SCREEN_MAIN_ID_BUTTON_SUNWM_NOTIFICATION_CLICKED_ID_IMAGE_SUNAPPW_JOB_SETVIS
Code:	<u>E</u> dit code
	<u>O</u> k <u>C</u> ancel

Figure 15. ID_BUTTON_SUN Interaction When Clicked

ID_BUTTON_S		RELEASED RELEASED	SETVIS SETVIS	ID_IMAGE_SUN ID_IMAGE_SUN_PRES	SED	Toggle visibility Toggle visibility
🥑 Set interactio	on parameters					×
Set visibility: Slot:	Toggle▼	NWM_NOTIFI	CATION_RELEA	SED_ID_IMAGE_SUN	_PRESSEDAPP	W_JOB_SETVIS
Code:			Edit co	de		
					Ok	Cancel

Figure 16. ID_BUTTON_SUN Interaction When Released

3.4 Add emWin Widget to AppWizard Project

You may need to use an emWin widget that is not yet supported by the AppWizard or need to create one in your custom code. The AppWizard allows that capability via the emWin API calls.

The **Logging Panel** in this graphics application features a Logging dialog created by using the Multiline Text widget.

The steps to add an emWin widget to the AppWizard project are as follows:

- Create an emWin widget by using emWin APIs in the slot routine for the AppWizard screen in the CustomCode folder.
- Handle GUI events/messages if needed via slot routines in the file <ScreenID >Slots.c located in the \AppWizard\Source\CustomCode folder.
- Figure 17 shows the function that creates the Multiline Text widget by using MULTIEDIT_CreateEx API and other APIs.



Figure 17. Adding Multiline Text Widget to AppWizard Application by using emWin APIs



4. Understanding the Graphics Application

While the HMI is certainly a large part of understanding any HMI application, there are many other areas that you must understand while developing with the Renesas FSP applications. These include how the project is physically structured in e² studio, how threads and thread resources are added to the project, how threads communicate, the state machine design, and how state data is shared among cooperating threads, especially the emWin thread.

4.1 Source Code Layout

Prior to diving into the actual application code, it is best to understand the overall source code layout of an FSP project first. Renesas FSP applications generally consist of two different types of code: your code and auto-generated code. The auto-generated code can be further broken down into two sub-categories: code that is auto-generated by the FSP and code that is auto-generated by AppWizard.



Figure 18. Graphics Application Project Source File Layout



Figure 18 shows the source code layout for the EK-RA6M3G board. FSP auto-generated code is in the ra_gen folder, AppWizard auto-generated code is highlighted in the Generated folder, and the code you generated is in the CustomCode folder.

Your generated code /AppWizard/Source/CustomCode is mainly used to handle HMI events. Your code in the /src folder is related to MCU peripherals and other functionalities.

4.2 Application Block Diagram

As mentioned, the graphics application consists of two panels: the **Weather Panel** and the **Logging Panel**. The two application panels interface with the graphics framework through interaction such as touch events and data (variables) changes. It communicates with FSP and HAL drivers to send and receive touch sensing data, GPT PWM duty cycle, and RTC date and time.

The graphics framework includes the SEGGER AppWizard framework, emWin library, emWin RA port, and interfaces with several HAL drivers such as GLCDC, JPEG CODEC, and D/AVE 2D. Figure 19 shows the application diagram.



Figure 19. Application Block Diagram



4.3 Thread Overview

As mentioned in the introduction, the graphics application is a multi-threaded application that runs under FreeRTOS. There are two types of threads found in an FSP application: those created by you and those created automatically to support the operation of FSP. While it is obvious what threads you created, it is not always obvious what threads are created by FSP. The graphics application uses both user-created threads and FSP threads. Threads communicate through the emWin-type events using AppWizard and emWin APIs. The emWin thread processes data and touch events that are sent by the Touch thread and Timer thread. The FSP Configuration section details how to add your threads to your application. Figure 20 shows a high-level diagram of the threads and event flow in the graphics application. Notice the distinction between your threads and FSP threads.



Figure 20. Graphics Application Event Flow

4.3.1 emWin Thread

The emWin thread is an HMI thread that initializes various services and resources used by the graphics application. Once this initialization is complete, the emWin thread processes touch events and window messages. If any of these inputs result in a change to the system state, the emWin invokes the AppWizard Slot routines, which are the callback routines, resulting in changes to the graphical HMI. The flowchart in Figure 21 gives the high-level view of the emWin Thread.



Figure 21. High-Level View of the emWin Thread



4.3.2 Touch Thread

A separate touch thread is created to read the touch sensor data. The touch sensor IC signals an event, such as a user interaction on the LCD screen, by toggling a pin connected to the MCU. In response, the touch thread reads the information from the touch sensor IC registers. Figure 21 shows the flowchart of the touch thread.



Figure 22. Touch Thread Flowchart

5. FSP Configuration

One of the first things you must do when developing an FSP application is to configure the FSP. To properly configure the FSP, you must have detailed knowledge of both the software design that you will be implementing and the specific hardware it will be running on. For the hardware, this includes the types of peripherals to be used on the hardware, the pins they are mapped to, whether they are internal or external to the MCU, and so on. From the software perspective, you need to decide how many threads will be used, which threads need access to what hardware components, and what additional software objects, like semaphores and queues, each thread will require. Once you have this information, you will be ready to successfully configure the FSP for your specific application needs.

In the graphics application, the FSP configuration is stored in a file named configuration.xml. Doubleclicking on this file brings up the **RA Configuration** tab for the project.



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 > 2 ra > 2 ra_gen > 2 rsc > 2 Debug > 2 ra_cfg > 2 script > configuration.xml > Graphics_App_EK_RA6M3G Debug.jlink > M Graphics_App_EK_RA6M3G Debug.launch > R7FA6M3AH3CFC.pincfg > ra_cfg.txt > RA6M3G-EK.pincfg 	> 🔊 Includes	
 > Pagen <	> 🔁 AppWizard	
 Src Debug ra_cfg ra_crg configuration.xml Graphics_App_EK_RA6M3G Debug.jlink Graphics_App_EK_RA6M3G Debug.launch R7FA6M3AH3CFC.pincfg ra_cfg.txt RA6M3G-EK.pincfg 	> 😂 ra	
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 > > configuration.xml > Graphics_App_EK_RA6M3G Debug.jlink > @ Graphics_App_EK_RA6M3G Debug.launch > @ R7FA6M3AH3CFC.pincfg > @ RA6M3G-EK.pincfg 	> 🔁 src	
 Configuration.xml Graphics_App_EK_RA6M3G Debug.jlink Graphics_App_EK_RA6M3G Debug.launch R7FA6M3AH3CFC.pincfg ra_cfg.txt RA6M3G-EK.pincfg 	> 🔁 Debug	
 configuration.xml Graphics_App_EK_RA6M3G Debug.jlink Graphics_App_EK_RA6M3G Debug.launch R7FA6M3AH3CFC.pincfg ra_cfg.txt RA6M3G-EK.pincfg 	> 🔁 ra_cfg	
 Graphics_App_EK_RA6M3G Debug.jlink Graphics_App_EK_RA6M3G Debug.launch R7FA6M3AH3CFC.pincfg ra_cfg.txt RA6M3G-EK.pincfg 	> 🔁 script	
Graphics_App_EK_RA6M3G Debug.launch R7FA6M3AH3CFC.pincfg ra_cfg.txt RA6M3G-EK.pincfg	🔅 configuration.xml	
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☐ ra_cfg.txt ☐ RA6M3G-EK.pincfg	Graphics_App_EK_RA6M	13G Debug.launch
RA6M3G-EK.pincfg	R7FA6M3AH3CFC.pincfg	9
	📄 ra_cfg.txt	
> 🕜 Developer Assistance		
	> ⑦ Developer Assistance	



When a project is built from scratch, this configuration tab is where you will perform the initial configuration of the FSP. As you can see in Figure 24, the RA Configuration pane contains a **Summary** tab highlighting the items you may configure along with a scrolling window that lists all the software components currently selected for this project. Below this scrolling window are tabs that allow you to tailor the FSP to the needs of your specific application.

For the purposes of this application note, we will highlight a few of the details of the FSP configuration, such as SEGGER emWin, the r_glcdc driver, the touch controller, and the PWM timer, as they pertain to the graphics application. For additional details, refer to the Renesas Flexible Software Package (FSP) User's Manual on how to configure the FSP.

When you have configured the project appropriately, click the **Generate Project Content**, the green arrow button above the summary screen, to build all the auto-generated files necessary to implement the components you defined.



ummary			Generate Project C	Conte
Project Summar	у			
Board:	EK-RA6M3G		RENESAS	
Board: Device:	R7FA6M3AH3CFC			
Core:	CM4			
Core. Toolchain:	GCC ARM Embedded			
Toolchain Version:				
FSP Version:	6.0.0			
Project Type:	Flat			
Location:		120		
Location:	C:/Graphics_App_EK_RA6	13G 🏧		
Selected software c	omponents			
RA6M3G-EK Boar	rd Support Files	v6.0.0		
Board support pac	kage for R7FA6M3AH3CFC	v6.0.0		
Board support pac	kage for RA6M3	v6.0.0		
Board support pac	kage for RA6M3 - FSP Data	v6.0.0		
Board support pac	kage for RA6M3 - Events	v6.0.0		
Board support pac	kage for RA6M3 - Linker	v6.0.0		
FreeRTOS		v11.1.0+fsp.6.0.0		
FreeRTOS - Memo	ory Management - Heap 4	v11.1.0+fsp.6.0.0		
TES DAVE 2D Dra	awing Engine	v3.8.0+fsp.6.0.0		
SEGGER emWin	Library	v6.48.0+fsp.6.0.0		
Arm CMSIS Versio	on 6 - Core (M)	v6.1.0+fsp.6.0.0		
	ckage Common Files	v6.0.0		
TES D/AVE 2D Po	-	v6.0.0		
Data Transfer Con	ntroller	v6.0.0		
Graphics LCD Cor	ntroller	v6.0.0		
General PWM Tim	ner	v6.0.0		
External Interrupt		v6.0.0		
I2C Master Interfa	се	v6.0.0		
I/O Port		v6.0.0		
JPEG Codec		v6.0.0		
Real Time Clock		v6.0.0		
SEGGER emWin	RA Port	v6.0.0		

Figure 24. Summary of the Graphics Application Configuration

5.1 Components Tab

Even though the **Components** tab is the last tab showing, it is one of the first things you should configure. Selecting components first makes them available in subsequent operations, such as mapping hardware resources to specific threads in the **Stacks** tab. One of the advantages of FSP is that it will only compile in the components you choose, thereby reducing the size of your overall application. As shown in Figure 25, components are broken down into several categories.



Components Configuration			Generate Project Content
□ 1		Group by: Vendor v Filter: All	✓ Search
Component	Version	Description	Variant
🗸 🟪 Arm			
> 救 Abstractions			
> 救 CMSIS			
> 救 Ethos-U			
> 救 mbed			
> 救 Mbed			
> 救 PSA			
AWS			
> 救 Abstractions			
> 救 c_sdk			
> 救 FreeRTOS			
> 救 FreeRTOS_Plus			
🗸 💸 Heaps			
🗸 💡 FreeRTOS			
📃 heap_1	11.1.0+fsp.6.0.0	FreeRTOS - Memory Management - Heap 1	
heap_2	11.1.0+fsp.6.0.0	FreeRTOS - Memory Management - Heap 2	
heap_3	11.1.0+fsp.6.0.0	FreeRTOS - Memory Management - Heap 3	
🔽 heap_4	11.1.0+fsp.6.0.0	FreeRTOS - Memory Management - Heap 4	
heap_5	11.1.0+fsp.6.0.0	FreeRTOS - Memory Management - Heap 5	
> 救 Libraries			
🗸 💸 RTOS			
> 💡 FreeRTOS			
- 🏪 Google			
🔒 品 Intel			
🛛 🏪 Linaro			
			>

Figure 25. Components Tab Categories

You may expand any of the categories by clicking the arrow to the left of the category name.

The following table highlights the selections used for the graphics application.



Category	Component	Version	Description
BSP	ra6m3g_ek	6.0.0	RA6M3G-EK Board Support Package Files
CMSIS	CoreM	6.1.0+fsp.6.0.0	Arm CMSIS Version 6 - Core (M)
Common	fsp_common	6.0.0	Board Support Package Common Files
GUI	emWin	6.48+fsp6.0.0	SEGGER emWin Library
HAL	r_drw	6.0.0	TES D/AVE 2D Port
Drivers	r_dtc	6.0.0	Data Transfer Controller
	r_glcdc	6.0.0	Graphics LCD Controller
	r_icu	6.0.0	External Interrupt
	r_iic_master	6.0.0	I2C Master Interface
	r_ioport	6.0.0	I/O Port
	r_jpeg	6.0.0	JPEG Codec
	r_rtc	6.0.0	Real-Time Clock
Heaps	heap_4	11.1.0+fsp.6.0.0	FreeRTOS - Memory Management – Heap 4
Middleware	rm_emwin_port	6.0.0	SEGGER emWin RA Port
RTOS	FreeRTOS	11.1.0+fsp.6.0.0	FreeRTOS
TES	dave2d	3.8.0+fsp.6.0.0	TES DAVE 2D Drawing Engine

Table 2. Components Used in the Graphics Application

5.2 Stacks Tab

The Stacks tab is where you can add and configure the threads that the FSP automatically creates for your

application. You define a new thread by clicking the button and then entering a unique name for your new thread. Once you add a new thread, you must define the modules that the thread will use along with any thread objects that will be used by your thread.

As an example, if you click the **Threads** panel and then single-click on the **emWin Thread**, you should see something like the screen capture shown in Figure 26. This shows that the emWin thread requires multiple modules, such as the GLCDC driver, which is used to control the LCD screen on the graphics expansion board of the EK-RA6M3G kit.



Threads	🍓 New Thread ญ Remove 🛛 📄	emWin Thread Stacks		된 New S	tack > 🚔 Extend Stack > 💼 Ren
	AL/Common	SEGGER emWin			FreeRTOS Heap 4
	g_ioport I/O Port (r_ioport)				
	nWin Thread				
	SEGGER emWin	í			(i)
	FreeRTOS Heap 4		A		
	g_touch_irq External IRQ (r_icu)	🖑 SEGGER emWin RA P	ort (rm_emwin_port)		
+	g_i2c_touch I2C Master (r_iic_master)				
🗙 🎡 Tir	mer Thread	(i)			
	g_rtc_timer Realtime Clock (r_rtc)		A		
- +	g_timer_PWM Timer, General PWM (r_gpt)	🖑 g_display0 Graphics I			7
<	>	(r_glcdc)	CD	g_jpeg0 JPEG Codec (r_jpeg)	
Objects	🛃 New Object > 💼 Remove	í	(i)	(i)	
-			▲		
● g_i2c_	ich_semaphore Binary Semaphore _semaphore Binary Semaphore		U/AVE 2D (r_drw)		
🔵 g_tim	er_semaphore Binary Semaphore				
● g_tim	er_semaphore Binary Semaphore		(i)		
		ker Sections Stacks Compon		[
ummary I	BSP Clocks Pins Interrupts Event Links Lin s Console Properties X Smar	ker Sections Stacks Compon t Browser Smart Manua	ents	Search Fault Status	
Problems	BSP Clocks Pins Interrupts Event Links Lin s Console Properties X Smar		ents	Search Fault Status	
Problems	BSP Clocks Pins Interrupts Event Links Lin s Console Properties X Smar Thread	t Browser Smart Manua	ents	Search Fault Status	
ummary I Problem: mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar Thread Property	t Browser Smart Manua	ents	Search Fault Status	
ummary I Problem: mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar Thread Property V Common	t Browser Smart Manua	ents	Search Fault Status	
ummary I Problem: mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar hread Property • Common > General	t Browser Smart Manua	ents	Search Fault Status	
Immary I Problem mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar Thread Property Common S General Hooks	t Browser Smart Manua	ents	Search Fault Status	
Immary I Problem mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar hread Property Common S General Hooks S Stats	t Browser Smart Manua	ents	Search Fault Status	
Problems	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar hread Property Common > General > Hooks > Stats > Timers	t Browser Smart Manua	ents	Search Fault Status	
Problems	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar hread Property Common General Hooks Stats Stats Stats Optional Functions	t Browser Smart Manua	ents	Search Fault Status	
Immary I Problem mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar hread Property	t Browser Smart Manua Value	ents I Memory Debug	Search Fault Status	
mmary Problem nWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar Thread Property Common > General > Hooks > Stats > Timers > Optional Functions > RA > Logging	t Browser Smart Manua Value emWin_thre	ents I Memory Debug	Search Fault Status	
Immary I Problem mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar hread Property Common > General > Hooks > Stats > Timers > Optional Functions > RA > Logging V Thread Symbol Name	t Browser Smart Manua Value emWin_thre emWin_thre	ents I Memory Debug	Search Fault Status	
mmary I Problem mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar hread Property Common > General > Hooks > Stats > Timers > Optional Functions > RA > Logging V Thread Symbol Name Stack size (bytes)	t Browser Smart Manua Value emWin_thre emWin_thre 4096	ents I Memory Debug	Search Fault Status	
mmary I Problem mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar Thread Property Common > General > Hooks > Stats > Timers > Optional Functions > RA > Logging V Thread Symbol Name Stack size (bytes) Priority	t Browser Smart Manua Value emWin_thre emWin_thre 4096 10	ents I Memory Debug	Search Fault Status	
mmary I Problem mWin T	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar Thread Property	t Browser Smart Manua Value emWin_thre emWin Thre 4096 10 NULL	ents I Memory Debug	Search Fault Status	
ummary	BSP Clocks Pins Interrupts Event Links Lin s Console Properties × Smar Thread Property Common > General > Hooks > Stats > Timers > Optional Functions > RA > Logging V Thread Symbol Name Stack size (bytes) Priority	t Browser Smart Manua Value emWin_thre emWin_thre 4096 10	ents I Memory Debug	Search Fault Status	

Figure 26. emWin Thread Properties and Modules Used for the Graphics Application

You can add additional modules to any thread by clicking the ^{****} button. If you have chosen the appropriate components prior to adding modules to your threads, you should not receive any errors. As an example, Figure 27 shows you how to add a GPT timer to the Timer Thread. The timer is added by choosing (+) New Stack > Timers > Timer, General PWM (r_gpt)

If you have not preselected the appropriate component for a module that you select, the FSP automatically selects the component for you. If the FSP detects errors with the module addition, it prefaces the module with an error. You may examine the errors by hovering over the module name.



Immer Thread	acks Configuration			Generate Pro	Oject Cont	tent
Analog → Audio → Bootloader → Clock (r_rtc) 9 g_rtc_timer Realtime Clock (r_rtc) 9 g_rtc_timer Realtime Clock (r_rtc)	reads	Timer Thread Stacks	New Stack > =			ive
jects Imers Imers <td< td=""><td> HAL/Common g_ioport I/O Port (r_ioport) emWin Thread EGGER emWin FreeRTOS Heap 4 Touch Thread g_touch_irq External IRQ (r_ic g_i2c_touch I2C Master (r_irc.) Timer Thread g_rtc_timer Realtime Clock (r </td><td>Clock (r_rtc)</td><td></td><td>Analog Audio Bootloader CapTouch Connectivity DSP Graphics Input Monitoring Motor Networking Power RTOS Security Sensor</td><td>> > > > > > > > > > > > ></td><td></td></td<>	 HAL/Common g_ioport I/O Port (r_ioport) emWin Thread EGGER emWin FreeRTOS Heap 4 Touch Thread g_touch_irq External IRQ (r_ic g_i2c_touch I2C Master (r_irc.) Timer Thread g_rtc_timer Realtime Clock (r 	Clock (r_rtc)		Analog Audio Bootloader CapTouch Connectivity DSP Graphics Input Monitoring Motor Networking Power RTOS Security Sensor	> > > > > > > > > > > > >	
g_touch_semaphore Binary Semaphore Binar	iects			Timers		Port Output Enable for GPT (r_poeg)
	g_touch_semaphore Binary Semapl			Transfer	*	
a timer semaphore Rinan/ Semaph	2 · · · · ·		6	🖗 Search		
Timer, Concern and Capital Timer, Low-Power (r_aqt)	g_timer_semaphore Binary Semaph					Timer, General PWM (r_gpt)

Figure 27. Adding r_gpt driver

5.3 Thread Objects

FreeRTOS supports various objects such as mutexes, queues, semaphores, and timers. In the **Objects** window, you will see that there are three semaphore objects, **g_touch_semaphore**, **g_i2c_semaphore**, and **g_timer_semaphore**, created for this application.

You can allocate additional thread objects by clicking on the 🗐 button next to the **Objects** window. As you

can see in Figure 28, after clicking the button ¹ in the **Objects** window, you will be presented with a dropdown list that will allow you to add the standard thread objects supported by FreeRTOS.

Objects	New Object > 👔 Remove
 g_touch_semaphore Binary Semaphore g_i2c_semaphore Binary Semaphore g_timer_semaphore Binary Semaphore 	 Binary Semaphore Counting Semaphore Event Group Message Buffer Mutex
Summary BSP Clocks Pins Interrupts Event Links Link	er Sections Stac Timer

Figure 28. Objects window



Objects		된 New Object > 👔	Remove	
■ g_i2c ● g_tim	ch_semaphore Binary Semaphore semaphore Binary Semaphore er_semaphore Binary Semaphore			_
Summary	BSP Clocks Pins Interrupts Event	Links Linker Sections Stacks Co	omponents	
Problem	s Console Properties X	Smart Browser Smart	Manual ³ 4	
				1 8
g_touch_	semaphore Binary Semapho	ore	_	
Settings	Property	Value		
settings	✓ Object			
	Symbol	g_touch_semaphore		
	Memory Allocation	Static		

Figure 29. g_touch_semaphore Properties

5.4 Module Configuration

Once you have added a module to your project, you need to configure its properties. The properties are dependent on the module(s) that you have added. Use the **Properties** tab to configure them. The graphics application adds the r_glcdc driver module as part of the SEGGER emWin stack. This module is used to configure the GLCDC peripheral of the Renesas RA6M3 MCU.

5.4.1 GLCDC Configuration

As you can see in Figure 30, selecting the **g_display0 Graphics LCD** on the **g_glcdc** module under the **emWin Thread > Modules** tab brings up the associated properties under the **Properties** tab. The first thing you will notice is that it is a lengthy list of properties within the module group. The module group is where you configure the GLCDC controller. These properties can be a bit daunting at first but can be broken down. First, you will notice a few broad categories inside the module grouping.

- Name: The name given to this instance of the module **g_display0** by default.
- Interrupts: You set the Line Detect interrupt and other interrupts here.
- **Input:** This block of module properties defines the input to the graphics controller, most notably, the framebuffer name and the number of the framebuffers, the memory address where the frame buffer is located, and others.
- **Output:** This is the area where you define the output properties of the GLCD. This includes properties such as the total horizontal and video cycles, the active video cycles, both horizontal and vertical, front and back porch duration, and so on.
- **TCON:** You use these lines in conjunction with the **Pins** tab to map the Horizontal Sync (Hsync), Vertical Sync (Vsync), and Data Enable signals. You can specify the LCD panel clock divisor that divides the clock input into the GLCD. This divisor ratio currently ranges from 1/1 to 1/32.
- **Color Correction:** This is where you can add various levels of color correction, for example, brightness, contrast, and gamma, to your display. Color, contrast, and gamma correction of LCD screens are outside the scope of this application note, but this is the area where you would do that type of adjustment.



		Remove 📄 SI	EGGER emWin Stacks		Standard Sta	ack > 🚔 Extend Stack > 📓 Remove
-	L/Common g_ioport I/O Port (r_ioport)		SEGGER emWin			
	Win Thread SEGGER emWin		(i)			
49	FreeRTOS Heap 4			A		
#	ich Thread g_touch_irq External IRQ (r_icu		SEGGER emWin RA Por	t (rm_emwin_port)		
9 ∰ Tim	g_i2c_touch I2C Master (r_iic_r	master)	(i)			
	g_rtc_timer Realtime Clock (r_	rtc)	U			
	g_timer_PWM Timer, General I					1
			g_display0 Graphics LC (r_glcdc)	D	e 	
Objects	된 New Object >	🔊 Remove	0	<u>(</u>)	١	
g_i2c_s	h_semaphore Binary Semapho semaphore Binary Semaphore rr_semaphore Binary Semapho	e		U/AVE 2D (r_drw)	_	
				١	-	~
ımmary B	SP Clocks Pins Interrupts E	vent Links Linker S	ections Stacks Componen	ts		
Problems	Console Propertie	s × Smart Bro	owser Smart Manual	Memory Debug	Search Fault Status	
display	0 Graphics LCD (r_glcdc	:)				
	Property	Value				
ettings						

Figure 30. GLCD Properties Configuration using the Properties Tab

5.4.2 TCON Configuration

If you scroll down a little further in the **Properties** tab, you will see four TCON properties. One of these is associated with the **Panel clock division ratio**. This allows additional division of the pixel clock that is driven directly from the PLLOUT branch of the clock tree. The other three are associated with the LCD sync signals. These three signals can be confusing to new users, so how these signals map to the physical pins they are connected to is discussed here.

✓ TCON	
Hsync pin select	LCD_TCON0
Vsync pin select	LCD_TCON1
Data enable (DE) pin select	LCD_TCON2
Panel clock source	Internal clock (GLCDCLK)
Panel clock division ratio	1/24

Figure 31. TCON Configuration for EK-RA6M3G Kit

To provide flexibility, the GLCD controller of the RA6M3 MCU provides two-pin grouping options. Each option uses different pins on the MCU to drive the data lines connected to the LCD display. It is up to the hardware designer to pick the group of pins they want to use. Picking one or the other may free up MCU pins that are necessary for some other part of the hardware design.

If you look at the schematics for the EK-RA6M3G kit, you can see the pins header for the LCD board. You will also notice the three pins connected to the sync signals, which are highlighted in red. The data lines chosen by the hardware designer must match one of the two pin groupings available under the GLCD module.





Figure 32. EK-RA6M3G LCD-Specific Signals from the Schematics

The easiest way to understand this is to go to the **Pins** tab in the RA Configuration. You will see selections for **Ports**, **Peripherals**, and **Other Pins**, as shown in Figure 33. If you expand the **Peripherals** dialog, you will see all the various MCU peripherals that can be configured from this screen.

If you scroll down to the **Graphics: GLCDC** entry and click to expand it, you will see two options: **GLCD0 Pin Group Selection A** and **GLCD0 Pin Group Selection B**. For the EK-RA6M3G kit, the **GLCD0 Pin Group Selection A** was selected to drive the LCD display.

Notice that TCON0 is associated with the Port 1 Pin 02 (P102). On the schematic (P102), we see that it is connected to HSYNC, which is the horizontal synchronization pin for this LCD screen. Referring to Figure 31, we see that TCON0 has been selected to drive the HSYNC signal.



elect Pin Configuration		E)	xport to CSV file Con	figure Pin Driver Warnings
RA6M3G-EK.pincfg	✓ Manage configurations	[Generate data: g_bs	p_pin_cfg
in Selection $\models \boxdot \downarrow^a_Z$	Pin Configuration			😲 Cycle Pin Group
Type filter text	Name	Value	Link	
> V Ports > V Peripherals				
> 🗸 Other Pins				
	<			2

Figure 33. Pin Configuration Tab

If you look at all the LCD data lines, such as LCD_DATA_DATA00, and the pins they are connected to, they should match the pins they are connected to on the schematic. Clicking on the arrow to the right of the pin brings you directly to the associated **Pin Configuration** dialog, just as if you had selected the **Ports Group** and then the specific port and pin that you are interested in.



elect Pin Configuration		🔛 E	xport to CSV file ፤	Configure Pin Driver Wa	rnings		
RA6M3G-EK.pincfg	 Manage configuration 	Ons	Generate data:	g_bsp_pin_cfg			
in Selection	E 🕀 🖻 🖡	Pin Configuration				📢 Cycle Pin Group	,
Type filter text		Name	Value	Lock	Link		1
h at Conservativity (C)		Pin Group Selection	_A only				
> Connectivity:SCI		Operation Mode	Custom				
 Connectivity:SPI Connectivity:SSI 		✓ Input/Output			<>		
 Connectivity:SSI Connectivity:USBFS 		LCD_CLK	🛩 P101	- 6	4		
		LCD_DATA00	P106	- f	4		
		LCD_DATA01	P107		4		
		LCD_DATA02	🛩 P600	- 6	4		
> 🗸 InputIRQ		LCD_DATA03	🛩 P601		4		
> Input:KINT		LCD_DATA04	✓ P602		4		
 Graphics:GLCDC 		LCD_DATA05	🛩 P610	l di	4		
✓ GLCDC0		LCD_DATA06	P609	a	4		
> Graphics:PDC		LCD_DATA07	P608		4		
> V Storage:QSPI		LCD_DATA08	🛩 P115	1	4		
> Storage:SDHI		LCD_DATA09	🛩 P114		4		
> System:BUS		LCD_DATA10	🛩 P113		4		
> V System:CGC		LCD_DATA11	✓ P112	a	4		
> System:DEBUG		LCD_DATA12	🛩 P111		4		
> System:TRACE		LCD_DATA13	🛩 P301	- f	4		
> Timer:AGT		LCD_DATA14	✓ P302		4		
> Timer:GPT Timer:OPS		LCD_DATA15	🛩 P303		4		
> Timer:POEG		LCD_DATA16	None		⇒		
> Timer:RTC		LCD_DATA17	None		⇒		
		LCD_DATA18	None	170	⇒		
 Other Pins AVCC0 		LCD_DATA19	None		⇒		
		LCD_DATA20	None				
 AVCCUSBHS AVSS0 		LCD_DATA21	None	- mile	\Rightarrow		
 AVSSU AVSSUSBHS 		LCD_DATA22	None	n n n	⇒		
 AVSSUSBHS PVSSUSBHS 		LCD_DATA23	None	- mi	4		
RES		LCD_TCON0	✓ P102		4		
VSBDM		LCD_TCON1	🛩 P103		4		
✓ USBDM ✓ USBDP		LCD_TCON2	🛩 P104		4		
VSBHSDM		LCD_TCON3	None				
✓ USBHSDM ✓ USBHSDP		LCD_EXTCLK	None		4		
 ✓ USBHSDP ✓ USBHSRREF 							-
VBAT		Module name: GLCDC0					
VCC	~						
Function Pin Number	*						

Figure 34. LCD Pin Configuration Using Configurator

For example, clicking on this arrow to the right of the LCD_TCON0 pin should bring you to the **Pin Selection Screen** that looks like Figure 35. Notice that the pin is appropriately set to the **Peripheral mode**. At the time of writing this application note, the pins default to no pull-up, high drive capacity, and CMOS output type. Clicking on the arrow button to the right of this screen brings you back to the associated peripheral screen.

elect Pin Configuration			🔛 Export to CSV file	Configure Pin Driver Warnings	
RA6M3G-EK.pincfg	✓ Mana	age configurations	Generate data:	g_bsp_pin_cfg	
in Selection	⊫ 🕀 🗗 ↓ª₂	Pin Configuration			🕐 Cycle Pin Group
Type filter text		Name Symbolic Name	Value	Link	
Ports ^	Comment				
> 🛩 P0		Mode	Peripheral mode	1	
✓ ✓ P1		Pull up	None		
 ✓ P100 ✓ P101 ✓ P102 		Drive Capacity	High		
		Output type	CMOS		
✓ P102		✓ Input/Output			
✓ P103		P102	GLCDC0_LCD_TCON0	↓	
P105					
🗸 P106					
🗸 P107		Module name: P102			
P108		Port Capabilities: ADC0: ADT	RG		
🗸 P109		AGT0: AGT0			
🛩 P110		BUS0: D2_D			
🗸 P111		CANO: CRX GLCDC0: LC			
✓ P112		GPT2: GTIO			
✓ P113		KINTO: KRN			
✓ P114 ✓ P115		OPS0: GTO			
> V P2	~	SCI0: SCK SPI0: RSPC	/		
		SPIU: KSPC	`		

Figure 35. LCD_TCON0 Settings in Pin Selection window



5.4.3 Touch Controller Configuration

The touch event on the LCD screen is sensed by the RA6M3 MCU external IRQ pin, and the touch sensor is read via the I2C master.

As shown in Figure 38, the interrupt signal of the Touch Controller on the LCD screen connected to P206 on header J1 of the EK-RA6M3 board, which is MCU IRQ channel 0. The r_icu and r_iic_master drivers are added to a Touch Thread to handle the IRQ channel 0 and I2C Master Channel 2, respectively.

Stacks	Configuration						Generate Project Conten
Threads	New Thread 🔒 Remove 🛛 📃	Touch Thread Stacks			된 N	lew Stack > 🚢 E	xtend Stack > 👔 Remove
 ✓ @ er ✓ @ Tc Objects 	AL/Common g_ioport I/O Port (r_ioport) mWin Thread SEGGER emWin FreeRTOS Heap 4 ouch Thread g_touch ing External IRO (r_icit) New Object > Remove uch_semaphore Binary Semaphore _semaphore Binary Semaphore	g_touch_irq External IRQ (r_icu)	 g_transfer(2 TXI (Transmit	g_transf	er1 Transfer IC2 RXI (Receive I)	
Problem	BSP Clocks Pins Interrupts Event Links ns Console Properties S irq External IRQ (r_icu)	Linker Sections Stacks Componer mart Browser Smart Manual	Memory	Debug	Search	Fault Status	
touch							
Settings	Property			Value			
API Info	> Common						
	 Module g_touch_irq External IRQ (r_i 	cu)					
	Name			g_touch_irq			
	Channel			0 Falling			
	Trigger Digital Filtering			Enabled			
	Filter Source						
		nly valid when Digital Filtering is En	abled)	PCLK filter			
	Callback	ing valid when Digital Filtering is En	ableu)	PCLK / 64 touch_irg_cb			
	Pin Interrupt Priority			Priority 5			
				Filonty 5			
	N Ding						
	V Pins IRQ00			None			

Figure 36. External Interrupt Configuration



conny	uration				Generate Project Conte
Threads	🔄 New Thread	Touch Thread Stacks			💿 New Stack > 🚣 Extend Stack > 🔞 Remov
 emWin Three SEGGER FreeRTO Touch Three g_touch, g_i2c_to Gimer Three 	t I/O Port (r_joport) ead emWin S Heap 4 ad irq External IRQ (r_icu) uch I2C Master (r_iic_master)	G_touch_irq External IRQ (r_icu)	 g_i2c_touch i2C Master (r) g_transfer0 Transfer (r_dtc) IIC2 TXI (Transmit data empty) g_transfer0 TX 	iic_master)	
-	PWM Timer, General PWM (r_gpt)				
Objects	🐑 New Object > 🔬 Remove				
g_timer_semaţ	phore Binary Semaphore				
mmary BSP Cloo Problems C	ohore Binary Semaphore cks Pins Interrupts Event Links Linker Sectio Console Properties X Smart Browse Master (r_iic_master)		ny Debug Search	Fault Status	
Immary BSP Cloc Problems C i2c_touch I2C	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master)			Fault Status	
mmary BSP Cloc Problems C i2c_touch I2C ettings Propert	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master)		ory Debug Search Value	Fault Status	
mmary BSP Cloc Problems C i2c_touch I2C ettings Propert Dulato > Com	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) ty mon			Fault Status	
mmary BSP Cloc Problems C i2c_touch I2C Propert Pl Info > Com > Moo	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master)		Value	Fault Status	
mmary BSP Cloc Problems C i2c_touch I2C ettings Propert Pl Info ∨ Moc N	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) ty mon dule g_i2c_touch I2C Master (r_iic_master)			Fault Status	
mmary BSP Cloc Problems CC i2c_touch I2C Propert Pl Info > Com > Com > Com > Com > Com	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) ty Immon Jule g_i2c_touch I2C Master (r_iic_master) Name		Value g_i2c_touch	Fault Status	
mmary BSP Cloc Problems C ii2c_touch I2C ettings Propert PI Info > Con > Moc N C R	cks Pins Interrupts Event Links Linker Section console Properties Smart Browse Master (r_iic_master) ty immon Jule g_i2c_touch I2C Master (r_iic_master) Name		Value g_j2c_touch 2	Fault Status	
mmary BSP Cloc Problems C iic_touch I2C Propert PI Info → Moc N C C C C C C C C C C C C C	Cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) The section ty Immon Immon Jule g_i2c_touch I2C Master (r_iic_master) Iame Iame Iame Iate Immon		Value g_j2c_touch 2 Fast-mode	Fault Status	
mmary BSP Cloc Problems C i2c_touch I2C Propert Propert Pl Info > Com >	cks Pins Interrupts Event Links Linker Section console Properties X Smart Browse Master (r_iic_master) transport transport ty mon dule g_i2c_touch I2C Master (r_iic_master) tame transport transport tate tate tate tostom Rate (bps) tate tase		Value g_j2c_touch 2 Fast-mode 0	Fault Status	
mmary BSP Cloc Problems C i2c_touch 12C Propert Pr Info > Com > Moc N C R C R C R C R	cks Pins Interrupts Event Links Linker Section console Properties Smart Browse Master (r_iic_master) Smart Browse hmon Jule g_i2c_touch I2C Master (r_iic_master) Jame Channel Late Lustom Rate (bps) Lise Time (ns) State (bps)		Value g_j2c_touch 2 Fast-mode 0 120	Fault Status	
mmary BSP Cloc Problems C iiii Cuth I2C ettings Propert PI Info > Con R CC R R R C C	cks Pins Interrupts Event Links Linker Section Console Properties Smart Browse Master (r_iic_master) Smart Browse Master (r_iic_master) Smart Browse ty Interrupts Interrupts tule 2/2c_touch 12C Master (r_iic_master) Name Interrupts Late Ustom Rate (bps) Lise Time (ns) Interrupts		Value g_j2c_touch 2 Fast-mode 0 120 120	Fault Status	
mmary BSP Cloc Problems C ij2c_touch 12C ettings Propert PI Info ∨ Moc R R F F S S S	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) Ity Ity Monon Jule g_i2c_touch I2C Master (r_iic_master) Jame Jule g_i2c_touch I2C Master (r_iic_master) Jame Jule g_i2c_touch I2C Master (r_iic_master) Jate Jule g_i2c_touch I2C Master (r_iic_master) Jate Jule g_i2C_touch I2C Master (r_iic_master) Jate Jule g_i2C_touch I2C Master (r_iic_master) Jule g_i2C_touch I2C Master (r_iic_master) Jule g_i2C_touch I2C Master (r_iic_master) Jate Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master) Jule G_i2C_touch I2C Master (r_iic_master)		Value g_j2c_touch 2 Fast-mode 0 120 120 50	Fault Status	
mmary BSP Cloc Problems C iiii Cuth I2C Propertings > Com > Moc N N C R C C R C C R C C A	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) Smart Browse Smart Browse Inmon Interrupts Linker Section Subscription Interrupts Linker Section State Interrupts Linker Section State Interrupts Linker Section State State State		Value g_j2c_touch 2 Fast-mode 0 120 120 50 0 x38	Fault Status	
mmary BSP Cloc Problems C i2c_touch 12C ettings Propert Pl Info > Con N C R C R C C R R C C R C C C C C C C C C C C C C	cks Pins Interrupts Event Links Linker Section console Properties Smart Browse Master (r_iic_master) Smart Browse ty Interrupts Interrupts tule _j2_ctouch 12C Master (r_iic_master) Name Interrupts Interrupts Example Sustom Rate (bps) Itse Time (ns) all Time (ns) July Cycle (%) Jlave Address Mode		Value g_j2c_touch 2 Fast-mode 0 120 120 120 50 0x38 7-Bit	Fault Status	
mmary BSP Cloc Problems C ii2c_touch I2C ettings Propert Pl Info > Con > Con C R C C R C C R C C R C C C C C C C C C C C C C	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) Image: Console Smart Browse Master (r_iic_master) Image: Console Image: Console ty Image: Console Image: Console Image: Console tate Image: Console Image: Console Imag		Value g_j2c_touch 2 Fast-mode 0 120 120 120 50 0 x38 7-Bit Short Mode	Fault Status	
mmary BSP Cloc Problems C i2c_touch 12C Propert > Com > Moc N C R C C R C C R C C R C C R C C C C C C C C C C C C C	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) Smart Browse Master (r_iic_master) ty Interrupts Event Links Linker Section dule g_i2c_touch I2C Master (r_iic_master) Interrupts Event Master Interrupts dame Interrupts Event Master Interrupts Interrupts Ustom Rate (bps) Ise Time (ns) Inter (ns) Interrupts July Cycle (%) Iave Address Interrupts Interrupts Inneout Mode Timeout Mode Interrupts Interrupts		Value g_j2c_touch 2 Fast-mode 0 120 120 50 0x38 7-Bit Short Mode Enabled	Fault Status	
mmary BSP Cloc Problems C i2c_touch 12C Propert > Com > Moc N C R C C R C C R C C R C C R C C C C C C C C C C C C C	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) Smart Browse Master (r_iic_master) Immon Jule g_i2c_touch I2C Master (r_iic_master) Jule g_i2c_touch I2C Master (r_iic_master) Jame Immon Immon Late Immon Immon July Cycle (%) Immon Immon July Cycle (%) Immon Immon Late Immon Immon July Cycle (%) Immon Immon Liave Address Mode Immon Liave Address Immon Immon Liave Address		Value g_j2c_touch 2 Fast-mode 0 120 120 50 0x38 7-Bit Short Mode Enabled touch_j2c_callback	Fault Status	
mmary BSP Cloc Problems C iiii Cutch I2C ettings Propert Pl Info M C R C R R C C R R C C R R C C C R R C C C C C C C C C C C C C	cks Pins Interrupts Event Links Linker Section Console Properties X Smart Browse Master (r_iic_master) Smart Browse Master (r_iic_master) Immon Jule g_i2c_touch I2C Master (r_iic_master) Jule g_i2c_touch I2C Master (r_iic_master) Jame Immon Immon Late Immon Immon July Cycle (%) Immon Immon July Cycle (%) Immon Immon Late Immon Immon July Cycle (%) Immon Immon Liave Address Mode Immon Liave Address Immon Immon Liave Address		Value g_j2c_touch 2 Fast-mode 0 120 120 50 0x38 7-Bit Short Mode Enabled touch_j2c_callback	Fault Status	

Figure 37. I2C Master Driver Configuration



Figure 38. Touch Controller Signals



The EK-RA6M3G User Manual recommends the touch interrupt input must have the internal pull-up feature enabled. Use Ports Configuration for this setting instead of Peripherals Configuration.

Select Pin Configuration			Export to CSV file	Configure Pin Driver Warnings	
RA6M3G-EK.pincfg	✓ Manage	2	Generate data:	g_bsp_pin_cfg	
Pin Selection	${\mathbb H} \boxdot {\mathbb H}^{a}_{{\mathbb Z}}$	Pin Configuration			😍 Cycle Pin Group
✓ ✓ P2 P200 P201 ✓ P202 ✓ P203 ✓ P204 ✓ P205 ✓ P206 P207 ✓ ✓ P208 ✓ P209 ✓ P210 ✓ P211 ✓ P213 ✓ P213 ✓ P214 > ✓		Name Symbolic Name Comment Mode Pull up IRQ Drive Capacity Output type ✓ Input/Output P206 Module name: P206 Port Capabilities: BUS0: WAI CTSU0: TSI ETHERC0: ETHERC0: ETHERC0: IIC1: SDA IRQ0: IRQ0 OPS0: GTII SCI4: RXD SCI4: SCL SDHID: DA SPI1: SSL1 SSI1: SSI1: SSI1: SSI2 USBFS0: VI	31 LINKSTA LINKSTA J J MISO T2 ATA	Link	



Note: When creating a project from scratch, you must add the touch driver to your project by copying the touch_ft5x06 folder in this application note project to the new project. Go to **Project > Properties** > C/C++ Build > Settings > GNU ARM Cross C Compiler > Includes to add its include path.

Project Explorer 🛛 🖻 🔄 🖓 🖇 🗖 🗖
✓ ☺️ Graphics_App_EK_RA6M3G [Debug]
> 🖑 Binaries
> 🔊 Includes
> 🔁 AppWizard
> 😕 ra
> 😂 ra_gen
✓ 🖽 src
✓ → touch_ft5x06
> .c touch_ft5x06.c
> 🖻 touch_ft5x06.h
> C APPW_X_NoFS.c
> c emWin_thread_entry.c
> 1 hal_entry.c > 1 timer_thread_entry.c
> is touch_thread_entry.c
> > Debug
- 🖉 Debug

Figure 40. touch_ft5x06 Driver Resources



Add directory path X]
Directory:	
\${workspace_loc:/\${ProjName}/src/touch_ft5x06}	
Add subdirectories	
OK Cancel Workspace File system	

Figure 41. Add Touch Driver Folder to the List of Include Paths

5.4.4 **PWM** Configuration

The LCD_BLEN signal (Blanking Enable), which is connected to the P603 on the RA6M3 MCU, is configured in PWM mode to control the intensity of the LCD backlight. Figure 42 shows an excerpt from the Graphics Expansion board schematic, which shows the LCD_BLEN signal connected to the backlight controller.



Figure 42. Backlight Control Pin on EK-RA6M3G

In **Pin Configuration**, set P603 as the GTIOCA output of the GPT channel 7. The **Pin Group Selection** is set as mixed, and the **Operation Mode** is GTIOCA or GTIOCB.

Select Pin Configuration			📑 Export to CSV file 📰	Configure Pi	n Driver Warnings	
RA6M3G-EK.pincfg	✓ Manage	2	Generate data:	_bsp_pin_cf	g	
Pin Selection	⊨ 🕀 🕒 ↓ª _z	Pin Configuration				Cycle Pin Group
Type filter text		Name	Value	Lock	Link	
GPT2	^	Pin Group Selection	Mixed			
GPT3		Operation Mode	GTIOCA or GTIOCB			
GPT4		✓ Input/Output				
GPT5		GTIOCA	✓ P603	i i i i i i i i i i i i i i i i i i i		
GPT6		GTIOCB	None	- É	\Rightarrow	
🗸 GPT7						
GPT8						
GPT9		Module name: GPT				
GPT10						
GPT11						
GPT12						
GPT13						
> Timer:OPS						
> Timer:POEG						
> Timer:RTC						
> 🗸 Other Pins						

Figure 43. GPT PWM Channel 7 Pin Configuration

The r_gpt in the Timer thread is set in PWM mode to modulate LCD backlight intensity. In this graphics application, moving a slider in the **Logging Panel** will generate a duty cycle percentage that will be calculated into the GPT timer period and written to the counter register.



Threads	🔬 New Thread 🔬 Remove 📃	g_timer_PWM Timer, General PWM (r_gpt) Stacks	된 New Stack > 🏯 Extend Stack > 🛍 Remove
	AL/Common g_ioport I/O Port (r_ioport) nWin Thread SEGGER emWin FreeRTOS Heap 4 uch Thread g_touch_jriq External IRQ (r_icu) g_i2c_touch I2C Master (r_iic_master) meT Thread g_rtc_timer Realtime Clock (r_rtc) g_timer_PWM Timer, General PWM (r_gpt)	 ● g_timer_PWM Timer, General PWM (r_gpt) ① 	
Objects			
● g_i2c	ch_semaphore Binary Semaphore semaphore Binary Semaphore er_semaphore Binary Semaphore		
	ns 📮 Console 🔲 Properties 🗙 🐝 Smart Br PWM Timer, General PWM (r_gpt)	owser 🤑 Smart Manual 🎄 Debug	
ettings		Mele -	
	Property	Value	
PLInfo	✓ Common		
PI Info	 Common Parameter Checking 	Default (BSP)	
PI Info	✓ Common	Default (BSP) Enabled	
PI Info	 Common Parameter Checking 	Default (BSP)	
PI Info	 Common Parameter Checking Pin Output Support 	Default (BSP) Enabled Disabled	
PI Info	 Common Parameter Checking Pin Output Support Write Protect Enable 	Default (BSP) Enabled Disabled	
Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWW	Default (BSP) Enabled Disabled	
NPI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWN General	Default (BSP) Enabled Disabled 1 (r_gpt)	
NPI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWN	Default (BSP) Enabled Disabled 4 (r_gpt) g_timer_PWM	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWN General Name Channel Mode Period	t (r_gpt) g_timer_PWM 7 50	
IPI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Unit	4 (r_gpt) Gault (BSP) Enabled Disabled g_timer_PWM 7 Saw-wave PWM	
IPI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Unit Vutput	t (r_gpt) g_timer_PWM 7 50	
IPI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Period Vutput Custom Waveform	A (r.gpt) Default (BSP) Enabled Disabled A (r.gpt) 7 Saw-wave PWM 50 Microseconds	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Period Output Custom Waveform Duty Cycle Percent (only applicable in	h (r_gpt) Pfault (BSP) Enabled Disabled I (r_gpt) g_timer_PWM g_timer_PWM S0 Microseconds Microseconds Disabled S0 Microseconds S0 S0 S0	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Unit Output Custom Waveform Duty Cycle Percent (only applicable in GTIOCA Output Enabled	n PWM mode) 50 True	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Period Votput Custom Waveform Duty Cycle Percent (only applicable in GTIOCA Stop Level	PWM mode) PWM mode) Public	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Period Period Duty Cycle Percent (only applicable in GTIOCA Output Enabled GTIOCA Stop Level GTIOCA Boutput Enabled	h PWM mode) 1 PWM mode) 50 T	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Period Period Duty Cycle Percent (only applicable in GTIOCA Stop Level GTIOCB Output Enabled GTIOCB Stop Level	PWM mode) PWM mode) Public	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Period Period Duty Cycle Percent (only applicable in GTIOCA Output Enabled GTIOCA Stop Level GTIOCA Boutput Enabled	h PWM mode) 1 PWM mode) 50 T	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Period Period Duty Cycle Percent (only applicable in GTIOCA Stop Level GTIOCB Output Enabled GTIOCB Stop Level	h PWM mode) 1 PWM mode) 50 T	
PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Unit Output Custom Waveform Duty Cycle Percent (only applicable in GTIOCA Output Enabled GTIOCA Stop Level GTIOCB Stop Level J Input	h PWM mode) 1 PWM mode) 50 T	
.PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Unit Output Custom Waveform Duty Cycle Percent (only applicable in GTIOCA Output Enabled GTIOCA Stop Level GTIOCA Stop Level GTIOCB Stop Level STIOCB Stop Level Input Input	h PWM mode) 1 PWM mode) 50 T	
.PI Info	Common Parameter Checking Pin Output Support Write Protect Enable Module g_timer_PWM Timer, General PWM General Name Channel Mode Period Period Period Period Duty Cycle Percent (only applicable in GTIOCA Stop Level GTIOCA Stop Level GTIOCA Stop Level Input Input Input Input Input Extra Features	h PWM mode) 1 PWM mode) 50 T	

Figure 44. GPT Driver Configuration in PWM Mode

Figure 45 and Figure 46 show the AppWizard configuration for the backlight slider. Its range limits are from 5 to 100. Some interactions and custom code are needed to control the duty cycle of PWM output as well.

٩		Application Event		Dark Mode	Image: state
Interactions	T				Set vertical mode Vertical mode enabled
2 · · · · · · · · · · · · · · · · · · ·	Emitter D.SCREN_LOG D.SWITCH_DARKMODE D.SWITCH_DARKMODE D.SWITCH_DARKMODE D.DOTART_TEXT_COLOR_CH. D.VAR_TEXT_COLOR_CH. TO_SUIDER_BACKLIGHT ID_BUITTON_BACK	INITDIALOG S CLEAR S SET S VALUE_CHANGED S VALUE_CHANGED S VALUE_CHANGED S VALUE_CHANGED	SET ID_VAR_DARKMODE SET ID_VAR_DARKMODE SET ID_VAR_TEXT_COLOR_C SET ID_VAR_BACKLIGHT	Comment Set default value Set value when clear and execute custom code Set value when clear and execute custom code Set value Oustom code Set value Control LCD backlight Execute custom code and go back to main screen	Invert direction Set bitmaps Set bitmaps Set bitmaps Set bitmaps Set bitmaps Set biend colors Set ange Xet ange

Figure 45. Slider Setup to Control LCD Backlight Intensity



Figure 46. Custom Code Controls PWM Update GPT Timer Duty Cycle

6. Application Code Highlights

This section details the highlights of the graphics application. The goal of the graphics application is to show you how to develop more complex multi-threaded HMI applications using the FSP, AppWizard, and emWin library.

The key goal of the FSP is to abstract much of the complexity of interfacing with various Renesas peripherals and to quickly get you to the point where you can focus on constructing more complex applications as quickly as possible.

6.1 Threads and Main

In the FSP, main() is an auto-generated file that looks like the following code. The threads and objects specified during the FSP configuration are initialized in the main().



Figure 47. The main () function in FSP with FreeRTOS Enabled

When you create a thread using the **New Threads** tab, the FSP creates several files. As an example, when the **emWin Thread** is added, the FSP creates three files for you: <code>emWin_thread.h</code>, <code>emWin_thread.c</code>, and <code>emWin_thread_entry.c</code>, as shown in Figure 48.

The first two files are auto-generated and, therefore, put into the ra_gen folder. The emWin_thread_entry.c file is the entry point for the **emWin Thread**, and this is where you put your



application code. Auto-generated files should not be updated by the user since they will be re-generated every time you build the project or click the **Generate Project Content** button. Auto-generated files always contain some form of **do not edit** message at the top of the file.



Figure 48. FSP Generated Source File Organization

6.1.1 AppWizard/emWin Initialization

The FSP does not automatically initialize the AppWizard system. To initialize it, simply include GUI.h and add the MainTask() API call to emWin_thread_entry() located in the emWin_thread_entry.c file.

#in ⊖ /* /*	<pre>clude "emWin thread.h" clude "GUI.h" emWin thread entry function */ pvParameters contains TaskHandle_t */</pre>
e voi	<pre>.d emWin_thread_entry(void *pvParameters)</pre>
ł	<pre>FSP_PARAMETER_NOT_USED (pvParameters);</pre>
	MainTask();
⊖ }	<pre>while (1) { vTaskDelay (1); }</pre>

Figure 49. MainTask() added to Initialize the AppWizard System in the emWin_thread

6.1.2 emWin Events and Messages

Touching the screen in the graphics application causes emWin to invoke the specific callback function generated for that screen in the AppWizard. AppWizard provides the callback function with specific information about the window that caused the event and the actual event that occurred. These events are defined in WM.h.

You can add your code to slot routines in the file <ScreenID >_Slots.c located in the \AppWizard\Source\CustomCode folder to handle window events. The slot routines are actual callback



routines generated by AppWizard. Since the <ScreenID >_Slots.c is updated whenever you add and generate new widgets or AppWizard interactions using AppWizard. However, custom code will be retained. It is a good practice to create your custom code in a separate file and call it in the appropriate slot routine.





6.1.3 AppWizard Variables

Variables in the AppWizard can be used to store a value. They can be accessed and changed by the GUI or from outside of the GUI. The GUI can react to a change of a variable using interactions. One of the typical uses is to update the variables in a non-GUI thread to trigger data exchange between the AppWizard and non-GUI threads.



Figure 51. AppWizard Variable Update in Timer Thread



+/-	1	\sim	[[Emitter	Signal	Job	Receiver	Comment	
×	1			D_VAR_TIME_UPDATE	VALUE_CHANGED			Update time w	hen this changed
××	1		🥑 Se	et interaction parameters					×
× ×	1		Slo	t: ID_SCREEN_MAINWM_N	OTIFICATION_VALUE	CHANGE	D_ID_VAR_TIME_UPDATE		
×	1	+ +	Cod	e:		Edit c	ode		
ŝ	1							Ok	Cancel

Figure 52. Setup Interaction to perform Date, Time Update when ID_VAR_TIME_UPDATE is changed in AppWizard

7. Importing and Building the Project

To bring the graphics application into the e2studio, follow these steps:

- 1. Launch e² studio.
- 2. In the workspace launcher, browse to the workspace location of your choice.
- 3. Close the **Welcome** window.
- 4. In e² studio, go to File > Import.
- 5. In the Import dialog box, pick Existing Projects in Workspace.
- 6. Select the archive file.
- 7. Select the Graphics_App_EK_RA6M3G project and click **Finish**.
- 8. Open configuration.xml.
- 9. Click on Generate Project Content on the FSP configurator window.
- 10. Now build the project.

8. Downloading the Executable to the EK-RA6M3G Kit

To connect and run the code, follow these steps:

- 1. Connect your PC to the USB port next to the Ethernet jack silkscreened DEBUG using a USB cable.
- 2. Go to Run > Debug Configurations.
- 3. Go to **Renesas GDB Hardware Debugging** > Chose **Graphics_App_EK_RA6M3G Debug** > Click **Debug**. The program will break at the reset handler.
- 4. Click **Switch** to the **Debug perspective** when prompted by the e² studio.
- 5. Click **Run > Resume**.
- 6. The **Weather Panel** will show as in Figure 53. You can select the forecast day or adjust the thermostat temperature. Touch the top right corner to move to the **Logging Panel**.



Figure 53. The Weather Panel

The **Logging Panel** allows you to adjust the LCD backlight using the slider or change the **Logging Dialog** text color and background color using the rotary and the switch, respectively. The logging buffer resets when it reaches the limit of 256 bytes. Touch the Renesas logo to go back to the **Weather Panel**.





Figure 54. The Logging Panel

9. e² studio Tricks

The e² studio IDE has a handy feature that you can use to ensure that the images you are seeing on your LCD screen are coming from your framebuffer. To use this feature, make sure to connect the e² studio to your board and run the program under the debugger. Ensure that your **Memory** tab is open in the **Console** window, normally located at the bottom of the screen in **Debug** view. Click the small green plus (+) sign in the Monitors Pane to add a memory monitor. You should see a **Monitor Memory** dialog as shown in . Enter the Framebuffer **fb_background[0]** or **fb_background[1]** and click the **OK** key.

A new tab should now appear under the **Memory** tab that displays the contents of the memory area you specified for the memory monitor.



Figure 55. Using the Memory Monitor to Display the Framebuffer Contents

You should now see the contents of the selected framebuffer memory area displayed in the memory monitor you just created. If you know what the hex value of every pixel should be on your display, you would be able to use this memory monitor to definitively say that your image is being stored in the framebuffer. However, as most of us do not know the hex values associated with our pixels, we will let the memory monitor do the work for us.



Console Registers	Debug Shell	Problems Del	bugger Console	Smart B	rowser Me	mory ×
Monitors	+ X 🔆	fb_background[1]: 0x20	0034B40 <hex li<="" td=""><td>nteger> 🗙 🕇</td><td>New Renderi</td><td>ngs</td></hex>	nteger> 🗙 🕇	New Renderi	ngs
fb_background[0]		Address	0 - 3	4 - 7	8 - B	C - F
fb_background[1]		000000020034B40	8D5C959C	8D5C8D5C	8D5C8D5C	8D5C8D5C
		000000020034B50	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034B60	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034B70	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034B80	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034B90	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034BA0	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034BB0	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034BC0	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034BD0	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		0000000020034BE0	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034BF0	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034C00	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034C10	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034C20	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034C30	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034C40	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C
		000000020034C50	8D7C8D7C	8D7C8D7C	8D7C8D7C	8D7C8D7C

Figure 56. Framebuffer 1 Contents

Select the **New Renderings** tab next to the memory monitor you just created, select **Raw Image** type from the list of options, and press the **Add Rendering(s)** button off to the right side of the screen.

Console	Registers	Debug Shell	Problems Debugger Console Smart Browser Memory X
Monitors		+ 🗙 🔆	fb_background[1] <hex integer=""> Integer></hex>
	kground[0]		Memory Monitor: fb_background[1]: 0x20034B40
🔶 fb_bac	kground[1]		Select rendering(s) to create:
			Waveform
			Hex Integer
			Fixed Floating Point
			Fixed Point
			Image Developeration
			Raw Image Floating Point
			Traditional
			Raw Hex
			ASCI
			Signed Integer
			Unsigned Integer

Figure 57. Rendering Format Selection

The **Raw Image Format** dialog box appears, which lets you enter the screen resolution Width and Height, along with the Encoding, which is 16 bpp (5:6:5) in our case.



Dimensions Width: [480 Height: 272 Encoding Monochrome: 1bpp Monochrome: 1bpp (% RGB: 16bpp (5:6:5) Start Position Top Bottom	Raw Image Forr	mat	×
○ Monochrome: 1bpp ✓ ● RGB: 16bpp (5:6:5) ✓ ○ BGR: 32bpp (8:8:8:8) ✓ ○ YCbCr: 32bpp (Y12-UV8 semi-planar) ✓ Line alignment: 4 bytes ✓ Start Position ✓ ✓ Image: Top ✓ ✓	Dimensions Width: 480		
 RGB: 16bpp (5:6:5) BGR: 32bpp (8:8:8:8) YCbCr: 32bpp (Y12-UV8 semi-planar) Line alignment: 4 bytes Start Position Top 	Encoding		
 ○ BGR: 32bpp (8:8:8:8) ○ YCbCr: 32bpp (Y12-UV8 semi-planar) Line alignment: 4 bytes ~ Start Position ● Top 	O Monochrome:	1bpp	~
 ○ YCbCr: 32bpp (Y12-UV8 semi-planar) Line alignment: 4 bytes Start Position ● Top 	• RGB:	16bpp (5:6:5)	~
Line alignment: 4 bytes ~ Start Position Top	⊖ BGR:	32bpp (8:8:8:8)	\sim
Start Position Top 	○ YCbCr:	32bpp (Y12-UV8 semi-planar)	\sim
Top	Line alignment:	4 bytes	~
	🖲 Тор		

Figure 58. Raw Image Format for Graphics Application on EK-RA6M3G

Once you press the **OK** key, the memory monitor presents you with the image that will be displayed at that memory address based on the parameters you entered.



Figure 59. Image Rendering Using Seen Using e² studio Memory Monitor



10. Website and Support

Visit the following URLs to learn about key elements of the RA family, download components and related documentation, and get support.

RA Product Information RA Product Support Forum RA Flexible Software Package Renesas Support www.renesas.com/ra www.renesas.com/ra/forum www.renesas.com/FSP www.renesas.com/support



Revision History

		Description	
Rev.	Date	Page	Summary
1.00	Jul.13.20	-	Initial version
2.00	Nov.11.21	-	Major updates for AppWizard v1.24_6.20
2.10	Jun.28.23	-	Minor updates for AppWizard v1.36a_6.32a + FSP v4.4.0
2.20	May.20.24	-	Minor updates for AppWizard v1.42_6.36 + FSP v5.2.0
2.30	Nov.25.24	-	Minor updates for AppWizard v1.52_6.44b + FSP v5.6.0
2.40	Jul.08.25	-	Minor updates for AppWizard v1.56_6.48 + FSP v6.0.0



General Precautions in the Handling of Microprocessing Unit and Microcontroller Unit Products

The following usage notes are applicable to all Microprocessing unit and Microcontroller unit products from Renesas. For detailed usage notes on the products covered by this document, refer to the relevant sections of the document, as well as any technical updates that have been issued for the products.

1. Precaution against Electrostatic Discharge (ESD)

A strong electrical field, when exposed to a CMOS device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop the generation of static electricity as much as possible, and quickly dissipate it when it occurs. Environmental control must be adequate. When it is dry, a humidifier should be used. This is recommended to avoid using insulators that can easily build up static electricity. Semiconductor devices must be stored and transported in an anti-static container, static shielding bag or conductive material. All test and measurement tools including work benches and floors must be grounded. The operator must also be grounded using a wrist strap. Semiconductor devices must not be touched with bare hands. Similar precautions must be taken for printed circuit boards with mounted semiconductor devices.

2. Processing at power-on

The state of the product is undefined at the time when power is supplied. The states of internal circuits in the LSI are indeterminate and the states of register settings and pins are undefined at the time when power is supplied. In a finished product where the reset signal is applied to the external reset pin, the states of pins are not guaranteed from the time when power is supplied until the reset process is completed. In a similar way, the states of pins in a product that is reset by an on-chip power-on reset function are not guaranteed from the time when power reaches the level at which resetting is specified.

3. Input of signal during power-off state

Do not input signals or an I/O pull-up power supply while the device is powered off. The current injection that results from input of such a signal or I/O pull-up power supply may cause malfunction and the abnormal current that passes in the device at this time may cause degradation of internal elements. Follow the guideline for input signal during power-off state as described in your product documentation.

4. Handling of unused pins

Handle unused pins in accordance with the directions given under handling of unused pins in the manual. The input pins of CMOS products are generally in the high-impedance state. In operation with an unused pin in the open-circuit state, extra electromagnetic noise is induced in the vicinity of the LSI, an associated shoot-through current flows internally, and malfunctions occur due to the false recognition of the pin state as an input signal become possible.

5. Clock signals

After applying a reset, only release the reset line after the operating clock signal becomes stable. When switching the clock signal during program execution, wait until the target clock signal is stabilized. When the clock signal is generated with an external resonator or from an external oscillator during a reset, ensure that the reset line is only released after full stabilization of the clock signal. Additionally, when switching to a clock signal produced with an external resonator or by an external oscillator while program execution is in progress, wait until the target clock signal is stable.

6. Voltage application waveform at input pin

Waveform distortion due to input noise or a reflected wave may cause malfunction. If the input of the CMOS device stays in the area between V_{IL} (Max.) and V_{IH} (Min.) due to noise, for example, the device may malfunction. Take care to prevent chattering noise from entering the device when the input level is fixed, and also in the transition period when the input level passes through the area between V_{IL} (Max.) and V_{IH} (Min.).

7. Prohibition of access to reserved addresses

Access to reserved addresses is prohibited. The reserved addresses are provided for possible future expansion of functions. Do not access these addresses as the correct operation of the LSI is not guaranteed.

8. Differences between products

Before changing from one product to another, for example to a product with a different part number, confirm that the change will not lead to problems. The characteristics of a microprocessing unit or microcontroller unit products in the same group but having a different part number might differ in terms of internal memory capacity, layout pattern, and other factors, which can affect the ranges of electrical characteristics, such as characteristic values, operating margins, immunity to noise, and amount of radiated noise. When changing to a product with a different part number, implement a system-evaluation test for the given product.

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(Rev.5.0-1 October 2020)

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