

瑞萨电子  
车身控制



More functions, less consumption



Save power, don't compromise



- > **RL78/F1X**  
More than 100 derivatives

> **New Snooze Power Save Mode**  
The new mode supports special power-down concepts for cyclic wake up applications. It allows monitoring of serial interfaces and analog input pins from few up to no CPU activities.

> **More CPU performance**  
Less power consumption

> **New 130-nm Technology**  
Thanks to the new 130-nm Flash technology we've realized a power reduction by 50%.
- > **RH850/F1X**  
More than 50 derivatives

> **New Low Power Sampling (LPS)**  
The new LPS supports special power-down concepts for cyclic wake up applications. IO-Ports, analogue input pins, LIN- and CAN-Interface and external sensor supply are regarded by the LPS by hardware without any CPU activities.

> **More CPU performance**  
Less power consumption

> **New 40-nm Technology**  
The industry's first 32-bit 40-nm generation of Automotive MCUs with considerably lower power consumption than other currently available 90-nm devices

Feature line-up

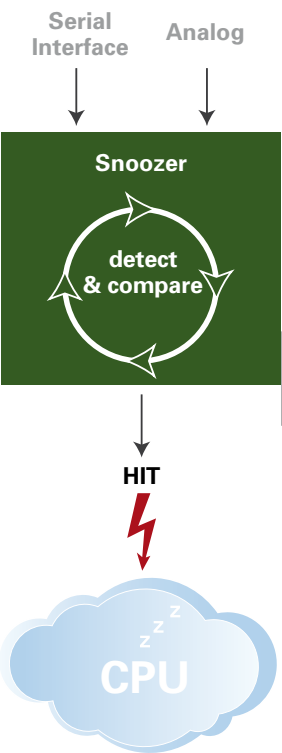
16-bit			32-bit		
RL78/F12	RL78/F13	RL78/F14	RH850/F1L	RH850/F1M	RH850/F1H
AUTOSAR					
				Ethernet	
				FlexRay	FlexRay
	0 - 1 ch. CAN	1 ch. CAN	1 - 6 ch. CAN	6 ch. CAN	7 ch. CAN
1 ch. LIN	1 ch. LIN	1 - 2 ch. LIN	3-6 ch. LIN	7 - 16 ch. LIN	16 - 18 ch. LIN
2 - 4 ch. UART	1 - 2 ch. UART	2 ch. UART	1 - 6 ch. UART	4 - 6 ch. UART	6 ch. UART
2 - 7 ch. CSI	2 - 4 ch. CSI	3 - 4 ch. CSI	2 - 6 ch. CSI	5 - 7 ch. CSI	8 ch. CSI
1 - 7 ch. I <sup>2</sup> C	2 - 5 ch. I <sup>2</sup> C	3 - 5 ch. I <sup>2</sup> C	1 ch. I <sup>2</sup> C	1 ch. I <sup>2</sup> C	1 ch. I <sup>2</sup> C
4 - 12 ch. 10-bit ADC	4 - 20 ch. 10-bit ADC	12 - 31 ch. 10-bit ADC	12 - 60 ch. 10/12 -bit ADC	32 - 60 ch. 10/12 -bit ADC	60 - 72 ch. 10/12 -bit ADC
4 - 7 ch. PWM	7 - 16 ch. PWM	11 - 20 ch. PWM	13 - 72 ch. PWM	40 - 80 ch. PWM	72 - 96 ch. PWM
8 ch. 16 - bit timer	7 - 21 ch. 16 - bit timer	21 - 25 ch. 16 - bit timer	0 - 8 ch. 16 - bit timer	16 - 32 ch. 16 - bit timer	32 ch. 16 - bit timer
			4 - 8 ch. 32 - bit timer	8 ch. 32 - bit timer	8 ch. 32 - bit timer
4 kB Dataflash	4 kB Dataflash	4 or 8 kB Dataflash	32 kB Dataflash	64 kB Dataflash	64 - 128 kB Dataflash
up to 32MHz	up to 32MHz	up to 32MHz	up to 80MHz	up to 120MHz	up to 120MHz
2.7 - 5.5V	2.7 - 5.5V	2.7 - 5.5V	3.0 - 5.5V	3.0 - 5.5V	3.0 - 5.5V
up to 125 °C	up to 150 °C	up to 150 °C	up to 125 °C	up to 125 °C	up to 125 °C

16-Bit MCU: RL78/F1x

The RL78 Family is Renesas Electronics next-generation microcontroller family combining advanced features from both the 78K and R8C families to deliver low power consumption and high performance.

Highlights

- > CPU performance: up to 51 DMIPS (Dhrystone V2.1) at 32MHz
- > Low power consumption: 200uA/MHz @ Operation, 0.32uA @ STOP mode with LVD
- > SNOOZE mode for efficient cyclic wake up applications
- > Functional safety concept supported by many safety features
- > High temperature: Ta up to 150°C



RL78/F14 Features

- > RL78 Core 32MHz

» 16-bit CISC CPU with MUL/DIV/MAC
- > Flash technology

» Wide voltage range: 2.7 – 5.5V

» Data flash: simultaneous operation for EEPROM emulation, 100k w/e cycles
- > Clock generator circuit

» On chip High-speed (64/32MHz) and Low-speed (15kHz) oscillator

» PLL (bypass for CAN IP)

» External sub-clock (32kHz) connection
- > System Peripherals

» Data Transfer Controller (DTC): equivalent to 24-channel DMA

» Event Link Controller (ELC): combines peripherals occasions
- > Analogue Peripherals

» 8-bit D/A converter

» Analogue Comparator with adjustable hysteresis and 4 channel multiplexer
- > Timer

» 16-bit Motor Control Timer for 3-phase motor
- > Serial Interfaces

» CAN: 20 mailboxes

» LIN: protocol controller with extended hardware support and master/slave mode
- > Functional Safety

» ECC on Flash and RAM

» Stack pointer monitor

» Invalid memory access detection

» Window watchdog timer with exclusive on chip oscillator

» HW-CRC

» Clock Monitor

» I/O port output signal level detection

» Automotive self-test software available

System	16-bit CPU	Interfaces	
DTC (24xDMA)	<b>RL78 Core</b> 32MHz @ -40 to +105°C 24MHz @ -40 to +125°C (150°C) Integrated MUL / DIV / MAC 2.7 to 5.5V (single voltage)	1x CAN	
Event Link Controller		2x HW LIN-Master	
PLL		up to 2x UART	
Internal 15kHz OSC		up to 4x CSI	
Internal 32MHz OSC		up to 4x I²C	
Ext. OSC 20MHz	On-Chip debug (Hot plug in, Live debug)	I²C Multimaster	
Ext. Sub-OSC 32kHz	<b>Memory</b> 256KB Flash (ECC) 20KB iRAM (ECC) 192KB Flash (ECC) 16KB iRAM (ECC) 128KB Flash (ECC) 10KB iRAM (ECC) 96KB Flash (ECC) 8KB iRAM (ECC) 64KB Flash (ECC) 6KB iRAM (ECC) 48KB Flash (ECC) 4KB iRAM (ECC) 8KB Data Flash (ECC)	15x Ext INT	
POR / LVD		8x KeyReturn	
Clock Monitor		92 I/O Ports	
<b>Analog</b> 31x 10-Bit ADC		<b>Timers</b> 3P Motor Timer 16-bit 16x 16-bit Timer 16-bit OS-Timer RTC Window WDT (15kHz)	
<b>BLDC Motor Ctrl.</b>			
Comparator 4x MUX			
8-bit DAC			

RL78/F1x Portfolio

FLASH						
256 KB						
192 KB						
128 KB						
96 KB						
64 KB						
48 KB						
32 KB						
24 KB						
16 KB						
8 KB						
	20 pin SSOP (300mil)	30 pin SSOP (300mil)	32 pin QFN (5x5)	48 pin QFP (7x7) QFN (7x7)	64 pin QFP (10x10)	80 pin QFP (12x12) 100 pin QFP (14x14)
	■ F12 LIN, Comparator ■ F13 LIN, CAN, Motor-Timer ■ F14 LIN, CAN, Motor-Timer + Comparator					

RL78/F1x hardware development tools

IECUBE2

Full functional In-Circuit Emulator



Debugger System



Target Board

Device specific MCU board with on-chip debug connector



E1

On-Chip Debugger and Flasher



PG-FP5

Flash programmer



RL78/F1x software development tools

Software	Type	Supplier
C/C++ compiler, Debugger, Editor	IAR, EWRL78 Workbench	Renesas, IAR
MCAL 4.0.3 (Microcontroller Abstraction Layer)	Peripheral driver software	Renesas (under development)
Applilet (device driver generation tool)	Peripheral driver generator software	Renesas
Flash Library (flash self programming, EEPROM emulation)	Flash driver software	Renesas
LIN driver	LIN Communication software	Vector Informatik
CAN driver	CAN Communication software	Vector Informatik, Elektrotbit
OSEK	Operating System	Vector Informatik, Elektrotbit
Flash Programmer Software	Programmer suitable for PG-FP5 and E1	Renesas



## 32-Bit MCU: RH850/F1x

The RH850 Family represents the next generation 32-bit RISC Microcontroller to endorse future automotive applications. The F Series products, designed for body applications, provide high scalability, extreme low power consumption and a broad range of networking IPs.

## Highlights

- > **Wide scalability**

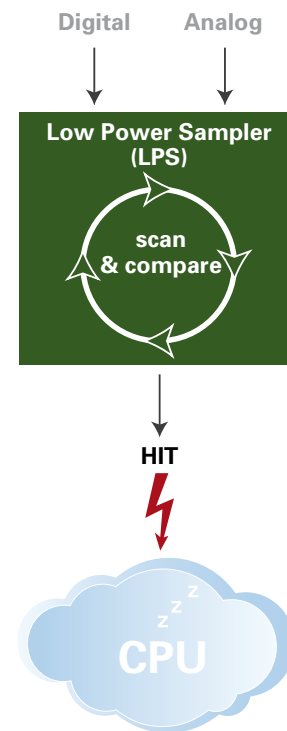
- » 256K byte to 8M byte embedded Flash memory with ECC
- » 48 pin to 357 pin QFP and BGA packages
- » Single-, dual- and multicore architecture

- > High reliability

- » Functional safety compliant (ASIL A to ASIL D)
- » Support of security standards (SHE/ ICU-S, HSM/ ICU-M)
- » Excellent high temperature performance (up to 170°C)

- Best in class solution for cyclic activities

- » Low Power Sampler (LPS) to scan and compare digital- and analog inputs CPUless
- » Cyclic RUN/ STOP Mode to realize low power LIN/ CAN communication



## Features

- > RH850 core

- » 32bit RISC CPU with FPU (Option)
- » 80MHz to 160MHz Clock
- » Single-Voltage 3.0 to 5.5V
- » Operating temperature range -40 to +125°C

- Flash technology

- » 40nm MONOS (Metal-Oxide-Nitrid-Oxide-Silicon) Flash
- » Up to 8M byte code flash
- » Up to 320K byte RAM + 32K byte retention RAM
- » Up to 128K byte data flash

## > System Peripherals

- » DMA Controller with minimum 16 channel
- » External memory I/F

## > Analogue Peripherals

- » 10-bit and 12-bit resolution multi channel ADC
- » Up to 6 Track & Hold circuits for synchronized conversion
- » Hardware support for external channel multiplexer
- » Self-diagnostic function

- > **Timer**

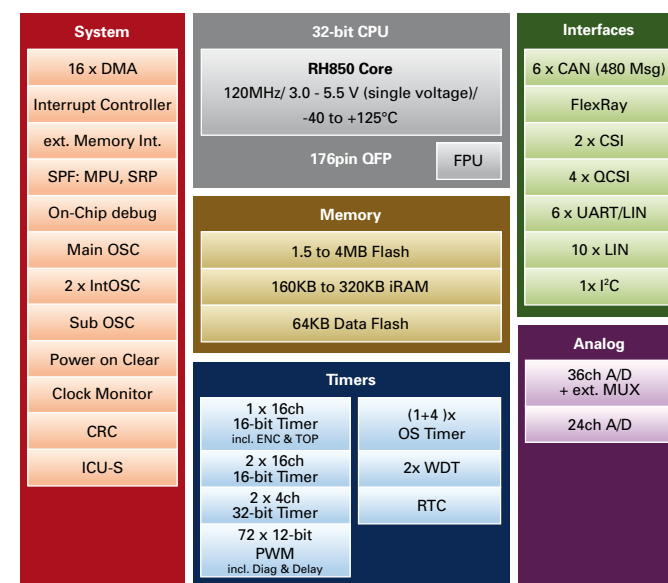
- » Timer array unit with 16-bit and 32-bit resolution
- » PWM diagnostic function

## Serial Interfaces

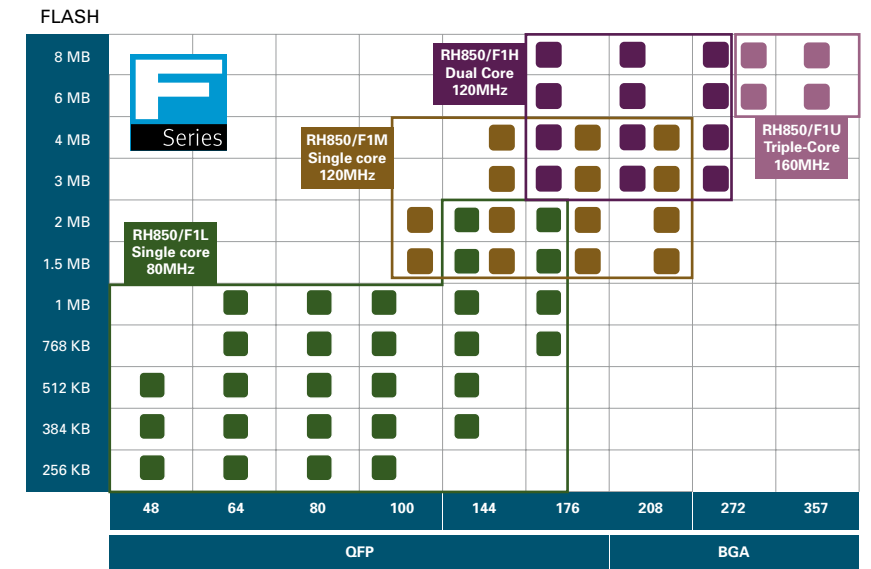
- » CAN, LIN, FlexRay, Ethernet, USB, MLB
- » UART, CSI, QCSI

## > Functional Safety

- » Software core selftest
- » Error Correction Coding (ECC)
- » Memory Protection Unit (MPU)
- » Redundant reset controller
- » Clock- and Core voltage monitor
- » Window watchdog
- » Error Correction Coding (ECC)
- » Core voltage monitor
- » Temperature sensor



## RH850/F1x Line-up



## RH850/F1x Development Tools

## E1

## On-Chip Debugger and Flash programmer



## Debugger System



## RH850 Evaluation platform

- with Device/Package specific MCU piggy-pack board adaptation



## PG-FP5

Flash programmer



## RH850/F1x software development tools

Software	Type	Supplier
C/C++ compiler, Debugger, Editor	GHS MULTI Version 6	Renesas/Green Hills
C/C++ compiler, Debugger, Editor	IAR EWRH850	IAR (under development)
MCAL (Microcontroller Abstraction Layer)	Peripheral driver software	Renesas
Flash Library (flash self programming, EEPROM emulation)	Flash driver software	Renesas
LIN driver	LIN Communication software	Vector Informatik
CAN driver	CAN Communication software	Vector Informatik
OSEK	Operating System	Vector Informatik
Flash Programmer Software	RFP Programmer for E1	Renesas

# 瑞萨电子车身控制应用目录

## Renesas Electronics Body Control Application Catalog

### Renesas Electronics Corporation

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