

Tsi384 Ballmap -- Bottom View (Document 80E1000_PN001_02)

	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1
A	PCI_AD[61]	PCI_AD[63]	PCI_CBE _n [6]	PCI_ACK64 _n	PCI_AD[1]	PCI_AD[3]	PCI_AD[7]	PCI_AD[10]	PCI_AD[12]	PCI_AD[14]	PCI_SERR _n	PCI_PCIXCAP	PCI_DEVSEL _n	PCI_CBE _n [2]	PCI_AD[19]	PCI_AD[21]
B	PCI_AD[59]	VDD_PCI	PCI_CBE _n [4]	VSS	VDD_PCI	PCI_AD[5]	PCI_AD[8]	PCI_M66EN	VDD_PCI	PCI_CBE _n [1]	PCI_PERR _n	VSS	PCI_IRDY _n	PCI_AD[17]	VDD_PCI	PCI_AD[23]
C	PCI_AD[57]	PCI_AD[62]	PCI_PAR64	PCI_CBE _n [7]	PCI_AD[0]	PCI_AD[2]	PCI_AD[6]	VSS	PCI_AD[11]	PCI_AD[15]	PCI_STOP _n	PCI_PCIXCAP_P U	PCI_AD[16]	PCI_AD[20]	PCI_AD[22]	PCI_CBE _n [3]
D	PCI_AD[55]	PCI_AD[58]	PCI_AD[60]	PCI_CBE _n [5]	PCI_REQ64 _n	PCI_AD[4]	PCI_CBE _n [0]	PCI_AD[9]	PCI_AD[13]	PCI_PAR	PCI_TRDY _n	PCI_FRAME _n	PCI_AD[18]	PCI_AD[24]	PCI_AD[25]	PCI_AD[27]
E	PCI_AD[51]	PCI_AD[53]	PCI_AD[54]	PCI_AD[56]	VDD_PCI	VDD	VDD_PCI	VDD	VDD	VDD_PCI	VDD	VDD_PCI	NC	PCI_AD[26]	VSS	PCI_AD[29]
F	PCI_AD[49]	VSS	PCI_AD[50]	PCI_AD[52]	VDD	VSS	VSS	VSS	VSS	VSS	VSS	VDD	PCI_AD[28]	PCI_AD[30]	VDD_PCI	PCI_AD[31]
G	PCI_AD[47]	VDD_PCI	PCI_AD[46]	PCI_AD[48]	VDD	VSS	VSS	VSS	VSS	VSS	VSS	VDD	PCI_PME _n	PCI_GNT _n [0]	PCI_REQ _n [0]	PCI_REQ _n [1]
H	PCI_AD[43]	PCI_AD[45]	PCI_AD[42]	PCI_AD[44]	VDD_PCI	VSS	VSS	VSS	VSS	VSS	VSS	VDD_PCI	PCI_RST _n	PCI_GNT _n [1]	PCI_REQ _n [2]	PCI_REQ _n [3]
J	PCI_AD[39]	PCI_AD[41]	PCI_AD[38]	PCI_AD[40]	VDD	VSS	VSS	VSS	VSS	VSS	VSS	VDD	PCI_GNT _n [2]	PCI_GNT _n [3]	VDD_PCI	PCI_CLK
K	PCI_AD[35]	PCI_AD[37]	PCI_AD[34]	PCI_AD[36]	VDD	VSS	VSS	VSS	VSS	VSS	VSS	VDD	PCI_INTC _n	PCI_CLKO[0]	VSS	PCI_CLKO[1]
L	PCI_AD[33]	VSS	PCI_AD[32]	NC	VDD_PCI	VSS	VSS	VSS	VSS	VSS	VSS	PWRUP_EN_AR B	PWRUP_CLK_M ST	PCI_INTA _n	PCI_CLKO[2]	PCI_CLKO[3]
M	SR_DIN	VDD_PCI	SR_CLK	NC	VDD	PCIE_RXD_ _n [3]	VDD	PCIE_RXD_ _n [2]	VDDA_PLL	PCIE_RXD_ _n [1]	VDD	PCIE_RXD_ _n [0]	PCI_SEL100	VDD_PCI	PCI_INTD _n	PCI_INTB _n
N	SR_DOUT	SR_CS _n	NC	NC	VSS	PCIE_RXD_ _p [3]	VSS	PCIE_RXD_ _p [2]	VSS	PCIE_RXD_ _p [1]	PWRUP_EXT_CL K_SEL	PCIE_RXD_ _p [0]	VSSA_PLL	PCI_CLKO[4]	JTAG_TDI	JTAG_TDO
P	TEST_BIDR_CTL	NC	NC	NC	VDDA_PCIE	VSS	VSS	VSS	VDD_PCIE	VSS	VSS	VSS	VDDA_PCIE	PWRUP_PLL_BY PASS _n	JTAG_TMS	JTAG_TCK
R	PCIE_LANE_VAL ID _n [3]	PCIE_LANE_VAL ID _n [1]	NC	NC	VDD_PCIE	PCIE_TXD_ _p [3]	VDD_PCIE	PCIE_TXD_ _p [2]	VDD_PCIE	PCIE_TXD_ _p [1]	VDD_PCIE	PCIE_TXD_ _p [0]	VDD_PCIE	PCIE_REFCLK_ _n	TEST_BCE	JTAG_TRST _n
T	PCIE_LANE_VAL ID _n [2]	PCIE_LANE_VAL ID _n [0]	NC	NC	VSS	PCIE_TXD_ _n [3]	NC	PCIE_TXD_ _n [2]	VSS	PCIE_TXD_ _n [1]	NC	PCIE_TXD_ _n [0]	VSS	PCIE_REFCLK_ _p	TEST_ON	PCIE_PERST _n

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Revision History

August 2009, 80E1000_PN001_02 -- This version of the document does not include any technical changes.

October 2006, 80E1000_PN001_01 -- This is the first version of the document.

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