

RC32308A001

FemtoClock3 Family Custom Configuration

General Description

This document details the custom configuration that is programmed into the one time programmable (OTP) memory of the RC32308A001. Please refer to the device datasheet for further information about the device.

Configuration List

Configuration Name	Configuration Index
config0	config_0
config1	config_1
config2	config_2
config3	config_3

Output Frequency Overview

Config Index	OUT0	OUT1	OUT3	OUT5
config_0	-	-	-	-
config_1	-	-	-	-
config_2	-	-	-	-
config_3	-	-	-	-

Config Index	OUT6	OUT7	OUT8	OUT9
config_0	-	-	-	-
config_1	-	-	-	-
config_2	-	-	-	-
config_3	-	-	-	-

Configuration Selection Overview: Static Multi Config

Config Slot	Config Selection 1	Config Selection 0	Config Index
slot_0	GPIO1 Low	GPIO0 Low	config_0
slot_1	GPIO1 Low	GPIO0 High	config_1
slot_2	GPIO1 High	GPIO0 Low	config_2
slot_3	GPIO1 High	GPIO0 High	config_3

Serial Interface Configuration

Config Index	Serial Port Configuration
config_0	I2C (1-byte address), 7-bit address: 0 0 0 1 A2 A1 A0

config_1	I2C (1-byte address), 7-bit address: 0 0 0 1 A2 A1 A0
config_2	I2C (1-byte address), 7-bit address: 0 0 0 1 A2 A1 A0
config_3	I2C (1-byte address), 7-bit address: 0 0 0 1 A2 A1 A0

I2C Address Selection Bits

Config Index	I2C Address Bit A2	I2C Address Bit A1	I2C Address Bit A0
config_0	0	SDO (PIN 7)	nCS (PIN 6)
config_1	0	SDO (PIN 7)	nCS (PIN 6)
config_2	0	SDO (PIN 7)	nCS (PIN 6)
config_3	0	SDO (PIN 7)	nCS (PIN 6)

GPIO Startup Configuration

Pin Number	GPIO	Function Description
47	GPIO0	CONFIG_SEL0
33	GPIO1	CONFIG_SEL1
32	GPIO2	N/A
28	GPIO3	N/A
2	LOCK	N/A

VDD Pins

Property	Value
VDD_VCO	1.8V
VDDXO_DCD	1.8V
VDDD33_SERIAL	3.3V
VDDD33_DIA	1.8V
VDD_CLK	1.8V
VDD_FOD0	1.8V
VDDO0	1.8V
VDDO1	1.8V
VDDO3	1.8V
VDDO5	1.8V
VDDO6	1.8V
VDDO7	1.8V
VDDO8_FOD1	1.8V
VDDO9_FOD2	1.8V

config0 (config_0) General Overview

Property	Value
Serial Interface	I2C (1-byte address), 7-bit address: 0 0 0 1 A2 A1 A0
Operation Mode	Synthesizer
External EEPROM Load	Enabled
XIN	73
Crystal CL	8.24pF
VCO Frequency	10.625GHz
CLKIN0	DISABLED
CLKIN1	DISABLED
CLKIN3	DISABLED
APLL Loop BW	~551.6862kHz
Lock BW	~61.7195Hz
Acquire BW	~8.8876kHz

config0 (config_0) GPIO Settings

Pin Number	GPIO	Function Description	Internal PU	Internal PD	Output Drive Strength
47	GPIO0	General purpose input (input)	Enable	Disable	N/A
33	GPIO1	General purpose input (input)	Enable	Disable	N/A
32	GPIO2	General purpose input (input)	Enable	Disable	N/A
28	GPIO3	General purpose input (input)	Enable	Disable	N/A
2	GPIO8	APLL lock (from frequency-based lock detect) (output)	Enable	Disable	CMOS Output mode and power supply of 1.8V.

config0 (config_0) Output Overview

Output	IOD Mux Selection	Frequency	Status	Output Type	Output Boost
OUT0	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT1	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT3	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT5	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT6	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT7	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT8	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT9	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-

Note: All VDDOs need to ramp before or at the same time as other cores power rails.

config0 (config_0) External EEPROM Settings

Property	Value
Part Number	R1EX24064ASA
Address	0x51
Address Size	2-byte address
I2C Speed	100kHz
Length	8KB

config1 (config_1) General Overview

Property	Value
Serial Interface	I2C (1-byte address), 7-bit address: 0 0 0 1 A2 A1 A0
Operation Mode	Synthesizer
External EEPROM Load	Enabled
XIN	73
Crystal CL	8.24pF
VCO Frequency	10.625GHz
CLKIN0	DISABLED
CLKIN1	DISABLED
CLKIN3	DISABLED
APLL Loop BW	~551.6862kHz
Lock BW	~61.7195Hz
Acquire BW	~8.8876kHz

config1 (config_1) GPIO Settings

Pin Number	GPIO	Function Description	Internal PU	Internal PD	Output Drive Strength
47	GPIO0	General purpose input (input)	Enable	Disable	N/A
33	GPIO1	General purpose input (input)	Enable	Disable	N/A
32	GPIO2	General purpose input (input)	Enable	Disable	N/A
28	GPIO3	General purpose input (input)	Enable	Disable	N/A
2	GPIO8	APLL lock (from frequency-based lock detect) (output)	Enable	Disable	CMOS Output mode and power supply of 1.8V.

config1 (config_1) Output Overview

Output	IOD Mux Selection	Frequency	Status	Output Type	Output Boost
OUT0	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT1	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT3	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT5	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT6	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT7	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT8	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT9	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-

Note: All VDDOs need to ramp before or at the same time as other cores power rails.

config1 (config_1) External EEPROM Settings

Property	Value
Part Number	R1EX24064ASA
Address	0x52
Address Size	2-byte address
I2C Speed	100kHz
Length	8KB

config2 (config_2) General Overview

Property	Value
Serial Interface	I2C (1-byte address), 7-bit address: 0 0 0 1 A2 A1 A0
Operation Mode	Synthesizer
External EEPROM Load	Enabled
XIN	73
Crystal CL	8.24pF
VCO Frequency	10.625GHz
CLKIN0	DISABLED
CLKIN1	DISABLED
CLKIN3	DISABLED
APLL Loop BW	~551.6862kHz
Lock BW	~61.7195Hz
Acquire BW	~8.8876kHz

config2 (config_2) GPIO Settings

Pin Number	GPIO	Function Description	Internal PU	Internal PD	Output Drive Strength
47	GPIO0	General purpose input (input)	Enable	Disable	N/A
33	GPIO1	General purpose input (input)	Enable	Disable	N/A
32	GPIO2	General purpose input (input)	Enable	Disable	N/A
28	GPIO3	General purpose input (input)	Enable	Disable	N/A
2	GPIO8	APLL lock (from frequency-based lock detect) (output)	Enable	Disable	CMOS Output mode and power supply of 1.8V.

config2 (config_2) Output Overview

Output	IOD Mux Selection	Frequency	Status	Output Type	Output Boost
OUT0	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT1	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT3	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT5	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT6	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT7	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT8	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT9	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-

Note: All VDDOs need to ramp before or at the same time as other cores power rails.

config2 (config_2) External EEPROM Settings

Property	Value
Part Number	R1EX24064ASA
Address	0x53
Address Size	2-byte address
I2C Speed	100kHz
Length	8KB

config3 (config_3) General Overview

Property	Value
Serial Interface	I2C (1-byte address), 7-bit address: 0 0 0 1 A2 A1 A0
Operation Mode	Synthesizer
External EEPROM Load	Enabled
XIN	73
Crystal CL	8.24pF
VCO Frequency	10.625GHz
CLKIN0	DISABLED
CLKIN1	DISABLED
CLKIN3	DISABLED
APLL Loop BW	~551.6862kHz
Lock BW	~61.7195Hz
Acquire BW	~8.8876kHz

config3 (config_3) GPIO Settings

Pin Number	GPIO	Function Description	Internal PU	Internal PD	Output Drive Strength
47	GPIO0	General purpose input (input)	Enable	Disable	N/A
33	GPIO1	General purpose input (input)	Enable	Disable	N/A
32	GPIO2	General purpose input (input)	Enable	Disable	N/A
28	GPIO3	General purpose input (input)	Enable	Disable	N/A
2	GPIO8	APLL lock (from frequency-based lock detect) (output)	Enable	Disable	CMOS Output mode and power supply of 1.8V.

config3 (config_3) Output Overview

Output	IOD Mux Selection	Frequency	Status	Output Type	Output Boost
OUT0	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT1	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT3	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT5	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT6	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT7	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT8	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-
OUT9	VCO/2	-	disabled	DISABLED (Hi-Z/Hi-Z)	-

Note: All VDDOs need to ramp before or at the same time as other cores power rails.

config3 (config_3) External EEPROM Settings

Property	Value
Part Number	R1EX24064ASA
Address	0x57
Address Size	2-byte address
I2C Speed	100kHz
Length	8KB

Ordering Info

Part Number	Carrier Type
RC32308A001GNE#BB0	Tray
RC32308A001GNE#KB0	Tape and Reel

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