

To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended
for new design

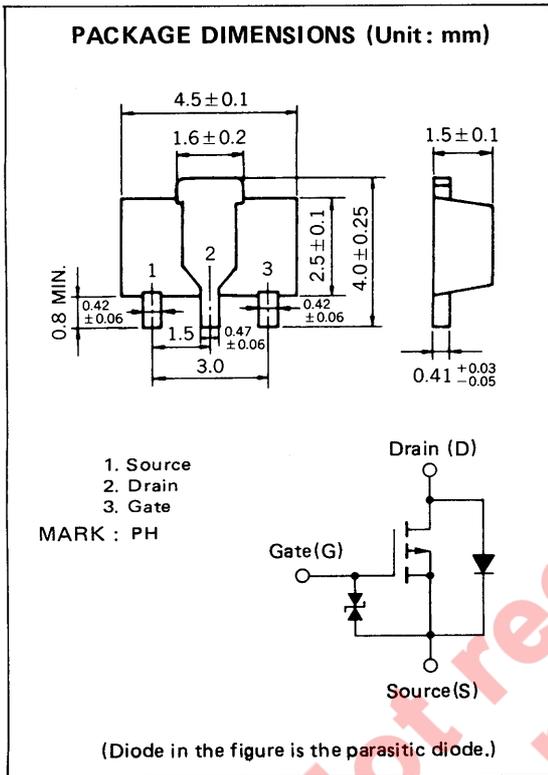
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(Note 2) “Renesas Electronics product(s)” means any product developed or manufactured by or for Renesas Electronics.

P-CHANNEL MOS FET
FOR SWITCHING



The 2SJ206, P-channel vertical type MOS FET, is a switching device which can be driven directly by the output of ICs having a 5 V power source.

As the MOS FET has low on-state resistance and excellent switching characteristics, it is suitable for driving actuators such as motors, relays, and solenoids.

FEATURES

- Directly driven by ICs having a 5 V power supply.
- Has low on-state resistance

$$R_{DS(on)} = 4.0 \Omega \text{ MAX. @ } V_{GS} = -4.0 \text{ V, } I_D = -0.3 \text{ A}$$

$$R_{DS(on)} = 3.0 \Omega \text{ MAX. @ } V_{GS} = -10 \text{ V, } I_D = -0.3 \text{ A}$$

QUALITY GRADE

Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS (T_a = 25 °C)

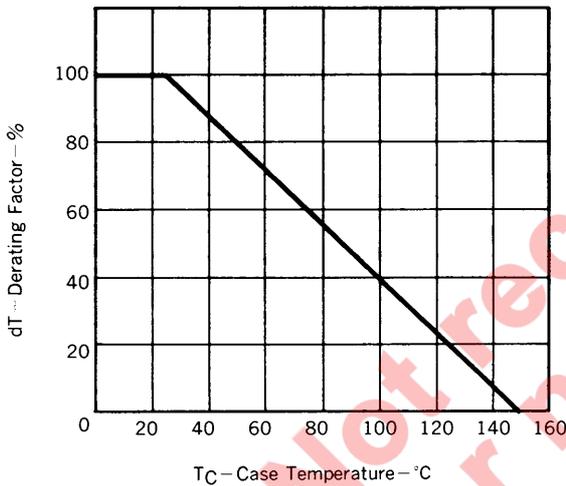
PARAMETER	SYMBOL	RATINGS	UNIT	TEST CONDITIONS
Drain to Source Voltage	V _{DSS}	-30	V	V _{GS} = 0
Gate to Source Voltage	V _{GSS}	±20	V	V _{DS} = 0
Drain Current	I _D (DC)	±500	mA	
Drain Current	I _D (pulse)	±1.0	A	PW ≤ 10 ms, Duty Cycle ≤ 50 %
Total Power Dissipation	P _T	2.0	W	When using ceramic board of 16 cm ² x 0.7 mm
Channel Temperature	T _{ch}	150	°C	
Storage Temperature	T _{stg}	-55 to +150	°C	

ELECTRICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

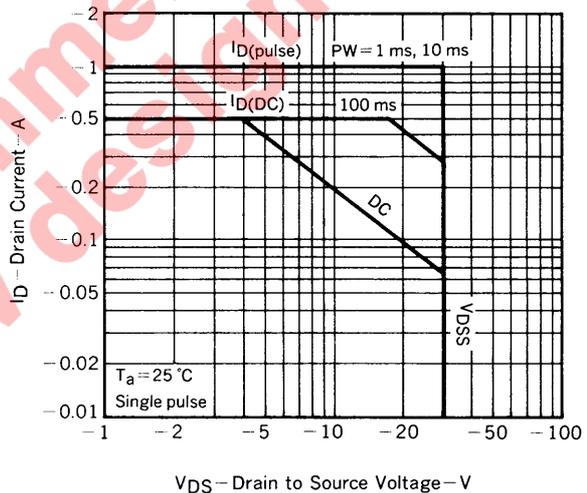
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Cut-off Current	I_{DSS}			-1.0	μA	$V_{DS} = -30\text{ V}, V_{GS} = 0$
Gate Leakage Current	I_{GSS}			± 5	μA	$V_{GS} = \pm 16\text{ V}, V_{DS} = 0$
Gate Cut-off Voltage	$V_{GS(off)}$	-1.0	-2.3	-3.0	V	$V_{DS} = -5\text{ V}, I_D = -1\text{ mA}$
Forward Transfer Admittance	$ y_{fs} $	0.4			S	$V_{DS} = -5\text{ V}, I_D = -0.3\text{ A}$
Drain to Source On-State Resistance	$R_{DS(on)1}$		2.0	4.0	Ω	$V_{GS} = -4.0\text{ V}, I_D = -0.3\text{ A}$
Drain to Source On-State Resistance	$R_{DS(on)2}$		0.8	3.0	Ω	$V_{GS} = -10\text{ V}, I_D = -0.3\text{ A}$
Input Capacitance	C_{iss}		100		pF	$V_{DS} = -5\text{ V}, V_{GS} = 0, f = 1\text{ MHz}$
Output Capacitance	C_{oss}		80		pF	
Feedback Capacitance	C_{rss}		15		pF	
Turn-On Delay Time	$t_{d(on)}$		120		ns	$V_{GS(on)} = -4\text{ V}, R_G = 10\ \Omega, V_{DD} = -5\text{ V}, I_D = -0.3\text{ A}, R_L = 17\ \Omega$
Rise Time	t_r		420		ns	
Turn-Off Delay Time	$t_{d(off)}$		75		ns	
Fall Time	t_f		140		ns	

TYPICAL CHARACTERISTICS ($T_a = 25^\circ\text{C}$)

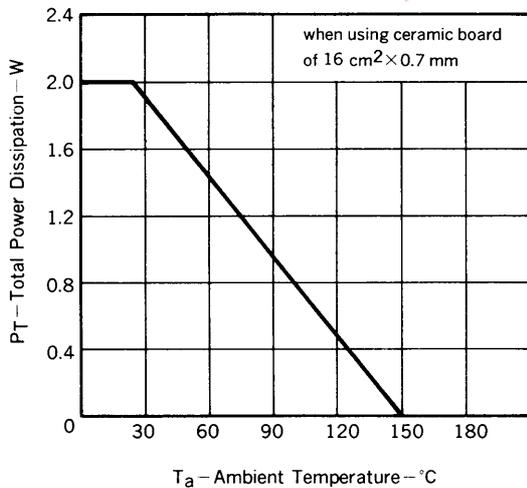
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



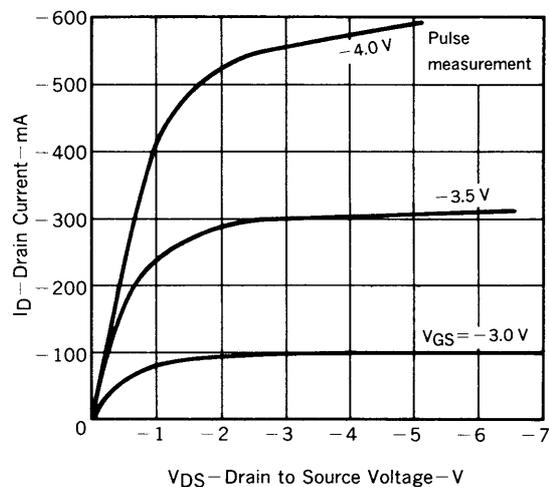
FORWARD BIAS SAFE OPERATING AREA

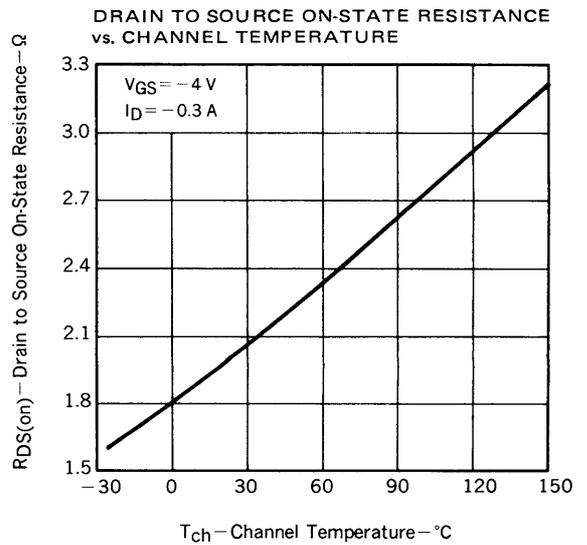
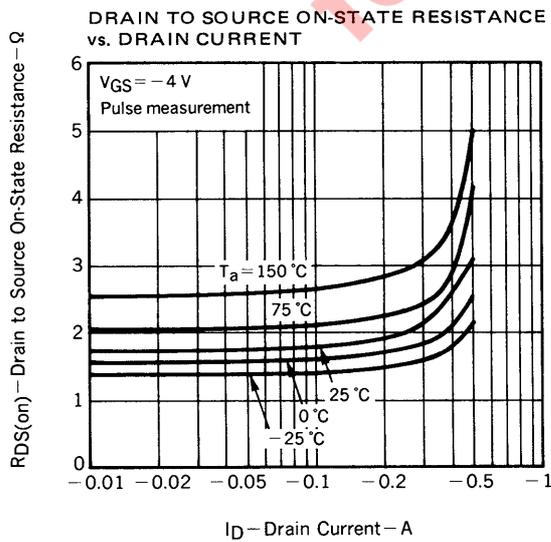
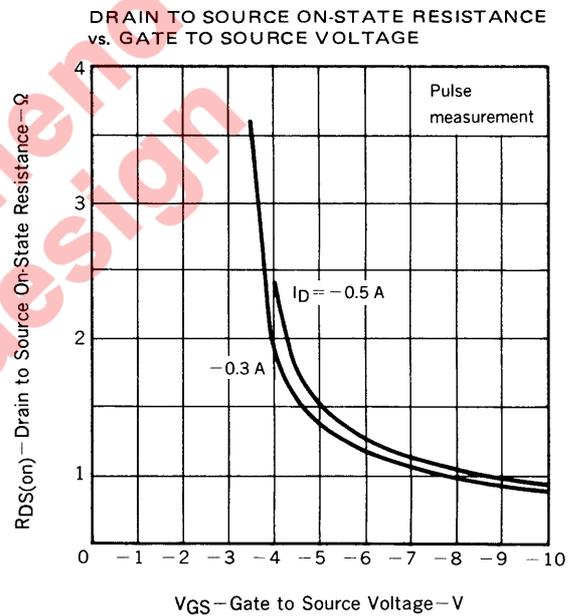
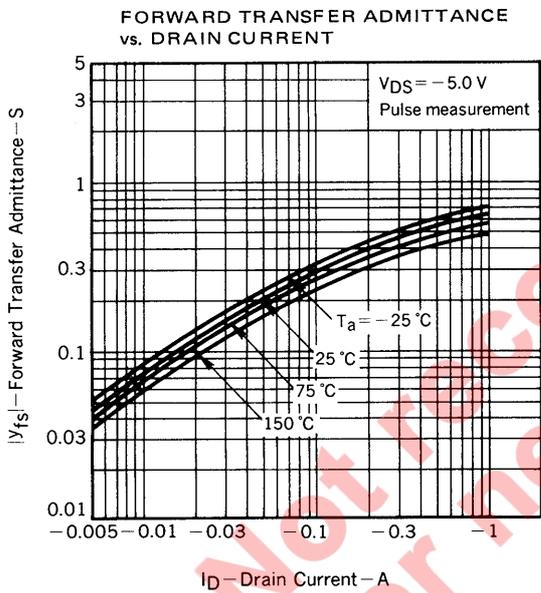
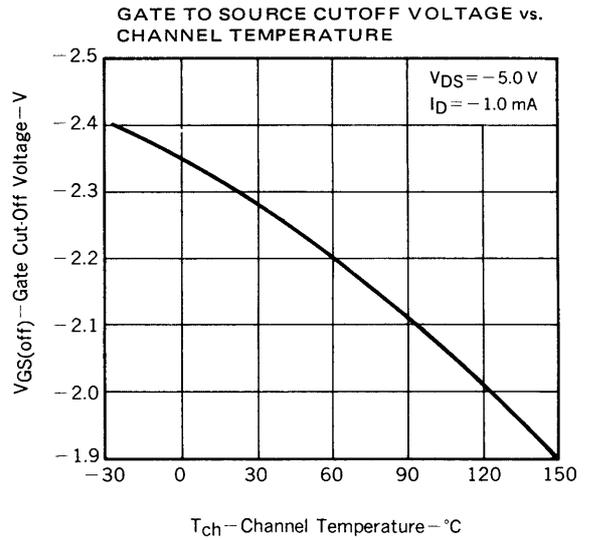
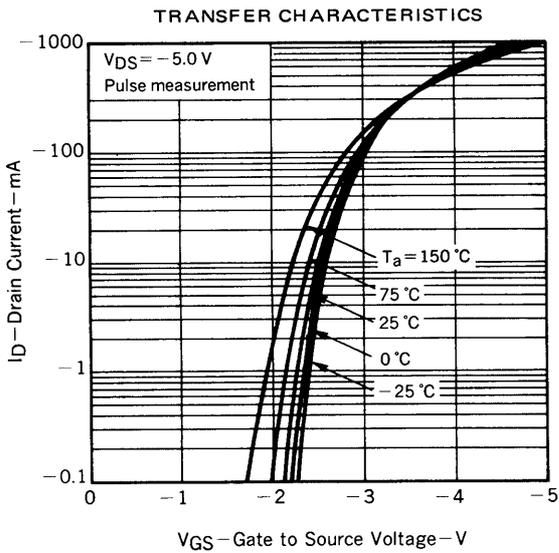


TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE

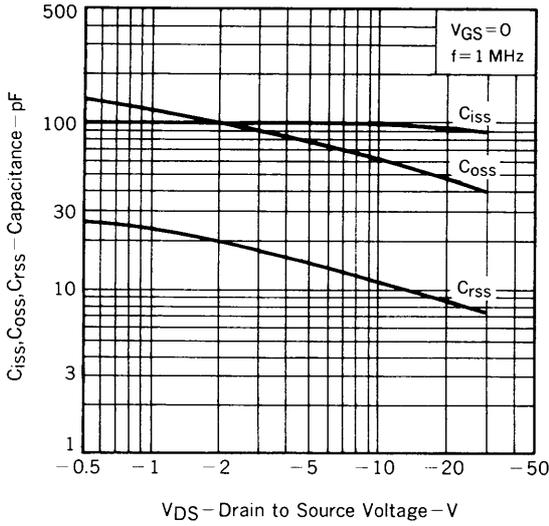


DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE

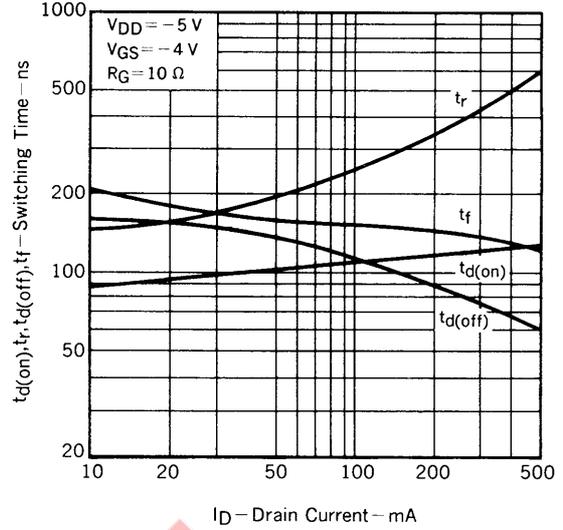




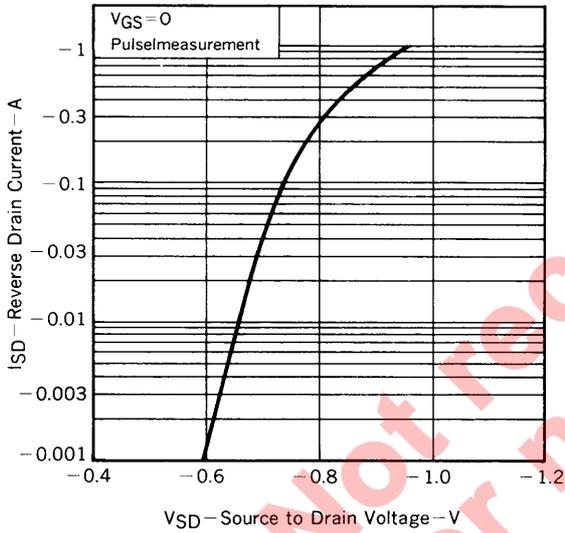
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



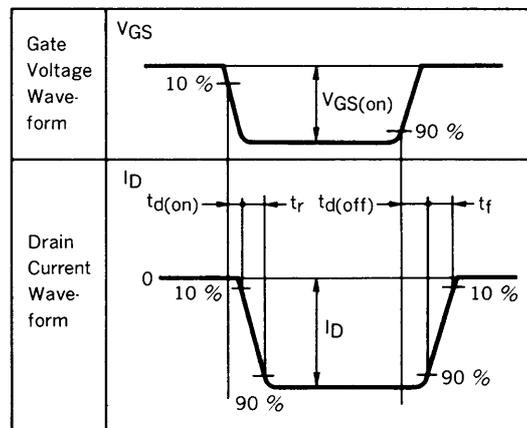
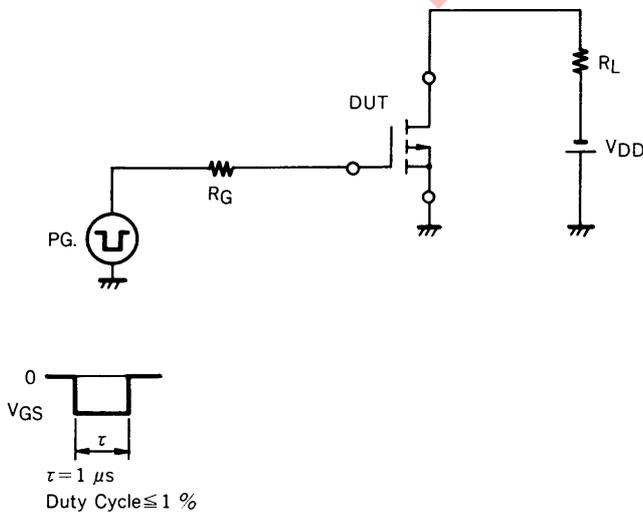
SWITCHING CHARACTERISTICS



SOURCE TO DRAIN DIODE FORWARD VOLTAGE



SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS



RECOMMENDED SOLDERING CONDITIONS

Mounting of this product by soldering should be done under the following conditions.

Please consult with our representatives about soldering methods and conditions other than these recommended.

SURFACE MOUNT TYPE

For details of the recommended soldering conditions, see the information document.

“Device Mounting Manual for Surface Mounting (IEI-1207).”

Soldering Method	Soldering Conditions	Symbol for Recommended Conditions
Infrared Reflow	Package peak temp.: 230 °C Soldering time: within 30 sec (above 210 °C) Soldering times: 1, Days limitation: none*	IR30-00
Vapor Phase Soldering	Package peak temp.: 215 °C Soldering time: within 40 sec (above 200 °C) Soldering times: 1, Days limitation: none*	VP15-00
Wave Soldering	Soldering bath temp.: below 260 °C Soldering time: within 10 sec Soldering times: 1, Days limitation: none*	WS60-00

* Stored days under storage conditions at 25 °C and below 65 % R.H. after dry-pack opened.

Note 1: Combination of soldering methods should be avoided.

REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134

Not recommended for new design

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Application examples recommended by NEC Corporation

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tools, Industrial robots, Audio and Visual equipment, Other consumer products, etc.

Special: Automotive and Transportation equipment, Traffic control systems, Antidisaster systems, Anticrime systems, etc.