To our customers,

Old Company Name in Catalogs and Other Documents

On April 1st, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

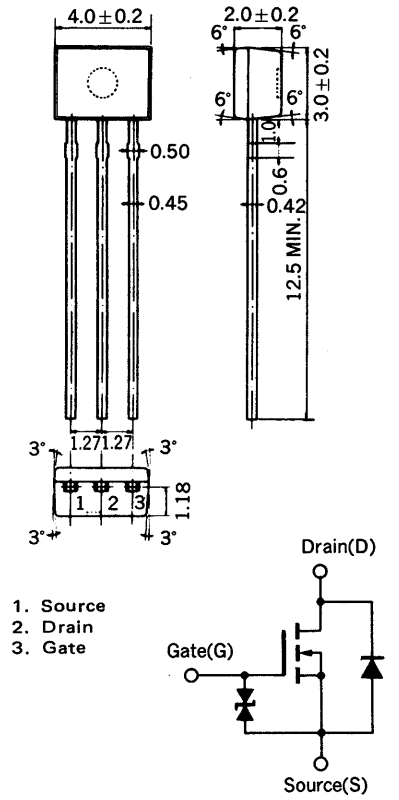
MOS FIELD EFFECT TRANSISTOR

Phase-out/Discontinued

2SK1132

N-CHANNEL MOS FET
FOR HIGH SPEED SWITCHING

PACKAGE DIMENSIONS (Unit : mm)



1. Source
2. Drain
3. Gate

(Diode in the figure is the parasitic diode.)

The 2SK1132, N-channel vertical type MOS FET, is a switching device which can be driven directly by the output of ICs having a 5 V power source.

The MOS FET has excellent switching characteristics and is suitable for use as a high-speed switching device in digital circuits.

FEATURES

- Directly driven by ICs having a 5 V power source.
- Not necessary to consider driving current because of its high input impedance.
- Possible to reduce the number of parts by omitting the bias resistor.
- Can be replaced with any resistor self-contained type transistor.
- Can be used complementarily with the 2SJ165.

QUALITY GRADE

Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

ABSOLUTE MAXIMUM RATINGS (T_a = 25 °C)

PARAMETER	SYMBOL	RATINGS	UNIT	TEST CONDITIONS
Drain to Source Voltage	V_{DSS}	50	V	V_{GS} = 0
Gate to Source Voltage	V_{GSS}	±7.0	V	V_{DS} = 0
Drain Current	$I_{D(DC)}$	±100	mA	
Drain Current	$I_{D(pulse)}$	±200	mA	PW ≦ 10 ms, Duty Cycle ≦ 50 %
Total Power Dissipation	P_T	250	mW	
Channel Temperature	T_{ch}	150	°C	
Storage Temperature	T_{stg}	−55 to +150	°C	

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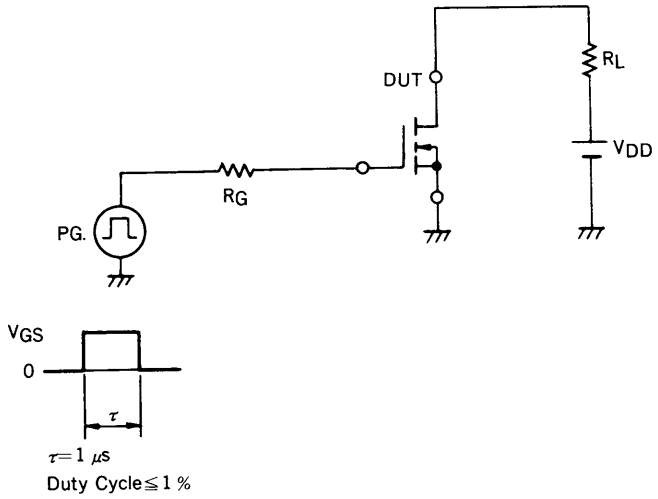
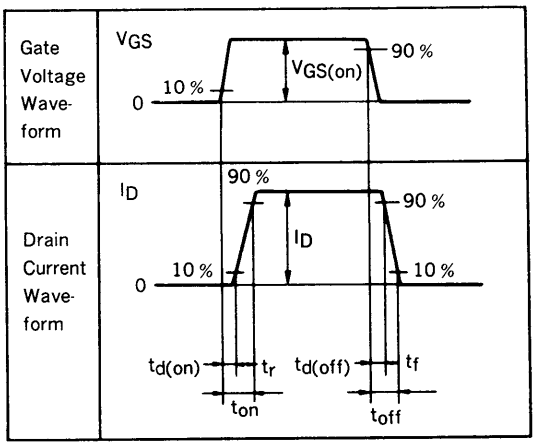
ELECTRICAL CHARACTERISTICS (T_a = 25 °C)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Cut-off Current	I_{DSS}			10	μA	V_{DS} = 50 V, V_{GS} = 0
Gate Leakage Current	I_{GSS}			±10	μA	V_{GS} = ±7 V, V_{DS} = 0
Gate Cut-off Voltage	$V_{GS(off)}$	1.0	1.7	2.0	V	V_{DS} = 5.0 V, I_D = 1.0 μA
Forward Transfer Admittance	$\|y_{fs}\|$	20	40		mS	V_{DS} = 5.0 V, I_D = 20 mA
Drain to Source On-State Resistance	$R_{DS(on)}$		16	50	Ω	V_{GS} = 4.0 V, I_D = 20 mA
Input Capacitance	C_{iss}		7		pF	V_{DS} = 5.0 V, V_{GS} = 0, f = 1 MHz
Output Capacitance	C_{oss}		6		pF	
Feedback Capacitance	C_{rss}		2		pF	
Turn-On Delay Time	$t_{d(on)}$		6		ns	$V_{GS(on)}$ = 5.0 V, R_G = 10 Ω V_{DD} = 5.0 V, I_D = 20 mA R_L = 250 Ω
Rise Time	t_r		25		ns	
Turn-Off Delay Time	$t_{d(off)}$		36		ns	
Fall Time	t_f		35		ns	

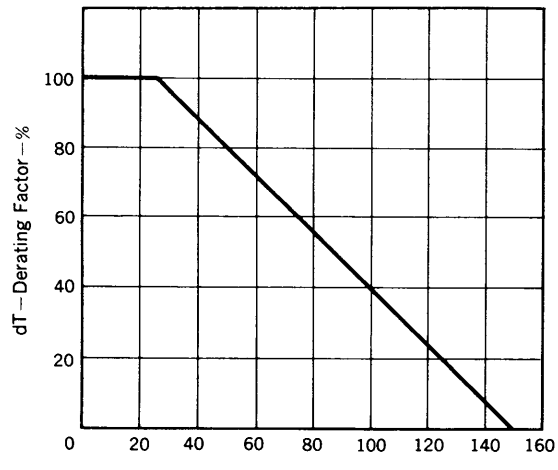
SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS



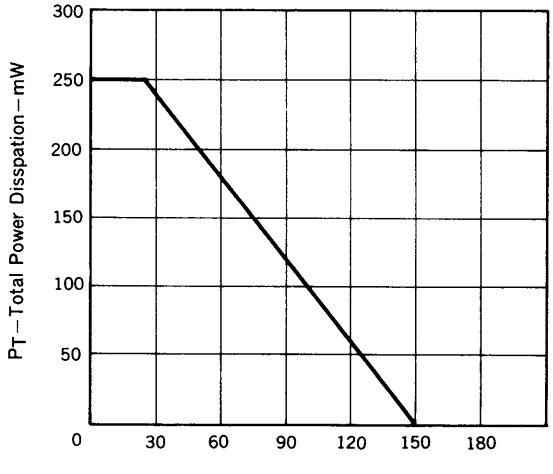


TYPICAL CHARACTERISTICS (T_a = 25 °C)

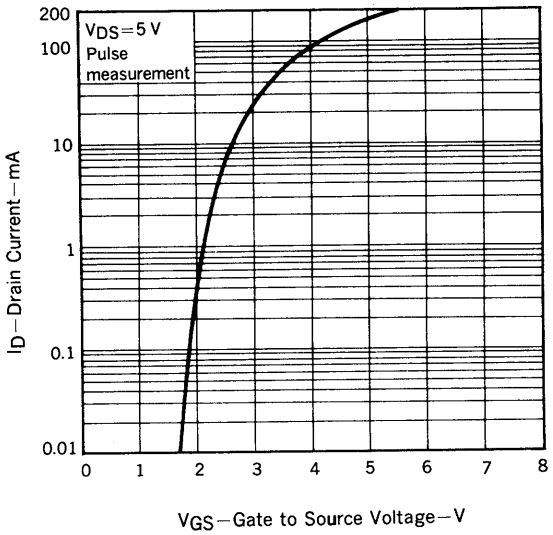
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



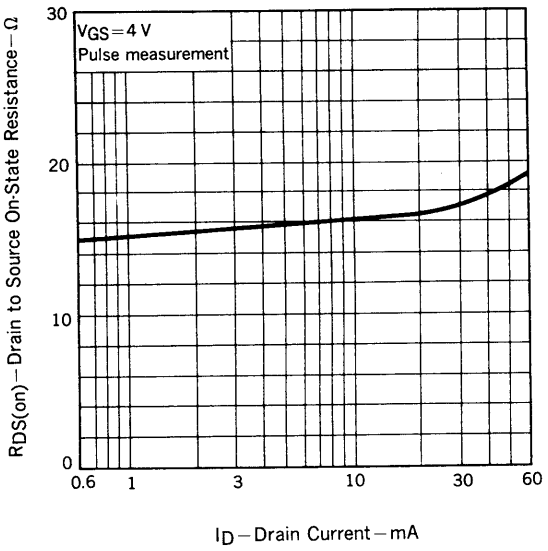
TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



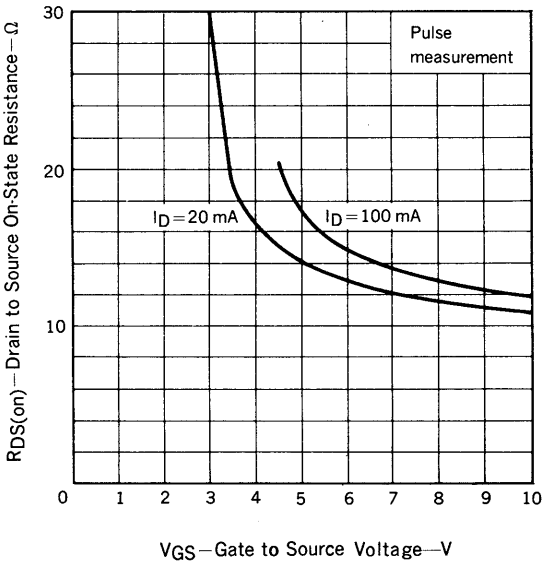
TRANSFER CHARACTERISTICS



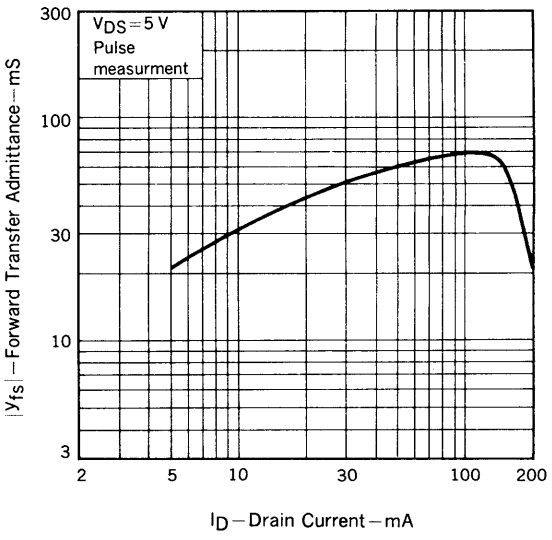
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



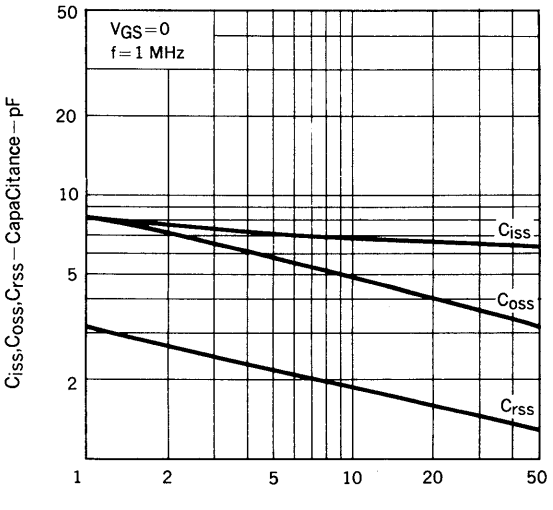
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



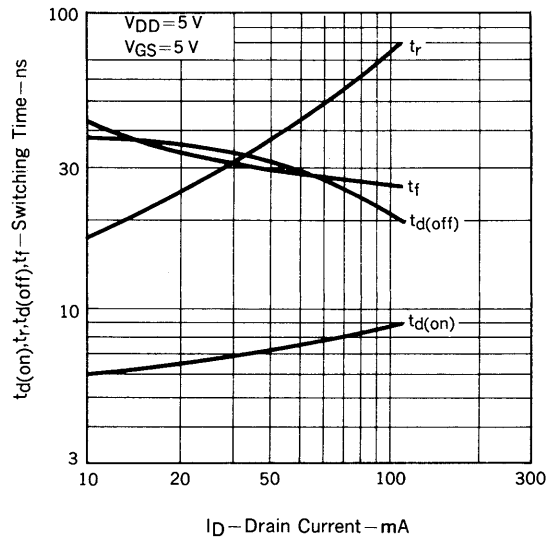
FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



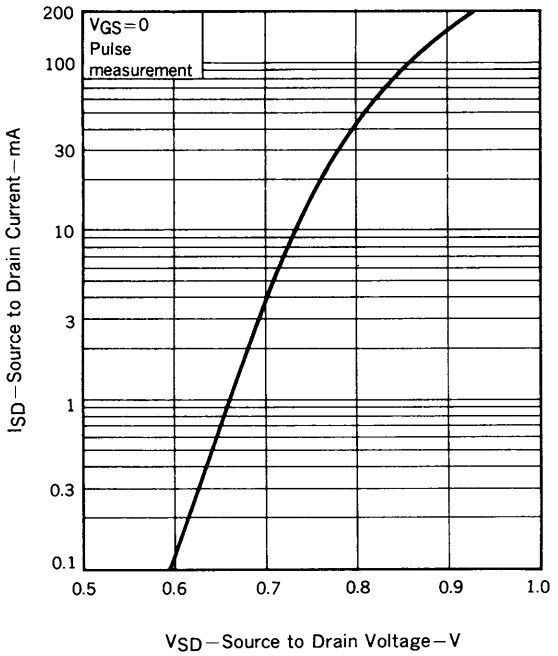
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



SWITCHING CHARACTERISTICS



SOURCE TO DRAIN DIODE
FORWARD VOLTAGE



RECOMMENDED SOLDERING CONDITIONS

Solder this product under the following recommended conditions.

For soldering methods or soldering conditions other than those recommended in the table, please consult our NEC salespeople.

Insert type

Soldering method	Soldering conditions	Recommended condition code
Wave soldering	Solder bath temperature: 260 °C max. Soldering time: 10 sec max.	

[MEMO]