

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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Not recommended  
for new design

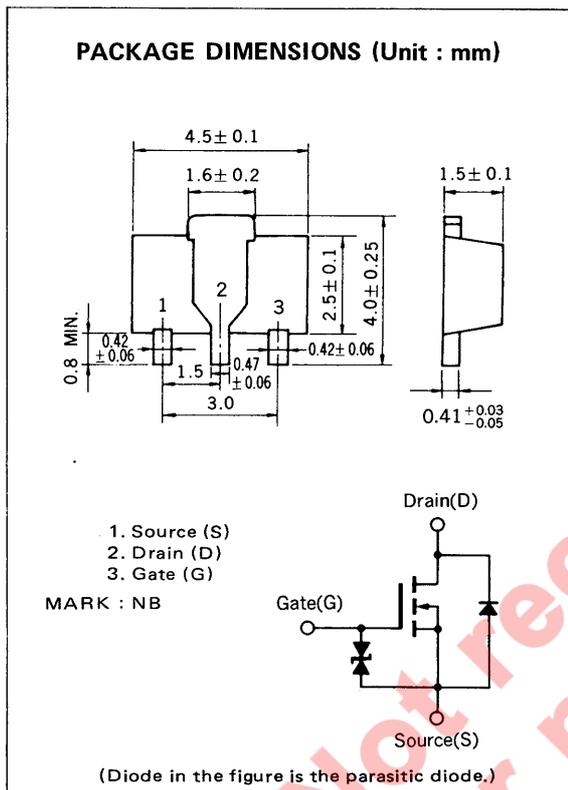
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N-CHANNEL MOS FET  
FOR SWITCHING



The 2SK1483 is an N-channel vertical type MOS FET switching device which can be directly driven from an IC operating with a 5 V single power supply. The device features low ON-state resistance and outstanding switching characteristics and thus is ideal for driving actuators such as motors, solenoids, and relays.

**FEATURES**

- Can be driven directly from an IC operating with a 5 V single power supply.
- Low ON-state resistance  
 $R_{DS(on)} = 0.8 \Omega$  MAX. at  $V_{GS} = 4 V, I_D = 0.5 A$   
 $R_{DS(on)} = 0.4 \Omega$  MAX. at  $V_{GS} = 10 V, I_D = 0.5 A$
- Can be used complementary with the 2SJ197.

**QUALITY GRADE**

Standard

Please refer to "Quality grade on NEC Semiconductor Devices" (Document number IEI-1209) published by NEC Corporation to know the specification of quality grade on the devices and its recommended applications.

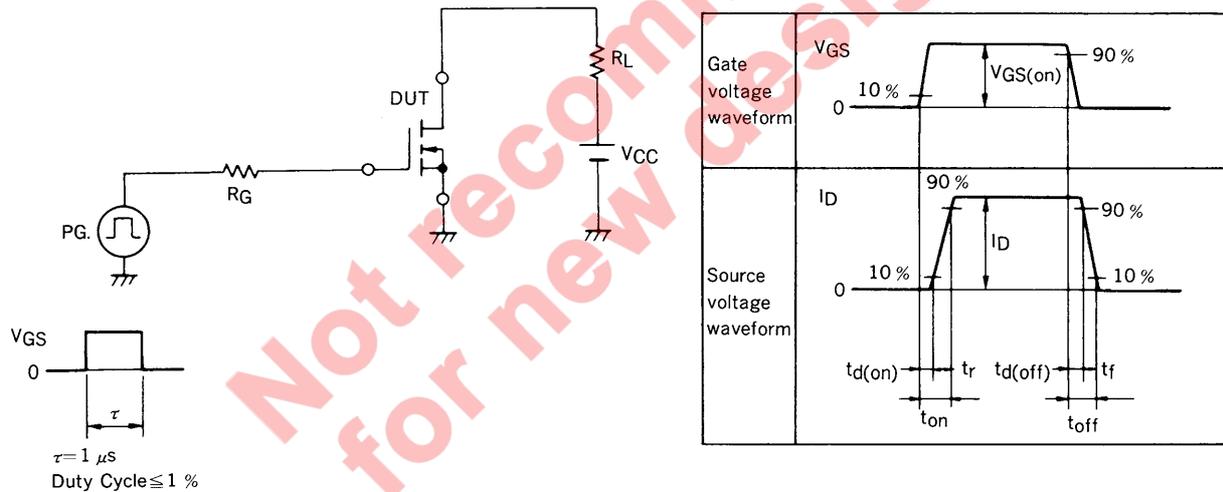
**ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ C$ )**

PARAMETER	SYMBOL	RATING	UNIT	TEST CONDITIONS
Drain to Source Voltage	$V_{DSS}$	30	V	$V_{GS} = 0$
Gate to Source Voltage	$V_{GSS}$	$\pm 20$	V	$V_{DS} = 0$
Drain Current (DC)	$I_D(DC)$	$\pm 2.0$	A	
Drain Current (pulse)	$I_D(pulse)$	$\pm 4.0$	A	$PW \leq 10 ms, Duty Cycle \leq 50 \%$
Total Power Dissipation	$P_T$	2.0	W	when using ceramic board of $16 cm^2 \times 0.7 mm$
Channel Temperature	$T_{ch}$	150	$^\circ C$	
Storage Temperature	$T_{stg}$	-55 to +150	$^\circ C$	

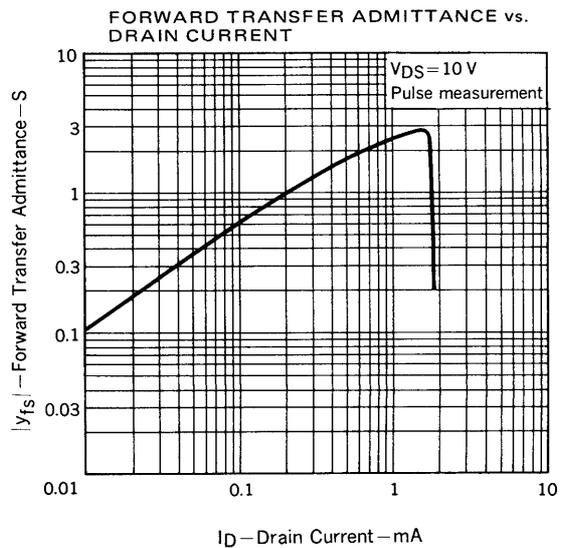
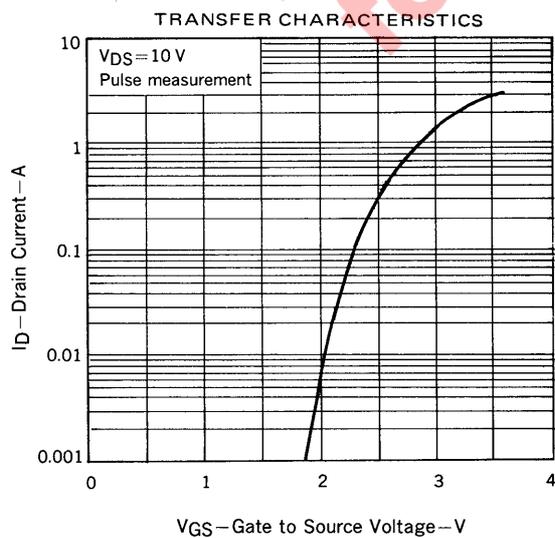
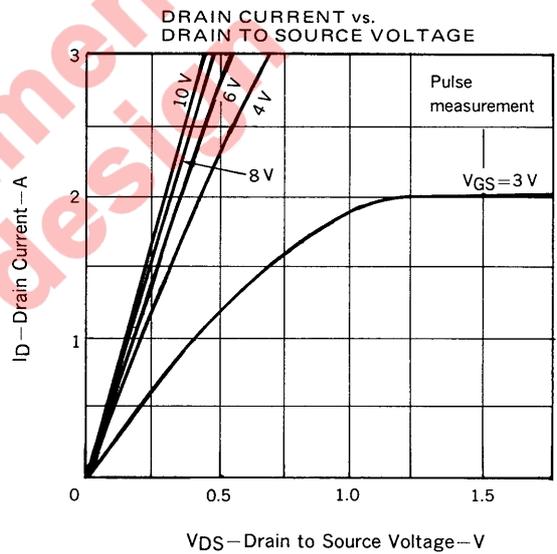
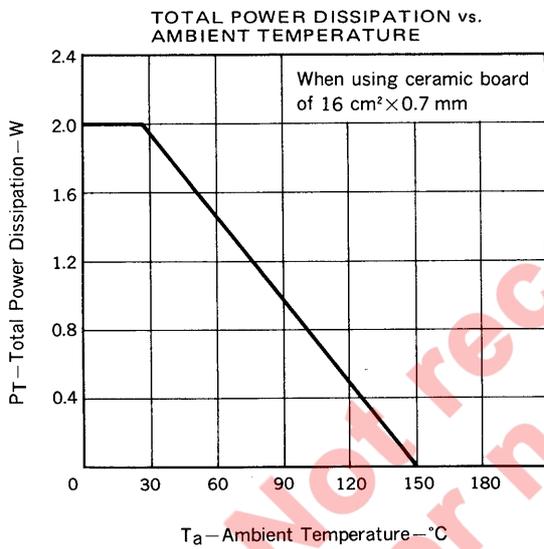
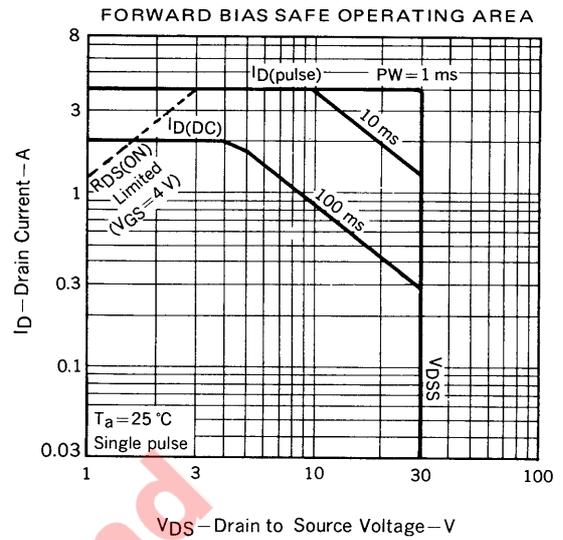
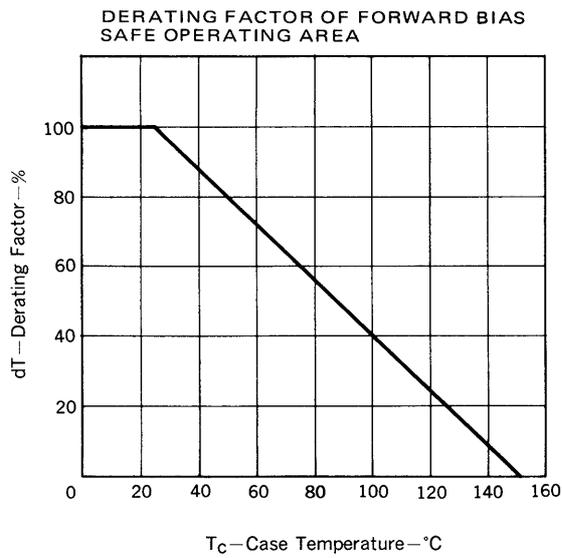
ELECTRICAL CHARACTERISTICS (T<sub>a</sub> = 25 °C)

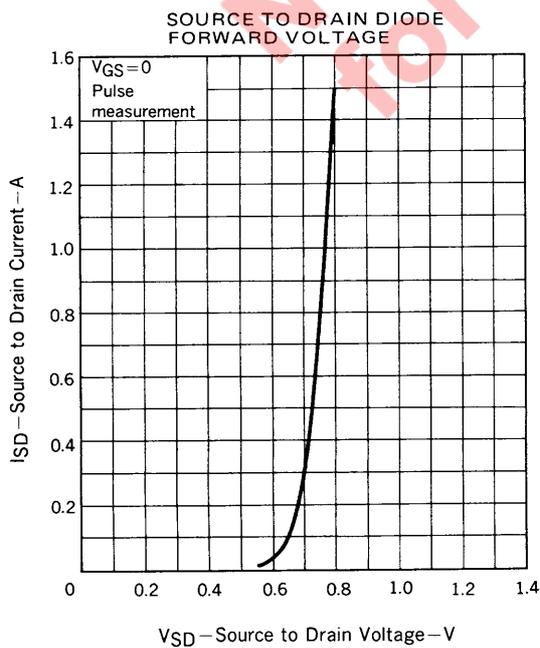
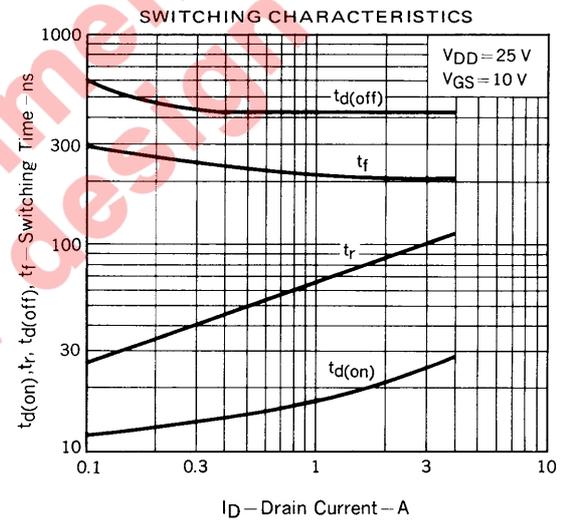
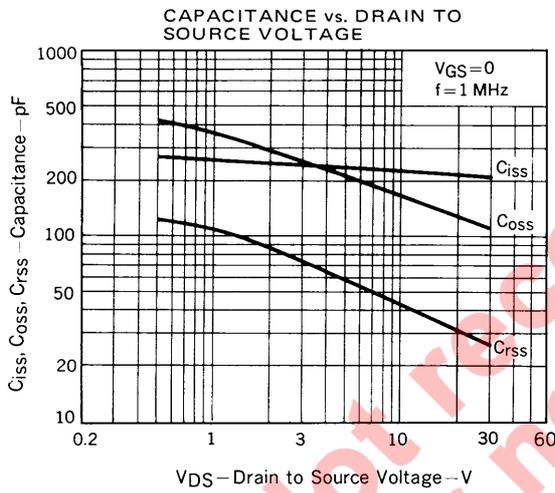
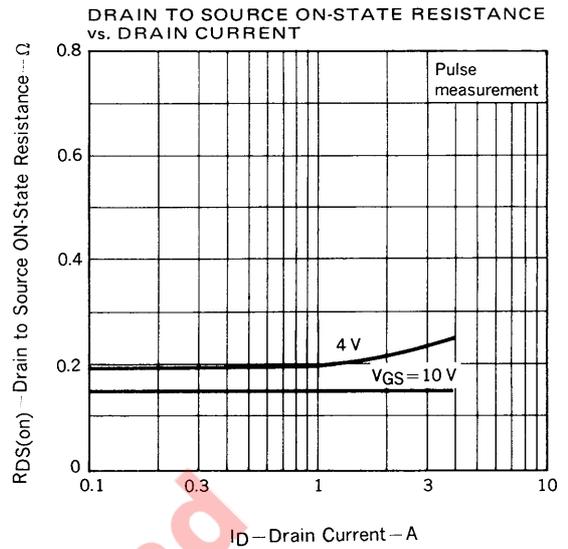
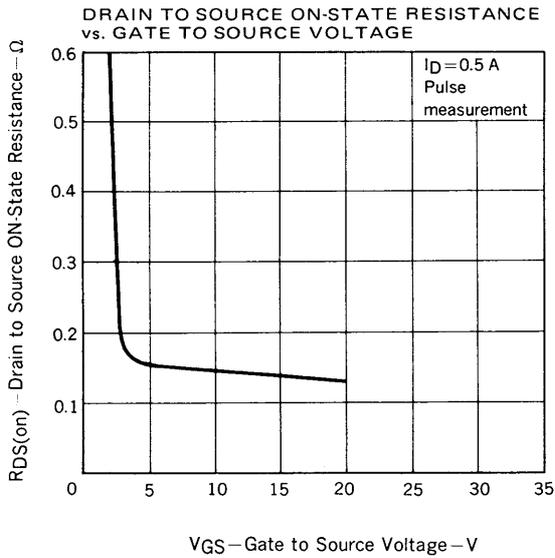
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Leakage Current	I <sub>DSS</sub>			10	μA	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0
Gate Leakage Current	I <sub>GSS</sub>			±10	μA	V <sub>GS</sub> = ±20 V, V <sub>DS</sub> = 0
Gate Cutoff Voltage	V <sub>GS(off)</sub>	1.3	1.8	2.5	V	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA
Forward Transfer Admittance	y <sub>fs</sub>	0.4			S	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 0.5 A
Drain to Source On-State Resistance 1	R <sub>DS(on)1</sub>		0.19	0.8	Ω	V <sub>GS</sub> = 4.0 V, I <sub>D</sub> = 0.5 A
Drain to Source On-State Resistance 2	R <sub>DS(on)2</sub>		0.15	0.4	Ω	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.5 A
Input Capacitance	C <sub>iss</sub>		230		pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0, f = 1.0 MHz
Output Capacitance	C <sub>oss</sub>		170		pF	
Feedback Capacitance	C <sub>rss</sub>		45		pF	
On-State Delay Time	t <sub>d(on)</sub>		15		ns	V <sub>GS(on)</sub> = 10 V, R <sub>G</sub> = 10 Ω V <sub>DD</sub> = 25 V, I <sub>D</sub> = 0.5 A R <sub>L</sub> = 50 Ω
Rise Time	t <sub>r</sub>		50		ns	
Off-State Delay Time	t <sub>d(off)</sub>		420		ns	
Fall Time	t <sub>f</sub>		240		ns	

SWITCHING TIME MEASUREMENT CIRCUIT & MEASUREMENT CONDITIONS



CHARACTERISTIC CURVES ( $T_a = 25^\circ\text{C}$ )





**RECOMMENDED SOLDERING CONDITIONS**

Solder this product under the following recommended conditions.

For soldering methods or soldering conditions other than those recommended in the table, please consult our NEC salespeople.

**SURFACE MOUNT TYPE**

For details of the soldering conditions, refer to the surface-mount type Device Packaging Manual (IEI-1207).

Soldering method	Soldering conditions	Recommended condition code
Infrared reflow	Package peak temperature: 230 °C , Soldering time: 30 sec max. (at 210 °C min.) No. of applied times: Once Limit on No. of days*: None	IR30-00
VPS	Package peak temperature: 215 °C Soldering time: 40 sec max. (at 200 °C min.) No. of applied times: Once Limit on No. of days*: None	VP15-00
Wave soldering	Solder bath temperature: 260 °C max. Soldering time: 10 sec max. No. of applied times: Once Limit on No. of days*: None	WS60-00

\*: Refers to the number of days to be kept in storage after unsealing the dry pack. The storage conditions are 25 °C and 65 % RH.

**Note:** Do not use any two of the above soldering methods in combination.

**REFERENCE**

Document Name	Document No.
NEC semiconductor device reliability/quality control system.	TEI-1202
Quality grade on NEC semiconductor devices.	IEI-1209
Semiconductor device mounting technology manual.	IEI-1207
Semiconductor device package manual.	IEI-1213
Guide to quality assurance for semiconductor devices.	MEI-1202
Semiconductor selection guide.	MF-1134

Not recommended for new design

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Application examples recommended by NEC Corporation

Standard: Computer, Office equipment, Communication equipment, Test and Measurement equipment, Machine tools, Industrial robots, Audio and Visual equipment, Other consumer products, etc.

Special: Automotive and Transportation equipment, Traffic control systems, Antidisaster systems, Anticrime systems, etc.