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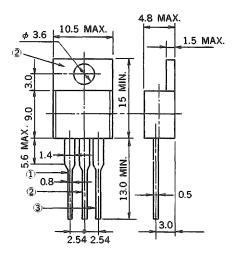


### **THYRISTORS**

# 8P2M,8P4M

### 8 A(12 A<sub>r.m.s.</sub>)THYRISTOR

# PACKAGE DIMENSIONS in millimeters



#### Pin Connection

- (Î) Cathode
- ② Anode
- ③ Gate

The 8P2M and 8P4M are P gate all diffused mold type Thyristor granted 8 Amp On-state Average Current ( $T_c = 90$  °C), with voltages up to 400 volts.

#### **FEATURES**

- Easy installation by TO-220 AB package.
- 100 A surge current.
- High Voltage.
  - $: V_{DRM}, V_{RRM} = 200 V (8P2M)$
  - $: V_{DRM}, V_{RRM} = 400 V (8P4M)$

#### **APPLICATIONS**

- Motor speed control for household appliance.
- Temperature control for heater and constant temperature box.
- Constant voltage power source and battery charger.
- Automotive application such as regulator.
- Various solid state relay etc.

#### **MAXIMUM RATINGS**

CHARACTERISTIC	SYMBOL	8P2M	8P4M	UNIT	NOTE
Non-Repetitive Peak Reverse Voltage	VRSM	300	500	V	
Non-Repetitive Peak Off-State Voltage	V <sub>DSM</sub>	300	500	V	<del></del>
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>	200	400	V	
Repetitive Peak Off-State Voltage	VDRM	200	400	V	
Average On-State Current	IT(AV)	8 ( $T_c = 90$ °C, $\theta = 180$ ° Single phase half wave)		Α	See Fig. 11
Surge On-State Current	ITSM	100		Α	See Fig. 2
Fusing Current	∫i⊤² dt	45 (1 ms ≤ t ≤ 10 ms)		A <sup>2</sup> s	· · · · · · · · · · · · · · · · · · ·
Peak Gate Power Dissipation	P <sub>GM</sub>	5 (f $\ge$ 50 Hz, Duty $\le$ 10 %)		w	C 5'- 0
Average Gate Power Dissipation	P <sub>G</sub> (AV)	0	.5	w	See Fig. 3
Peak Gate Forward Current	<sup>I</sup> FGM	2 (f ≥ 50 Hz, Duty ≤ 10 %)		Α	
Peak Gate Reverse Voltage	VRGM	10		V	
Junction Temperature	Tj	-40 to	°C		
Storage Temperature	T <sub>stg</sub>	-55 to +150		°c	
Weight			g		

### ELECTRICAL CHARACTERISTICS (T $_i$ = 25 °C)

CHARACTERISTIC	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT	NOTE	
Repetitive Peak Reverse Current	IRRM	V <sub>RM</sub> = V <sub>RRM</sub> , T <sub>j</sub> = 125 °C		_	2	mA		
Repetitive Peak Off-State Current	IDRM	$V_{DM} = V_{DRM}$ , $T_j = 125$ °C		_	2	mA		
On-State Voltage	V <sub>TM</sub>	I <sub>TM</sub> = 25 A	_	_	1.4	V	See Fig. 1	
Gate-Trigger Current	I <sub>GT</sub>	$V_{DM} = 6 V, R_{L} = 100 \Omega$	_	_	10	mA		
Gate-Trigger Voltage	V <sub>GT</sub>	V <sub>DM</sub> = 6 V, R <sub>L</sub> = 100 Ω	_	-	1.5	٧	See Fig. 4	
Gate Non-Trigger Voltage	V <sub>GD</sub>	V <sub>DM</sub> = 1/2 V <sub>DRM</sub> , T <sub>j</sub> = 125 °C	0.2	_	_	V	:	
Critical Rate of Rise of Off-State Voltage	dv/dt	V <sub>DM</sub> = V <sub>DRM</sub> , T <sub>j</sub> = 125 °C	_	40	_	V/μs		
Holding Current	1 <sub>H</sub>	V <sub>D</sub> = 24 V	_	6	_	mA		
Circuit Commuted Turn-Off Time	<sup>t</sup> q	$I_{TM} = 5 \text{ A, V}_{R} \ge 25 \text{ V}$ $V_{DM} = 2/3 \text{ V}_{DRM}, \text{diR/dt} = 15 \text{ A/}\mu\text{s}$ $\text{dv/dt} = 10 \text{ V/}\mu\text{s, T}_{j} = 125 ^{\circ}\text{C}$	_	100	_	μs		
Thermal Resistance	R <sub>th</sub>	Junction to case	_	_	3	°C/W	See Fig. 13	

