

To our customers,

Old Company Name in Catalogs and Other Documents

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April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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HD74HC125, HD74HC126

Quad. Bus Buffer Gates (with 3-state outputs)

REJ03D0565-0300

Rev.3.00

Mar 25, 2009

Description

The HD74HC125, HD74HC126 require the 3-state control input C to be taken high to put the output into the high impedance condition, whereas the HD74HC125, HD74HC126 requires the control input to be low to put the output into high impedance.

Features

- High Speed Operation: $t_{pd} = 8 \text{ ns typ (} C_L = 50 \text{ pF)}$
- High Output Current: Fanout of 15 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: $1 \mu\text{A max}$
- Low Quiescent Supply Current: $I_{CC} \text{ (static)} = 4 \mu\text{A max (} T_a = 25^\circ\text{C)}$
- Ordering Information

| Part Name | Package Type | Package Code (Previous Code) | Package Abbreviation | Taping Abbreviation (Quantity) |
|--------------------------------|--------------------|------------------------------|----------------------|--------------------------------|
| HD74HC125P HD74HC126P | DILP-14 pin | PRDP0014AB-B (DP-14AV) | P | — |
| HD74HC125FPEL HD74HC126FPEL | SOP-14 pin (JEITA) | PRSP0014DF-B (FP-14DAV) | FP | EL (2,000 pcs/reel) |
| HD74HC125RPEL HD74HC126RPEL | SOP-14 pin (JEDEC) | PRSP0014DE-A (FP-14DNV) | RP | EL (2,500 pcs/reel) |
| HD74HC125TELL HD74HC126TELL | TSSOP-14 pin | PTSP0014JA-B (TTP-14DV) | T | ELL (2,000 pcs/reel) |

Note: Please consult the sales office for the above package availability.

Function Table

| Inputs | | | Output | |
|--------|-------|---|--------|-------|
| C | | A | Y | |
| HC125 | HC126 | | HC125 | HC126 |
| H | L | X | Z | Z |
| L | H | L | L | L |
| L | H | H | H | H |

H: High level

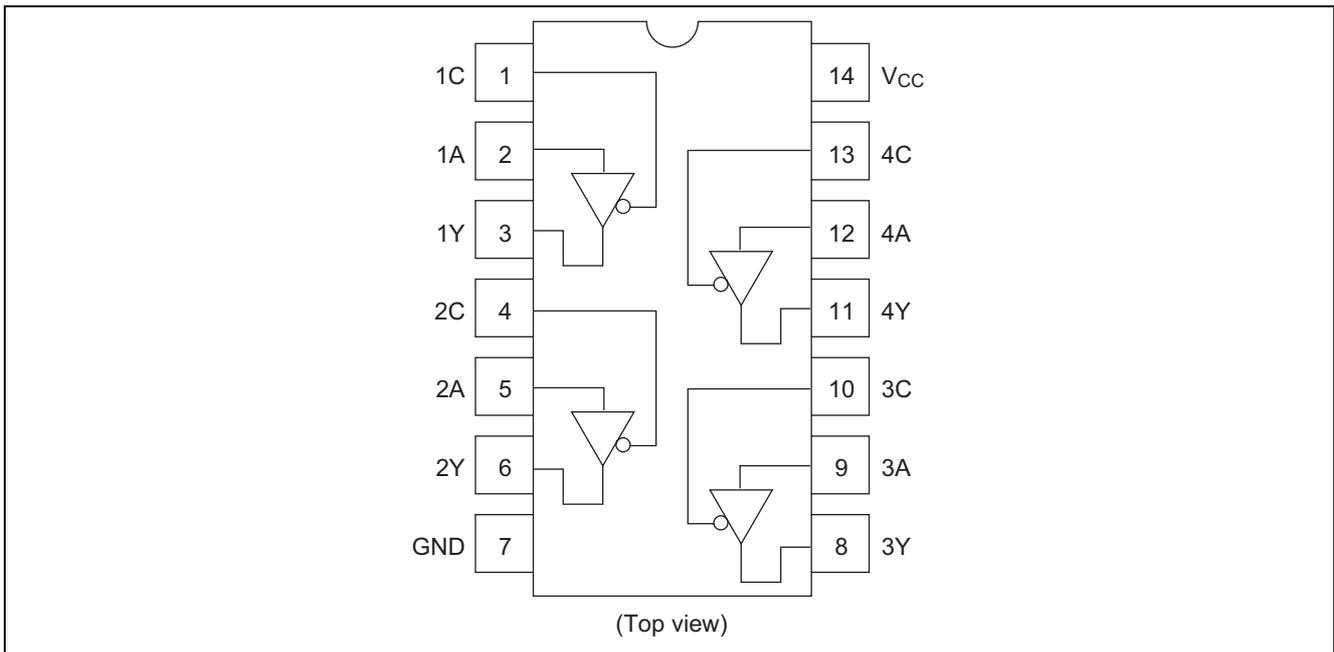
L: Low level

X: Irrelevant

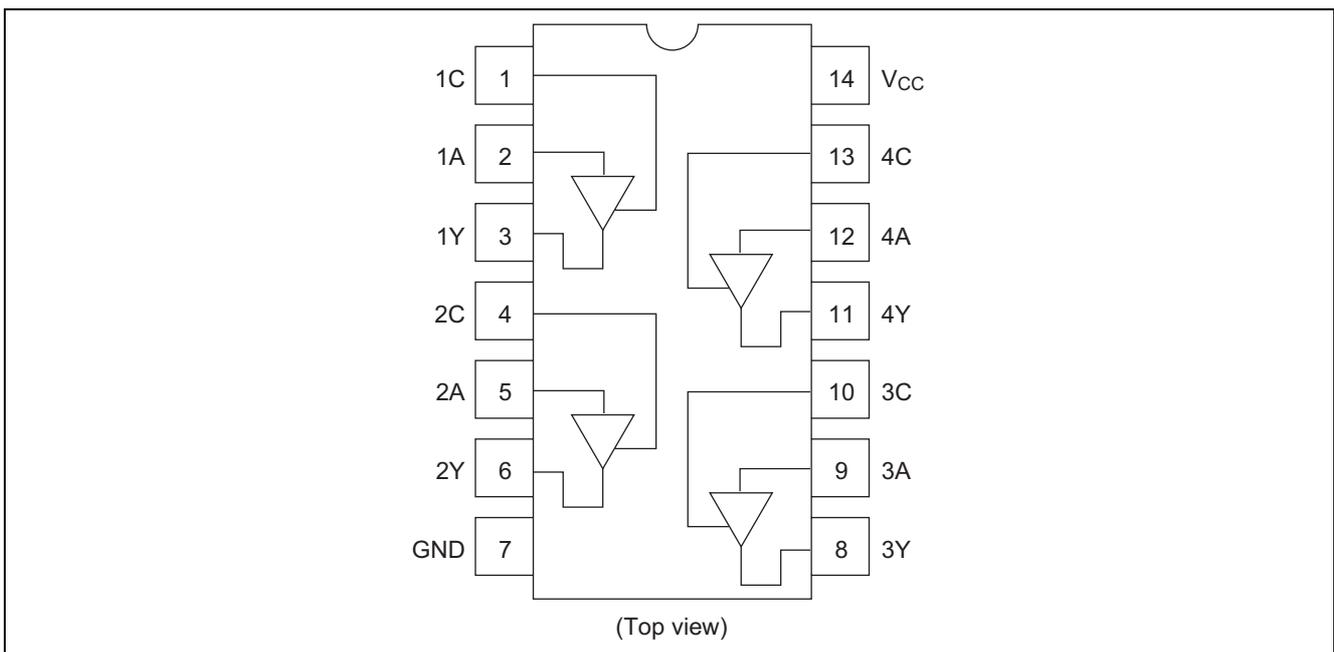
Z: Off (high-impedance) state of a 3-state output.

Pin Arrangement

- HD74HC125



- HD74HC126



Absolute Maximum Ratings

| Item | Symbol | Rating | Unit |
|-------------------------------------|----------------------|------------------------|------|
| Supply voltage range | V_{CC} | -0.5 to +7.0 | V |
| Input voltage | V_{IN} | -0.5 to $V_{CC} + 0.5$ | V |
| Output voltage | V_{OUT} | -0.5 to $V_{CC} + 0.5$ | V |
| Output current | I_{OUT} | ± 35 | mA |
| DC current drain per V_{CC} , GND | I_{CC} , I_{GND} | ± 75 | mA |
| DC input diode current | I_{IK} | ± 20 | mA |
| DC output diode current | I_{OK} | ± 20 | mA |
| Power dissipation per package | P_T | 500 | mW |
| Storage temperature | T_{stg} | -65 to +150 | °C |

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

| Item | Symbol | Ratings | Unit | Conditions |
|--------------------------------------|----------------------|---------------|------|------------------|
| Supply voltage | V_{CC} | 2 to 6 | V | |
| Input / Output voltage | V_{IN} , V_{OUT} | 0 to V_{CC} | V | |
| Operating temperature | T_a | -40 to 85 | °C | |
| Input rise / fall time ^{*1} | t_r , t_f | 0 to 1000 | ns | $V_{CC} = 2.0$ V |
| | | 0 to 500 | | $V_{CC} = 4.5$ V |
| | | 0 to 400 | | $V_{CC} = 6.0$ V |

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

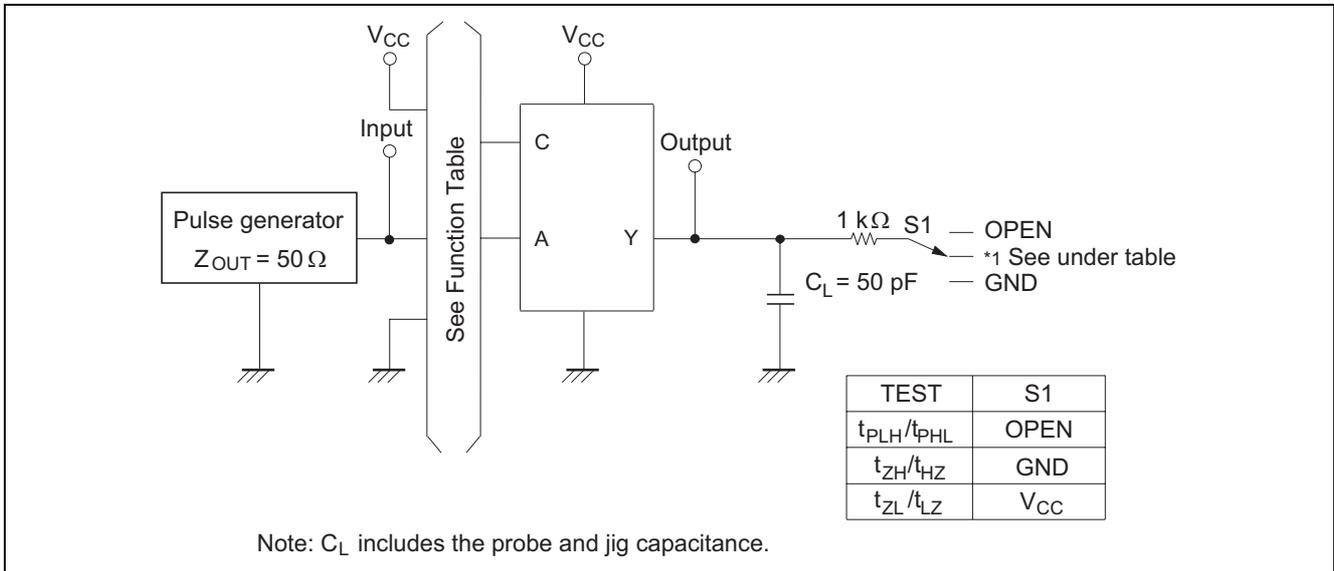
Electrical Characteristics

| Item | Symbol | V_{CC} (V) | $T_a = 25^\circ\text{C}$ | | | $T_a = -40 \text{ to } +85^\circ\text{C}$ | | Unit | Test Conditions | | | | |
|--------------------------|----------|--------------|--------------------------|-----|-----------|---|-----------|---------------|---|----------------------------|---|--|--|
| | | | Min | Typ | Max | Min | Max | | | | | | |
| Input voltage | V_{IH} | 2.0 | 1.5 | — | — | 1.5 | — | V | | | | | |
| | | 4.5 | 3.15 | — | — | 3.15 | — | | | | | | |
| | | 6.0 | 4.2 | — | — | 4.2 | — | | | | | | |
| | V_{IL} | 2.0 | — | — | 0.5 | — | 0.5 | | | | V | | |
| | | 4.5 | — | — | 1.35 | — | 1.35 | | | | | | |
| | | 6.0 | — | — | 1.8 | — | 1.8 | | | | | | |
| Output voltage | V_{OH} | 2.0 | 1.9 | 2.0 | — | 1.9 | — | V | $V_{in} = V_{IH} \text{ or } V_{IL}$ | $I_{OH} = -20 \mu\text{A}$ | | | |
| | | 4.5 | 4.4 | 4.5 | — | 4.4 | — | | | $I_{OH} = -6 \text{ mA}$ | | | |
| | | 6.0 | 5.9 | 6.0 | — | 5.9 | — | | | $I_{OH} = -7.8 \text{ mA}$ | | | |
| | | 4.5 | 4.18 | — | — | 4.13 | — | | | | | | |
| | | 6.0 | 5.68 | — | — | 5.63 | — | | | | | | |
| | V_{OL} | 2.0 | — | 0.0 | 0.1 | — | 0.1 | V | $V_{in} = V_{IH} \text{ or } V_{IL}$ | $I_{OL} = 20 \mu\text{A}$ | | | |
| | | 4.5 | — | 0.0 | 0.1 | — | 0.1 | | | $I_{OL} = 6 \text{ mA}$ | | | |
| | | 6.0 | — | 0.0 | 0.1 | — | 0.1 | | | $I_{OL} = 7.8 \text{ mA}$ | | | |
| | | 4.5 | — | — | 0.26 | — | 0.33 | | | | | | |
| | | 6.0 | — | — | 0.26 | — | 0.33 | | | | | | |
| Off-state output current | I_{OZ} | 6.0 | — | — | ± 0.5 | — | ± 5.0 | μA | $V_{in} = V_{IH} \text{ or } V_{IL}$, $V_{out} = V_{CC} \text{ or } \text{GND}$ | | | | |
| Input current | I_{in} | 6.0 | — | — | ± 0.1 | — | ± 1.0 | μA | $V_{in} = V_{CC} \text{ or } \text{GND}$ | | | | |
| Quiescent supply current | I_{CC} | 6.0 | — | — | 4.0 | — | 40 | μA | $V_{in} = V_{CC} \text{ or } \text{GND}$, $I_{out} = 0 \mu\text{A}$ | | | | |

Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

| Item | Symbol | V_{CC} (V) | $T_a = 25^\circ\text{C}$ | | | $T_a = -40 \text{ to } +85^\circ\text{C}$ | | Unit | Test Conditions |
|------------------------|--------------------|--------------|--------------------------|-----|-----|---|-----|------|-----------------|
| | | | Min | Typ | Max | Min | Max | | |
| Propagation delay time | t_{PLH}, t_{PHL} | 2.0 | — | — | 100 | — | 125 | ns | |
| | | 4.5 | — | 8 | 20 | — | 25 | | |
| | | 6.0 | — | — | 17 | — | 21 | | |
| Output enable Time | t_{ZH}, t_{ZL} | 2.0 | — | — | 150 | — | 190 | ns | |
| | | 4.5 | — | 9 | 30 | — | 38 | | |
| | | 6.0 | — | — | 26 | — | 33 | | |
| Output disable Time | t_{HZ}, t_{LZ} | 2.0 | — | — | 150 | — | 190 | ns | |
| | | 4.5 | — | 14 | 30 | — | 38 | | |
| | | 6.0 | — | — | 26 | — | 33 | | |
| Output rise/fall time | t_{TLH}, t_{THL} | 2.0 | — | — | 60 | — | 75 | ns | |
| | | 4.5 | — | 4 | 12 | — | 15 | | |
| | | 6.0 | — | — | 10 | — | 13 | | |
| Input capacitance | C_{in} | — | — | 5 | 10 | — | 10 | pF | |

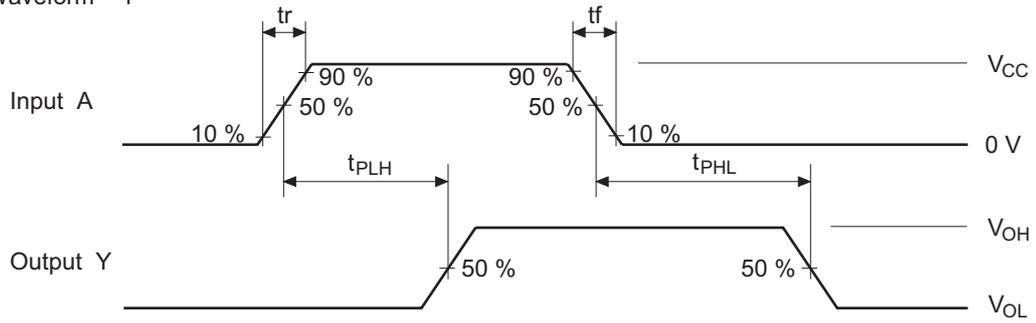
Test Circuit



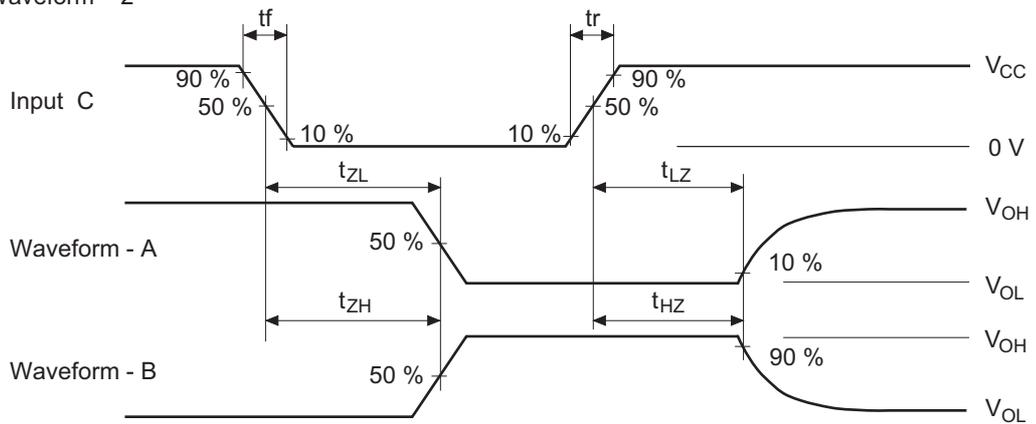
Waveforms

- HD74HC125

• Waveform – 1



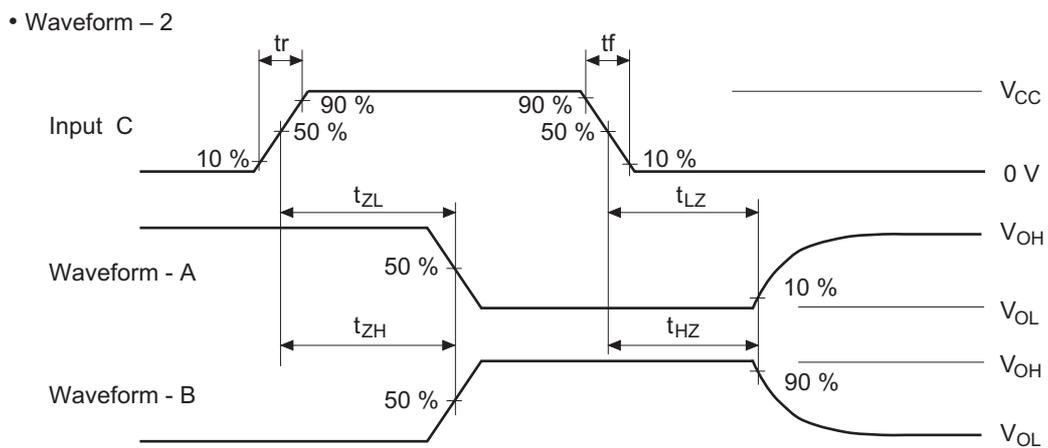
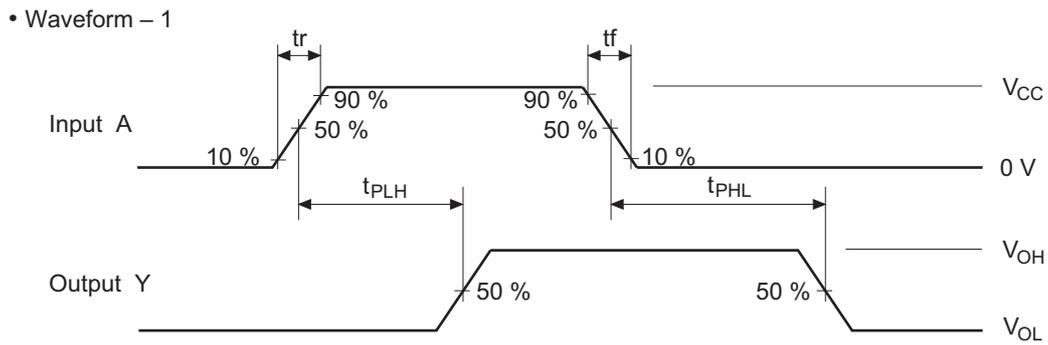
• Waveform – 2



- Notes :
1. $t_r \leq 6\text{ ns}$, $t_f \leq 6\text{ ns}$
 2. Input waveform : $\text{PRR} \leq 1\text{ MHz}$, duty cycle 50%
 3. Waveform– A is for an output with internal conditions such that the output is low except when disabled by the output control.
 4. Waveform– B is for an output with internal conditions such that the output is high except when disabled by the output control.

Waveforms

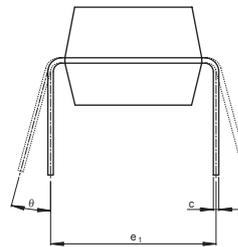
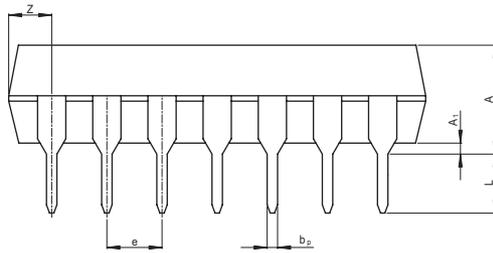
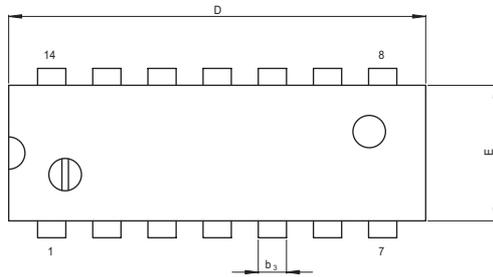
- HD74HC126



- Notes :
1. $t_r \leq 6 \text{ ns}$, $t_f \leq 6 \text{ ns}$
 2. Input waveform : PRR $\leq 1 \text{ MHz}$, duty cycle 50%
 3. Waveform– A is for an output with internal conditions such that the output is low except when disabled by the output control.
 4. Waveform– B is for an output with internal conditions such that the output is high except when disabled by the output control.

Package Dimensions

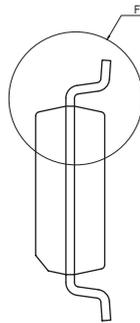
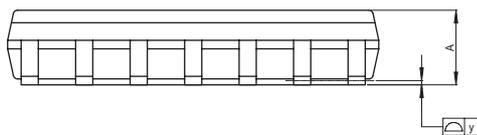
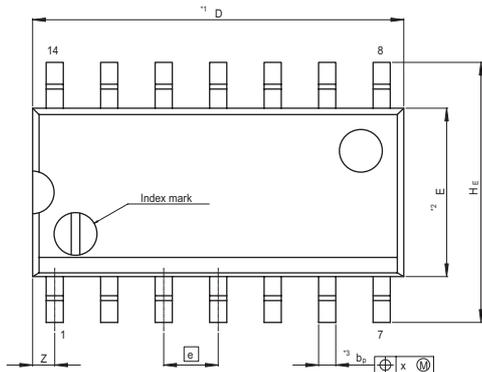
| | | | |
|-----------------------|--------------|---------------|------------|
| JEITA Package Code | RENESAS Code | Previous Code | MASS[Typ.] |
| P-DIP14-6.3x19.2-2.54 | PRDP0014AB-B | DP-14AV | 0.97g |



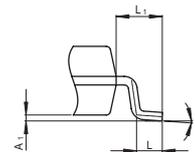
(Ni/Pd/Au plating)

| Reference Symbol | Dimension in Millimeters | | |
|------------------|--------------------------|------|-------|
| | Min | Nom | Max |
| e ₁ | — | 7.62 | — |
| D | — | 19.2 | 20.32 |
| E | — | 6.3 | 7.4 |
| A | — | — | 5.06 |
| A ₁ | 0.51 | — | — |
| b _p | 0.40 | 0.48 | 0.56 |
| b ₃ | — | 1.30 | — |
| c | 0.19 | 0.25 | 0.31 |
| θ | 0° | — | 15° |
| e | 2.29 | 2.54 | 2.79 |
| Z | — | — | 2.39 |
| L | 2.54 | — | — |

| | | | |
|------------------------|--------------|---------------|------------|
| JEITA Package Code | RENESAS Code | Previous Code | MASS[Typ.] |
| P-SOP14-3.95x8.65-1.27 | PRSP0014DE-A | FP-14DNV | 0.13g |



Terminal cross section
(Ni/Pd/Au plating)



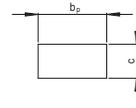
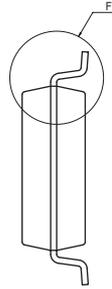
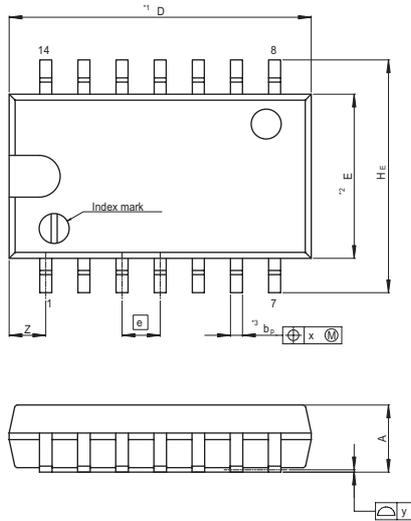
Detail F

NOTE)
1. DIMENSIONS*1 (Nom)*AND*2* DO NOT INCLUDE MOLD FLASH.
2. DIMENSION*3*DOES NOT INCLUDE TRIM OFFSET.

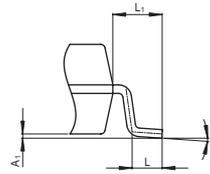
| Reference Symbol | Dimension in Millimeters | | |
|------------------|--------------------------|------|-------|
| | Min | Nom | Max |
| D | — | 8.65 | 9.05 |
| E | — | 3.95 | — |
| A ₂ | — | — | — |
| A ₁ | 0.10 | 0.14 | 0.25 |
| A | — | — | 1.75 |
| b _p | 0.34 | 0.40 | 0.46 |
| b ₁ | — | — | — |
| c | 0.15 | 0.20 | 0.25 |
| c ₁ | — | — | — |
| θ | 0° | — | 8° |
| H _E | 5.80 | 6.10 | 6.20 |
| ⓪ | — | 1.27 | — |
| x | — | — | 0.25 |
| y | — | — | 0.15 |
| Z | — | — | 0.635 |
| L | 0.40 | 0.60 | 1.27 |
| L ₁ | — | 1.08 | — |

HD74HC125, HD74HC126

| | | | |
|------------------------|--------------|---------------|------------|
| JEITA Package Code | RENESAS Code | Previous Code | MASS[Typ.] |
| P-SOP14-5.5x10.06-1.27 | PRSP0014DF-B | FP-14DAV | 0.23g |



Terminal cross section
(Ni/Pd/Au plating)

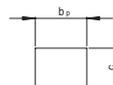
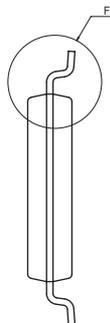
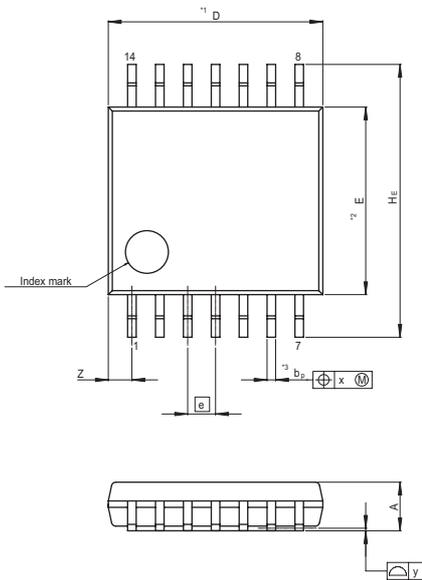


Detail F

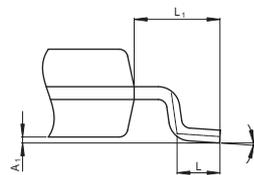
NOTE)
1. DIMENSIONS**1 (Nom)**AND**2*
DO NOT INCLUDE MOLD FLASH.
2. DIMENSION**3*DOES NOT
INCLUDE TRIM OFFSET.

| Reference Symbol | Dimension in Millimeters | | |
|------------------|--------------------------|-------|------|
| | Min | Nom | Max |
| D | — | 10.06 | 10.5 |
| E | — | 5.50 | — |
| A ₂ | — | — | — |
| A ₁ | 0.00 | 0.10 | 0.20 |
| A | — | — | 2.20 |
| b _P | 0.34 | 0.40 | 0.46 |
| b ₁ | — | — | — |
| c | 0.15 | 0.20 | 0.25 |
| c ₁ | — | — | — |
| θ | 0° | — | 8° |
| H _E | 7.50 | 7.80 | 8.00 |
| Ⓜ | — | 1.27 | — |
| x | — | — | 0.12 |
| y | — | — | 0.15 |
| Z | — | — | 1.42 |
| L | 0.50 | 0.70 | 0.90 |
| L ₁ | — | 1.15 | — |

| | | | |
|----------------------|--------------|---------------|------------|
| JEITA Package Code | RENESAS Code | Previous Code | MASS[Typ.] |
| P-TSSOP14-4.4x5-0.65 | PTSP0014JA-B | TTP-14DV | 0.05g |



Terminal cross section
(Ni/Pd/Au plating)



Detail F

NOTE)
1. DIMENSIONS**1 (Nom)**AND**2*
DO NOT INCLUDE MOLD FLASH.
2. DIMENSION**3*DOES NOT
INCLUDE TRIM OFFSET.

| Reference Symbol | Dimension in Millimeters | | |
|------------------|--------------------------|------|------|
| | Min | Nom | Max |
| D | — | 5.00 | 5.30 |
| E | — | 4.40 | — |
| A ₂ | — | — | — |
| A ₁ | 0.03 | 0.07 | 0.10 |
| A | — | — | 1.10 |
| b _P | 0.15 | 0.20 | 0.25 |
| b ₁ | — | — | — |
| c | 0.10 | 0.15 | 0.20 |
| c ₁ | — | — | — |
| θ | 0° | — | 8° |
| H _E | 6.20 | 6.40 | 6.60 |
| Ⓜ | — | 0.65 | — |
| x | — | — | 0.13 |
| y | — | — | 0.10 |
| Z | — | — | 0.83 |
| L | 0.4 | 0.5 | 0.6 |
| L ₁ | — | 1.0 | — |

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