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April 1st, 2010 Renesas Electronics Corporation

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HD74HC442, HD74HC443, HD74HC444

Quad. Tridirectional Bus Transceiver (with noninverted 3-state outputs)
Quad. Tridirectional Bus Transceiver (with inverted 3-state outputs)
Quad Tridirectional Bus Transceiver (with noninverted/inverted 3-state outputs)

REJ03D0627-0200 (Previous ADE-205-506) Rev.2.00 Mar 30, 2006

Description

These bus transceivers are designed for a synchronous three-way communication between four-line data buses. They give the designer a choice of selecting inverting, noninverting or a combination of inverting and noninverting data paths with 3-state outputs.

The S_0 and S_1 inputs select the bus from which data are to be transferred. The \overline{G} inputs enable the bus or buses to which data are to be transferred. The port for any bus selected for input and any other bus not enabled for output will be at high impedance.

Features

- High Speed Operation
- High Output Current: Fanout of 15 LSTTL Loads
- Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$
- Low Input Current: 1 μA max
- Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)
- Ordering Information

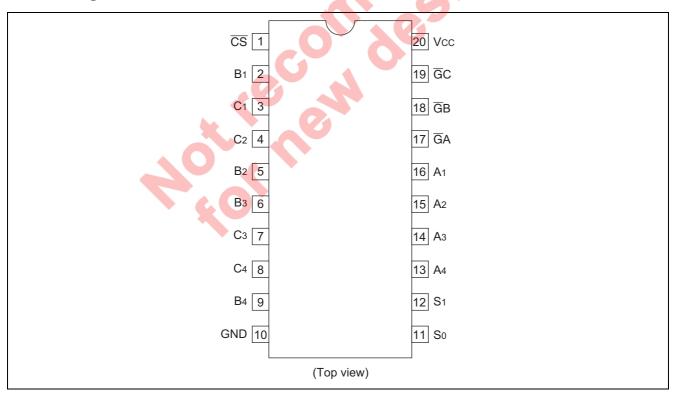
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC442RPEL HD74HC443RPEL HD74HC444RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)

Function Table

		Inp	uts			Transfers Between Buses			
CS	S ₁	S ₀	GA	GВ	GC	HD74HC442	HD74HC443	HD74HC444	
Н	Х	Х	Х	Х	Х	None	None	None	
Х	Н	Н	Х	Х	Х	None	None	None	
Х	Х	Х	Н	Н	Н	None	None	None	
Х	L	L	Х	Н	Н	None	None	None	
Х	L	Н	Н	Х	Н	None	None	None	
Х	Н	L	Н	Н	Х	None	None	None	
L	L	L	Х	L	L	$A\toB,A\toC$	$\overline{A} \to B, \overline{A} \to C$	$\overline{A} \to B, \overline{A} \to C$	
L	L	Н	L	Х	L	$B \rightarrow C, B \rightarrow A$	$\overline{B} \to C, \overline{B} \to A$	$B \to C, \overline{B} \to A$	
L	Н	L	L	L	Х	$C\toA,C\toB$	$\overline{C} \to A, \overline{C} \to B$	$\overline{C} \to A, C \to B$	
L	L	L	Х	L	Н	$A\toB$	$\overline{A} o B$	$\overline{A} o B$	
L	L	Н	Н	Х	L	$B\toC$	$\overline{B} \to C$	$B\toC$	
L	Н	L	L	Н	Х	$C\toA$	$\overline{C} \to A$	$\overline{C} \to A$	
L	L	L	Х	Н	L	$A\toC$	$\overline{A} \to C$	$\overline{A} o C$	
L	L	Н	L	Х	Н	$B \rightarrow A$	$\overline{B} \to A$	$\overline{B} \to A$	
L	Н	L	Н	L	Х	$C \to B$	$\overline{\mathbb{C}} \to B$	$C \to B$	

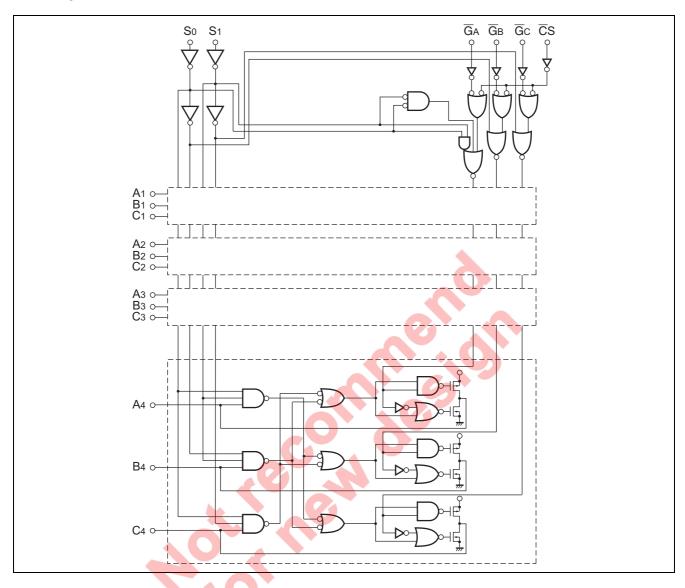
H: high levelL: low levelX: irrelevant

Pin Arrangement

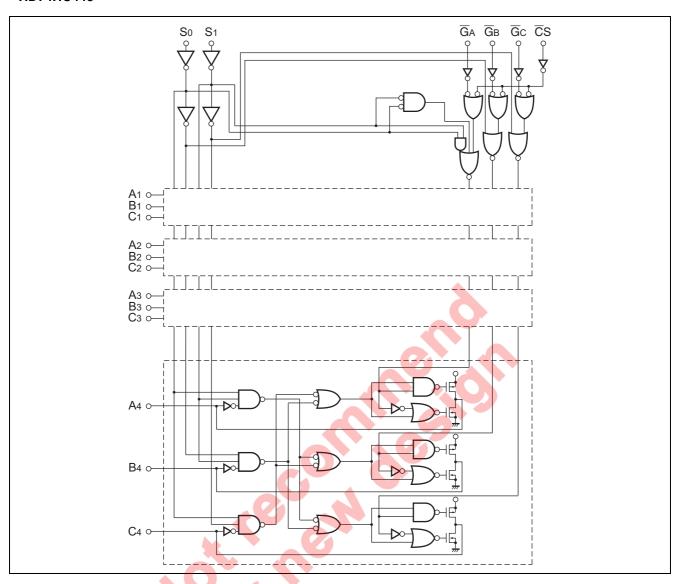


Logic Diagram

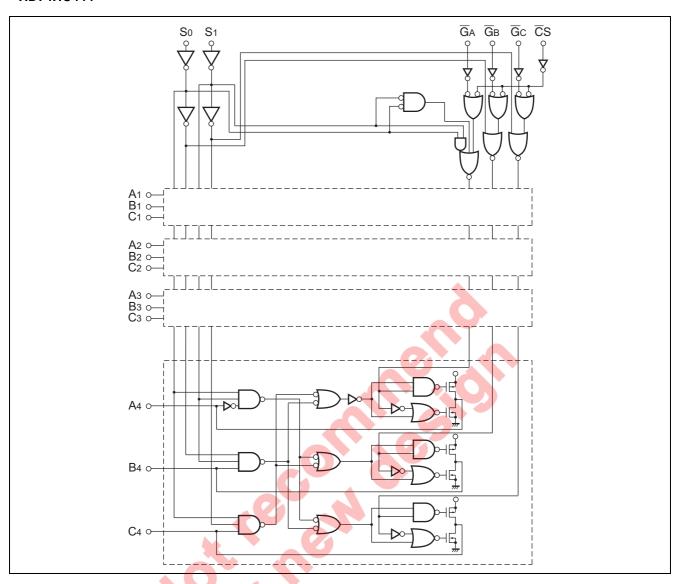
HD74HC442



HD74HC443



HD74HC444



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	lo	±35	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±75	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		V _{CC} = 2.0 V
Input rise / fall time*1	t _r , t _f	0 to 500	ns	V _{CC} = 4.5 V
		0 to 400		$V_{CC} = 6.0 \text{ V}$

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

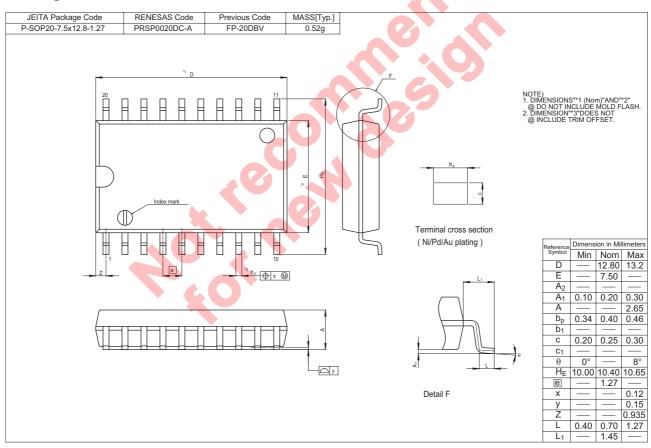
Electrical Characteristics

			Т	Ta = 25°C Ta = -40 to+85°C						
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Cor	nditions
Input voltage	V _{IH}	2.0	1.5			1.5		V		
		4.5	3.15		1	3.15	_			
		6.0	4.2	_	7	4.2				
	V_{IL}	2.0		4	0.5	_	0.5	V		
		4.5	_		1.35	_	1.35			
		6.0	1		1.8	_	1.8			
Output voltage	V _{OH}	2.0	1.9	2.0	1	1.9	1	V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4				
		6.0	5.9	6.0	_	5.9				
		4.5	4.18	_	_	4.13				$I_{OH} = -6 \text{ mA}$
		6.0	5.68	_	_	5.63				$I_{OH} = -7.8 \text{ mA}$
	V_{OL}	2.0	_	0.0	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	_	0.0	0.1	_	0.1			
		6.0	-	0.0	0.1	_	0.1			
		4.5	-	_	0.26	_	0.33			$I_{OL} = 6 \text{ mA}$
		6.0	-	_	0.26	_	0.33			$I_{OL} = 7.8 \text{ mA}$
Off-state output	l _{OZ}	6.0	_	_	±0.5	_	±5.0	μΑ	$Vin = V_{IH} or V_{IL}$	
current									Vout = V_{CC} or GND	
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	Vin = V _{CC} or GND	
Quiescent supply current	I _{CC}	6.0	_	_	4.0	_	40	μА	$Vin = V_{CC}$ or GN	ID, lout = $0 \mu A$

Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

			Т	a = 25°	С	Ta = -40 to +85°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PLH}	2.0	_	_	200	_	250	ns	
time	t _{PHL}	4.5	_	_	40	_	50		
		6.0	_	_	34	_	43		
Output enable	t _{zH}	2.0	_	_	150	_	190	ns	
time	t_{ZL}	4.5	_	_	30	_	38		
		6.0	_	_	26	_	33		
Output disable	t _{HZ}	2.0	_	_	150	_	190	ns	
time	t _{LZ}	4.5	_	_	30	_	38		
		6.0	_	_	26	_	33		
Output rise/fall	t _{TLH}	2.0	_	_	60	_	75	ns	
time	t _{THL}	4.5	_	_	12	_	15		
		6.0	_	_	10	_	13		
Input capacitance	Cin	_	_	5	10	_	10	pF	

Package Dimensions



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