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April 1st, 2010 Renesas Electronics Corporation

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HD74HC640, HD74HC643

Octal Bus Transceivers (with 3-state outputs)

REJ03D0637-0200 (Previous ADE-205-517) Rev.2.00 Mar 30, 2006

Description

Each device has an active enable \overline{G} and a direction control input, DIR. When DIR is high, data flows from the A inputs to the B outputs. When DIR is low, data flows from the B inputs to the A outputs. The HD74HC640 transfers inverted data from one bus to other and the HD74HC643 transfers inverted data from the A bus to the B bus and true data from the B bus to the A bus.

Features

High Speed Operation: t_{pd} = 12 ns typ (C_L = 50 pF)
 High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 2 \text{ to } 6 \text{ V}$

• Low Input Current: 1 µA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74HC640P	DILP-20 pin	PRDP0020AC-B (DP-20NEV)	Р	_
HD74HC640FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B (FP-20DAV)	FP	EL (2,000 pcs/reel)
HD74HC640RPEL HD74HC643RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A (FP-20DBV)	RP	EL (1,000 pcs/reel)

Note: Please consult the sales office for the above package availability.

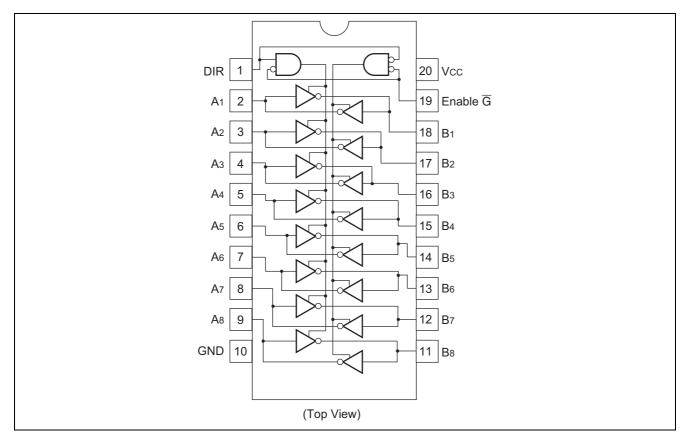
Function Table

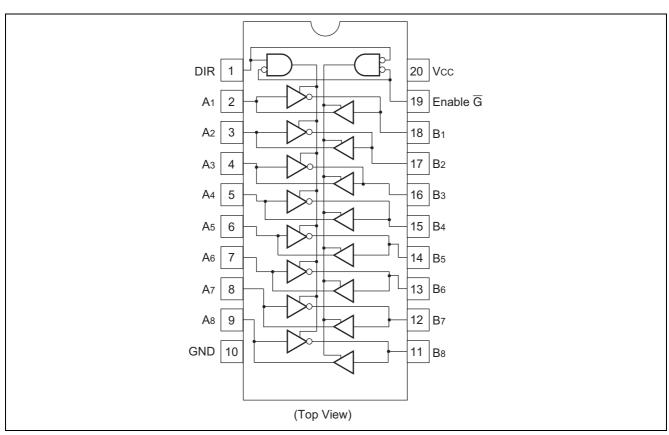
Contr	ol Inputs	Operation				
G	DIR	HD74HC640	HD74HC643			
L	L	B data to A bus	B data to A bus			
L	Н	Ā data to B bus	Ā data to B bus			
Н	X	Isolation	Isolation			

H: high levelL: low levelX: irrelevant

Pin Arrangement

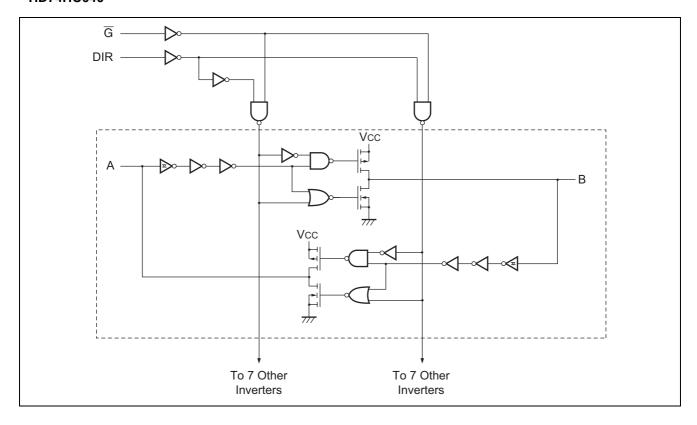
HD74HC640

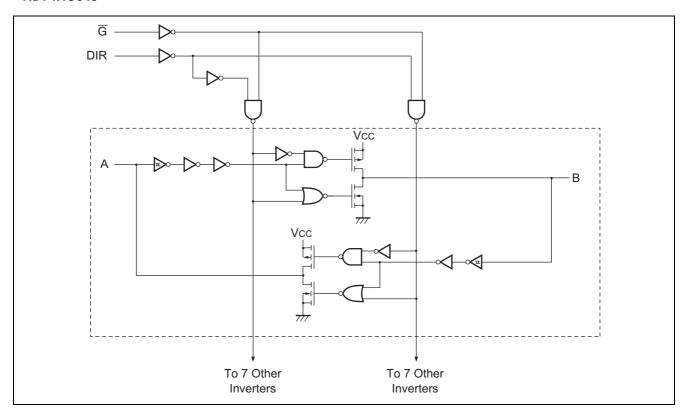




Logic Diagram

HD74HC640





Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	V _{CC}	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	Io	±35	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±75	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{CC}	2 to 6	V	
Input / Output voltage	V _{IN} , V _{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
		0 to 1000		V _{CC} = 2.0 V
Input rise / fall time*1	t_r , t_f	0 to 500	ns	$V_{CC} = 4.5 \text{ V}$
		0 to 400		V _{CC} = 6.0 V

Note: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

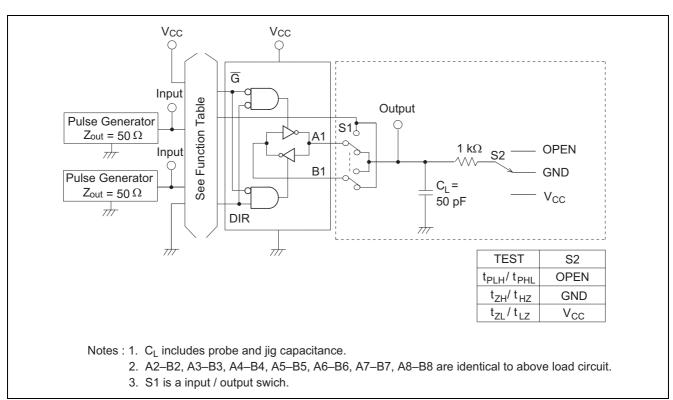
Electrical Characteristics

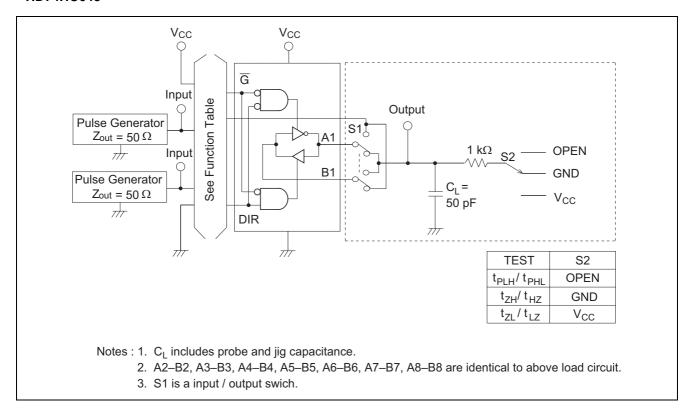
			Т	a = 25°	С	Ta = -40	to+85°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Cor	nditions
Input voltage	V _{IH}	2.0	1.5	_	_	1.5	_	V		
		4.5	3.15	_	_	3.15	_			
		6.0	4.2	_	_	4.2	_			
	V_{IL}	2.0	_	_	0.5	_	0.5	V		
		4.5		_	1.35		1.35			
		6.0			1.8		1.8			
Output voltage	V_{OH}	2.0	1.9	2.0	_	1.9	_	V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.4	4.5	_	4.4				
		6.0	5.9	6.0	_	5.9				
		4.5	4.18		_	4.13				$I_{OH} = -6 \text{ mA}$
		6.0	5.68		_	5.63				$I_{OH} = -7.8 \text{ mA}$
	V_{OL}	2.0		0.0	0.1		0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5	-	0.0	0.1	_	0.1			
		6.0	-	0.0	0.1	_	0.1			
		4.5			0.26		0.33			$I_{OL} = 6 \text{ mA}$
		6.0			0.26		0.33			$I_{OL} = 7.8 \text{ mA}$
Off-state output	l _{OZ}	6.0	1	_	±0.5	_	±5.0	μΑ	$Vin = V_{IH} or V_{IL}$	
current									Vout = V_{CC} or G	ND
Input current	lin	6.0	_	_	±0.1	_	±1.0	μΑ	$Vin = V_{CC} \text{ or } GN$	ID
Quiescent supply	I _{CC}	6.0	_	_	4.0	_	40	μΑ	$Vin = V_{CC} \text{ or GN}$	ID, Iout = $0 \mu A$
current										

Switching Characteristics ($C_L = 50 \text{ pF}$, Input $t_r = t_f = 6 \text{ ns}$)

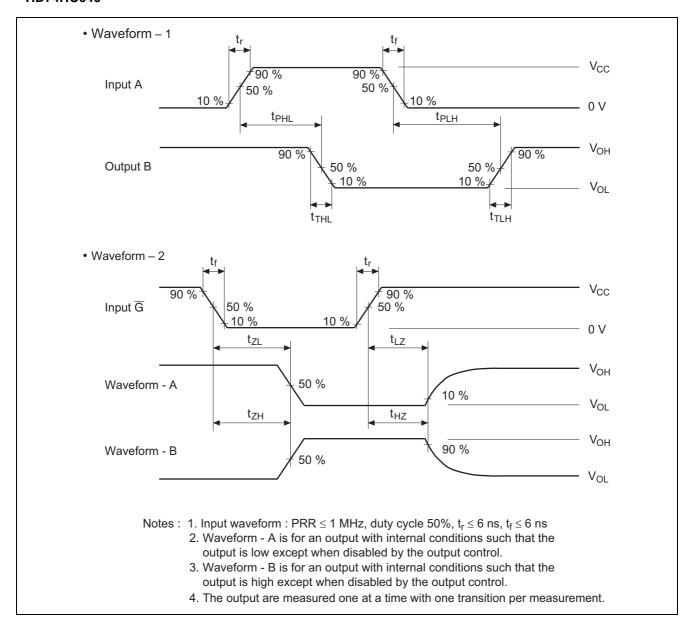
			Т	a = 25°	С	Ta = -40 to +85°C			
Item	Symbol	V _{cc} (V)	Min	Тур	Max	Min	Max	Unit	Test Conditions
Propagation delay	t _{PHL}	2.0	_	_	90	_	115	ns	
time		4.5	_	12	18	_	23		
		6.0	_	_	15	_	20		
	t _{PLH}	2.0	_	-	90	_	115	ns	
		4.5	_	12	18	_	23		
		6.0	_	_	15	_	20		
Output enable	t _{ZL}	2.0	_	_	230	_	290	ns	
time		4.5	_	15	46	_	58		
		6.0	_	_	39	_	49		
	t _{ZH}	2.0	_	_	230	_	290	ns	
		4.5	_	15	46	_	58		
		6.0	_	_	39	_	49		
Output disable	t _{LZ}	2.0	_	-	215	_	270	ns	
time		4.5	_	17	43	_	54		
		6.0	_	_	37	_	46		
	t _{HZ}	2.0	_	_	215	_	270	ns	
		4.5	_	17	43	_	54		
		6.0	_	_	37	_	46		
Output rise/fall	t _{TLH}	2.0	_	_	60	_	75	ns	
time	t _{THL}	4.5	_	4	12	_	15		
		6.0	_	_	10	_	13		
Input capacitance	Cin		_	5	10	_	10	pF	

Test Circuit

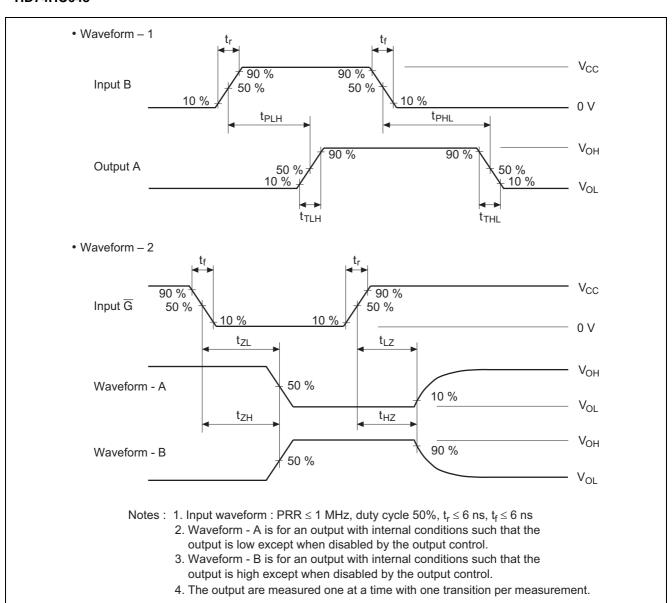




Waveforms

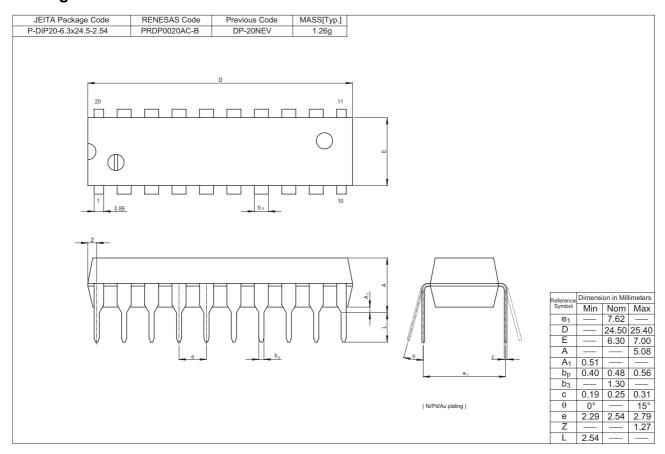


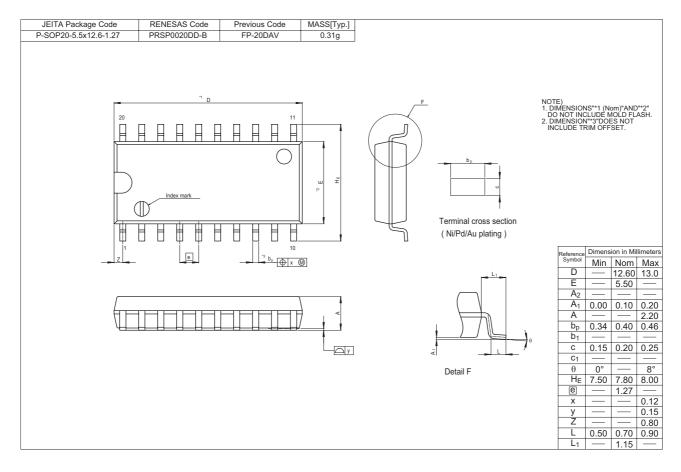
HD74HC643

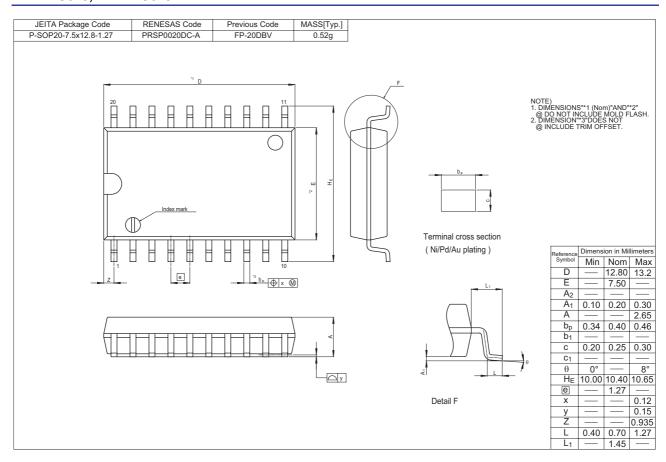


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Package Dimensions







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