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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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HD74HCT640

Octal Bus Transceivers (with inverted 3-state outputs)
Octal Bus Transceivers (with 3-state outputs)

REJ03D0672-0300 (Previous ADE-205-562A) Rev.3.00 Mar 30, 2006

Description

The HD74HCT640 has one active low enable input (\overline{G}) , and a direction control (DIR). When the DIR input is high, data flows from the A inputs to the B outputs. When DIR is low, data flows from B to A.

The HD74HCT640 transfers inverted data from one bus to the other.

Features

LSTTL Output Logic Level Compatibility as well as CMOS Output Compatibility

• High Speed Operation: t_{pd} (A to B) = 14.5 ns typ ($C_L = 50$ pF)

• High Output Current: Fanout of 15 LSTTL Loads

• Wide Operating Voltage: $V_{CC} = 4.5 \text{ to } 5.5 \text{ V}$

• Low Input Current: 1 µA max

• Low Quiescent Supply Current: I_{CC} (static) = 4 μ A max (Ta = 25°C)

• Ordering Information

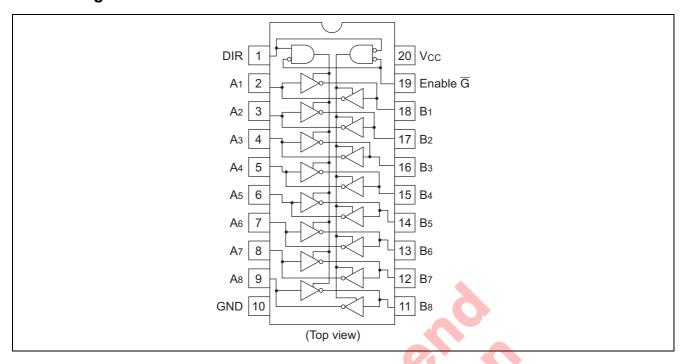
Part Name	Package Type	Package Code Packag (Previous Code) Abbreviat		Taping Abbreviation (Quantity)	
HD74HCT640P	DIP-20 pin	PRDP0020AC-B	P	_	
1107411010401	Вп 20 рп	(DP-20NEV)	•		
HD74HCT640FPEL	SOP-20 pin (JEITA)	PRSP0020DD-B	FP	EL (2,000 pcs/reel)	
TID74110104011 EE	001 20 piii (021171)	(FP-20DAV)	11		
HD74HCT640RPEL	SOP-20 pin (JEDEC)	PRSP0020DC-A	RP	EL (1,000 pcs/reel)	
HD14HC1040KFEL	SOF-20 pill (JEDEC)	(FP-20DBV)	KF		

Note: Please consult the sales office for the above package availability.

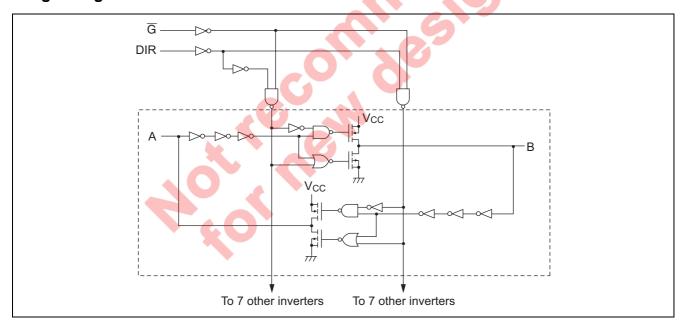
Function Table

Contro	Operation		
G	DIR	HD74HCT640	
L	L	B data to A bus	
L	Н	Ā data to B bus	
Н	X	Isolation	

Pin Arrangement



Logic Diagram



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit
Supply voltage range	Vcc	-0.5 to 7.0	V
Input / Output voltage	V _{IN} , V _{OUT}	-0.5 to V _{CC} +0.5	V
Input / Output diode current	I _{IK} , I _{OK}	±20	mA
Output current	l _{оит}	±35	mA
V _{CC} , GND current	I _{CC} or I _{GND}	±75	mA
Power dissipation	P _T	500	mW
Storage temperature	Tstg	-65 to +150	°C

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V_{CC}	4.5 to 5.5	V	
Input / Output voltage	V_{IN}, V_{OUT}	0 to V _{CC}	V	
Operating temperature	Та	-40 to 85	°C	
Input rise / fall time*1	t _r , t _f	0 to 500	ns	V _{CC} = 4.5 V

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

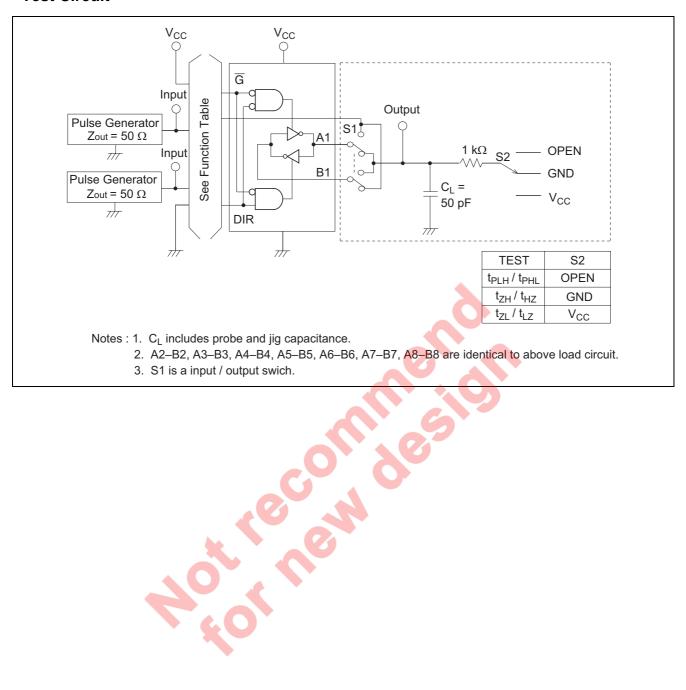
Item	Symbol	V _{cc} (V)	Т	Ta = 25°C Ta = -40 to+85°C		Unit	Test Conditions			
item	Syllibol	VCC (V)	Min	Тур	Max	Min	Max	Offic	rest con	uitions
Input voltage	V_{IH}	4.5 to 5.5	2.0		1	2.0	_	V		
	V_{IL}	4.5 to 5.5		_	0.8	_	0.8	V		
Output voltage	V_{OH}	4.5	4.4	_	_	4.4	_	V	$Vin = V_{IH} or V_{IL}$	$I_{OH} = -20 \mu A$
		4.5	4.18	_	_	4.13				$I_{OH} = -6 \text{ mA}$
	V _{OL}	4.5		_	0.1	_	0.1	V	$Vin = V_{IH} or V_{IL}$	$I_{OL} = 20 \mu A$
		4.5		_	0.26	_	0.33			$I_{OL} = 6 \text{ mA}$
Off-state output	l _{OZ}	5.5	_	_	±0.5	_	±5.0	μΑ	$Vin = V_{IH} or V_{IL}$	
current									Vout = V_{CC} or G	ND
Input current	lin	5.5		_	±0.1		±1.0	μΑ	$Vin = V_{CC} \text{ or } GN$	D
Quiescent supply	Icc	5.5		_	4.0		40	μΑ	$Vin = V_{CC} \text{ or } GN$	D,
current									lout = 0 mA	

Switching Characteristics

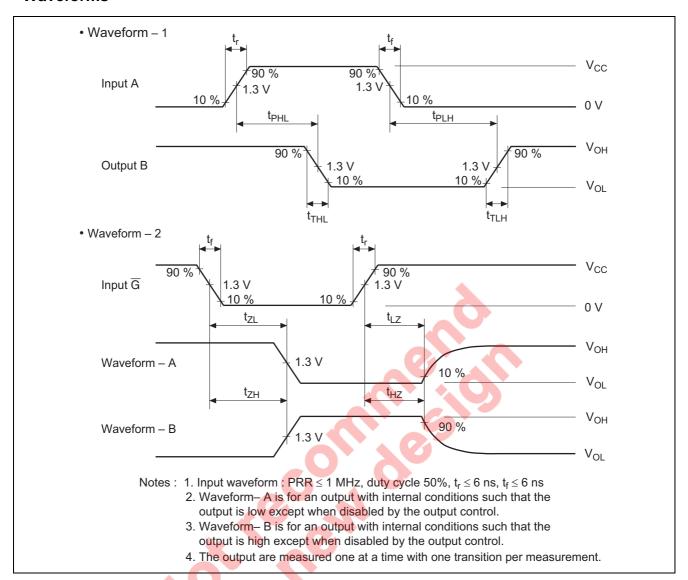
 $(C_L = 50 \text{ pF, Input } t_r = t_f = 6 \text{ ns})$

Item	Symbol \	Symbol V _{CC} (V)	1	Ta = 25°C Ta = -40 to +85°C		Unit	Test Conditions		
item	Syllibol	VCC (V)	Min	Тур	Max	Min	Max	J	rest conditions
Propagation delay time	t _{PLH}	4.5	_	13	18	_	23	ns	
	t _{PHL}	4.5	_	16	18	_	23		
Output enable time	t _{zH}	4.5	_	16	46	_	58	ns	
	t _{ZL}	4.5	_	16	46	_	58		
Output disable time	t _{HZ}	4.5	J	17	43		54	ns	
	t_{LZ}	4.5	1	21	43		54		
Output rise/fall time	t _{TLH}	4.5	_	4	12	_	15	ns	
	t _{THL}								
Input capacitance	Cin	_		5	10	_	10	pF	

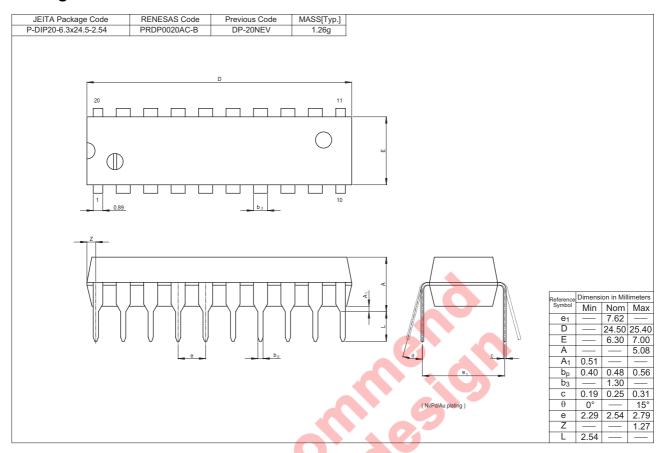
Test Circuit

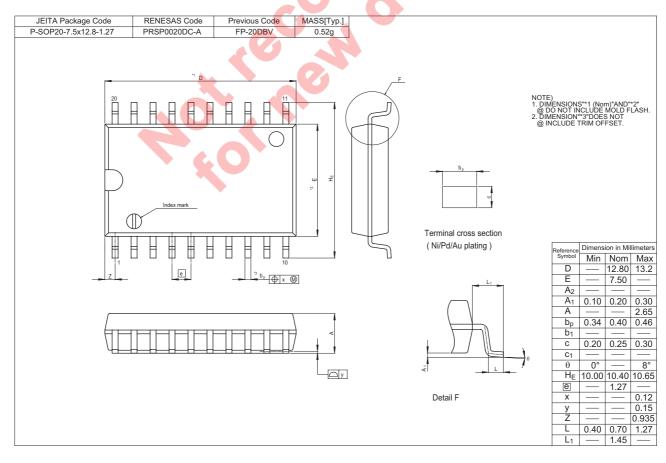


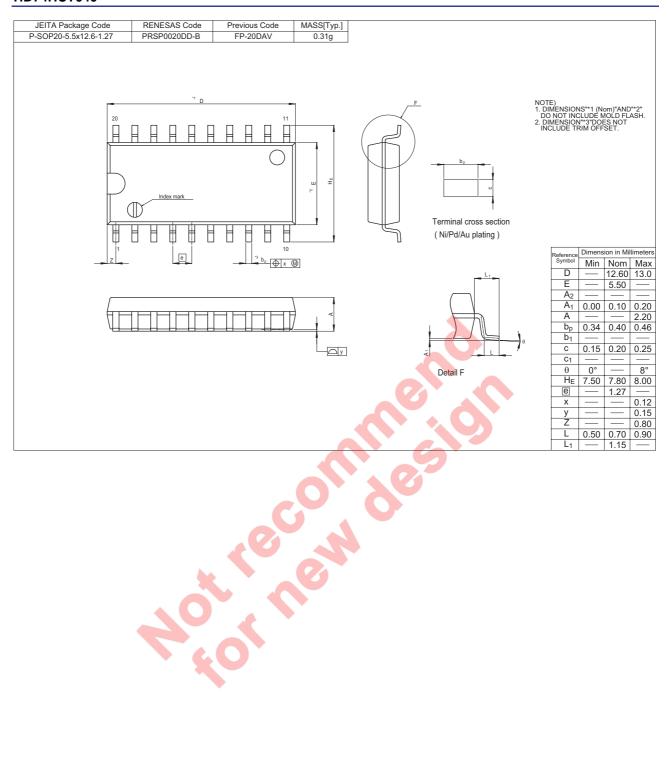
Waveforms



Package Dimensions







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