

RAA230161

R19DS0091EJ0100

REV.1.00

24V Input, USB Voltage Supply for Power Delivery

Dec. 4. 2017

Description

RAA230161 (USB Voltage Supply) is the power supply IC for the power supply application with USB power delivery. This IC provide 5.3V to 20V power supply. Power MOSFETs are included and maximum output power is 60W (20V, 3A). Various protection circuits are included to design safe system easily. The output voltage can be selected by I2C and the IC status can be monitored. The design of the power supply system with USB power delivery become easy by this IC.

Features

- DC/DC
 - Synchronous rectification type step-down DC/DC
 - Input voltage range : 21.6V to 26.4V (Typ.24V)
 - Output voltage : 5.3V, 9.15V, 12.1V, 15.1V, 20V
(Set by I2C)
 - Maximum output current : 3A
 - Output current setting : 0.5A, 1A, 1.5A, 2A, 2.5A, 3A
 - Switching frequency : 500kHz (fixed)
 - Integrated soft start
 - Integrated Power MOSFET
 - Integrated discharge circuit
 - Integrated phase compensator
- Protection circuit
 - Over voltage protection (Latch type)
 - Over current protection (Latch type)
 - Over temperature protection (Latch type)
165°C (typ.)
 - Short circuit protection (Latch type)
 - Under voltage lockout circuit
 - Watch dog timer (Monitoring by I2C)
- Package
 - 20-pin HTSSOP

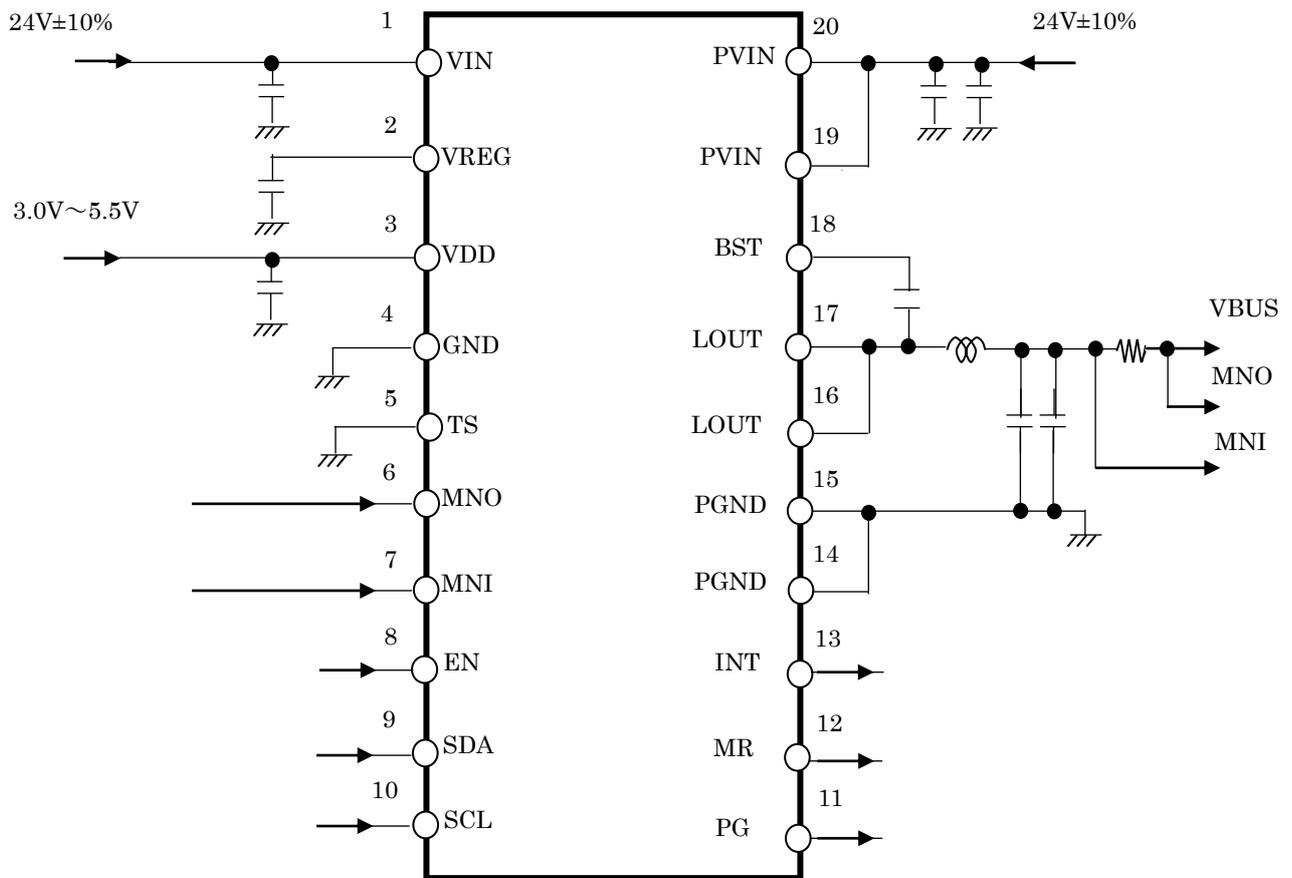
Application

Power supply application with USB power delivery: AC adopter, USB Hub, Monitor, STB, etc.

Note: The information in this document is being issued in developing the product, and may change before final product.

Note: A quality grade of the device is "Standard". Recommended applications are indicated below.
Computers, office equipment, communications equipment, test and measurement equipment, audio and visual equipment, home electronic appliances, machine tools, personal electronic equipment, and industrial robots, etc.

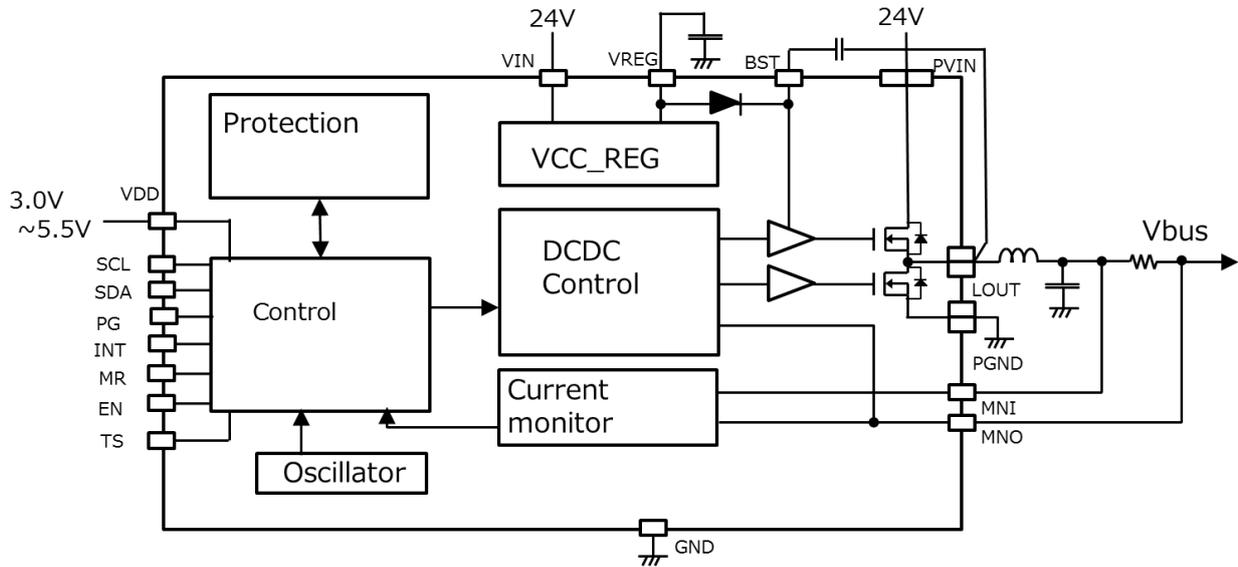
Pin configuration



Pin Description

Pin No.	Symbol	I/O	Function
1	VIN	I	Power supply
2	VREG	I	Internal power supply output (For 1uF capacitor connection)
3	VDD	I	Power supply for I2C
4	GND	I/O	Ground
5	TS	I/O	Test pin
6	MNO	I	Monitor pin for controlling DCDC converter output voltage.
7	MNI	I	Monitor pin for DCDC converter output current.
8	EN	I	Device enable Note : Integrated pull down resistor
9	SDA	I/O	I2C Data input and output
10	SCL	I	I2C Clock input
11	PG	O	Power good output Note : Open drain
12	MR	O	Reset signal output for microcontroller (Low active) Note : Open drain
13	INT	O	Status output (Low active) Note : Open drain
14	PGND	I/O	Power ground for DCDC converter
15	PGND	I/O	Power ground for DCDC converter
16	LOUT	O	DCDC converter output
17	LOUT	O	DCDC converter output
18	BST	I/O	Bootstrap pin
19	PVIN	I	Power supply for DCDC converter
20	PVIN	I	Power supply for DCDC converter

Block Diagram



Absolute Maximum Ratings

(Unless otherwise specified, TA = 25°C)

Parameter	Symbol	Ratings	Unit	Condition
VIN applied voltage	VIN, PVIN	-0.3 to +27	V	
MNI,MNO,LOUT applied voltage	MNI,MNO,LOUT	-0.3 to +27	V	
VDD applied voltage	VDD	-0.3 to +6.5V	V	
VIN input current(peak)	IVIN(peak)-	4.2	A	
LOUT output current(peak)	ILOUT(peak)+	4.2	A	
MNO sink current (DC)	IMNO(DC)-	45	mA	When discharge circuit operation
GND voltage	GND	-0.3 to +0.3	V	
Total power dissipation	PT	3400 ^{*1}	mW	
Operating ambient temperature	TA	-40 to +105	°C	
Operating junction temperature	TJ	-40 to +125	°C	TA ≤ +25°C
Storage temperature	Tstg	-55 to +150	°C	

Note: *1 This is the value at TA < +25°C. At TA > +25°C, the total power dissipation decrease with -34.0 mW/°C.

Board specification: 4-layers glass epoxy board, 76.2mm x 114.3mm x 1.664mm.

Copper coverage area: 50%, 0.070mm thickness (top and bottom layers)

95%, 0.035mm thickness (layers 2 and 3)

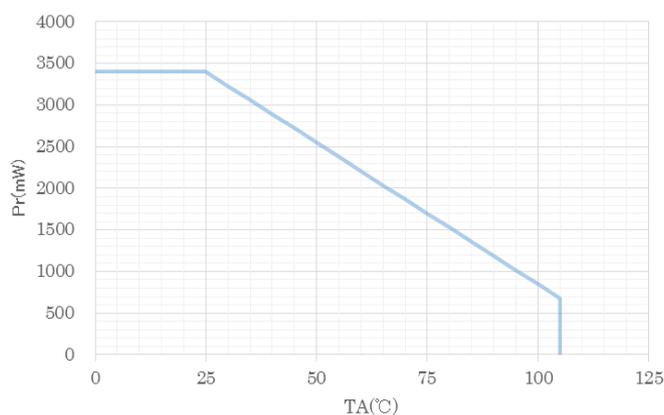
Connecting exposed pad

Caution: Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter.

That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

Derating curve (Reference)

20-pin HTSSOP



Recommended Operating Condition

(Unless otherwise specified, TA = 25°C)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Condition
VIN applied voltage	VIN	21.6	24	26.4	V	
VDD applied voltage	VDD	3.0		5.5	V	
SDA,SDL,EN applied voltage	-	0		5.5	V	
MNI,MNO applied voltage	MNI, MNO	0		22.0	V	

Electrical Characteristics

(Unless otherwise specified, TA = 25°C, VIN = 24V, VDD = 3.3V)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Total	Shutdown current	IDD(SHDN)	90		uA	EN=GND
Under voltage lock out circuit (UVLO_VIN)	Operating start voltage	Vrls(vin)	6.2		V	Detect VIN rising
	Operating stop voltage	Vdet(vin)	5.7		V	Detect VIN falling
Internal power supply (VREG)	Internal power supply voltage	VREG	4.7	5.0	5.3	V Ireg = 0mA
Output	Output voltage accuracy	Vacc	-5		+5	% (USB_PD spec.)
	High side FET on-resistance	Ronh		30		mΩ Io = 100mA
	Low side FET on-resistance	Ronl		70		mΩ Io = 100mA
Soft start	Soft start slope ^{*1}		2.44		V/ms	
Over voltage protection (OVP)	Detecting range (Vout ratio)		110		%	
Over current protection (OCP)	Detecting range (Iocp ratio)		120		%	
Over temperature protection (OTP)	Detecting temperature ^{*2}		165		°C	
Logic input	High level threshold voltage	VIH	VDD*0.7		VDD+0.3	V EN
	Low level threshold voltage	VIL	-0.3		VDD*0.3	V EN
	Input current	IEN		1		uA EN = 3.3V
	Pull down resistor	REN		5		MΩ EN
PG, INT, MR circuit	Output voltage	Vod		0.1	V	At 0.1mA
	Leak current	Iod		1	uA	VDD=3.3V

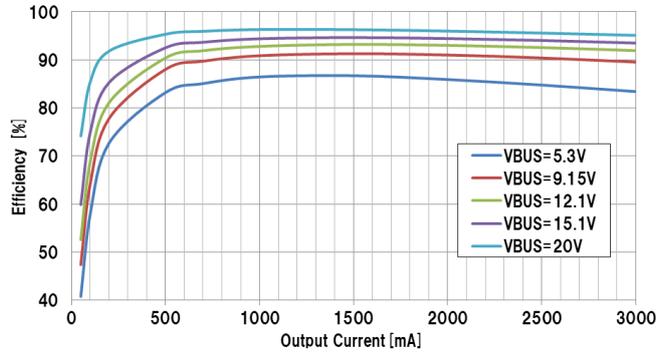
Note: *1 Reference value

*2 Not production tested.

Typical Performance Characteristics

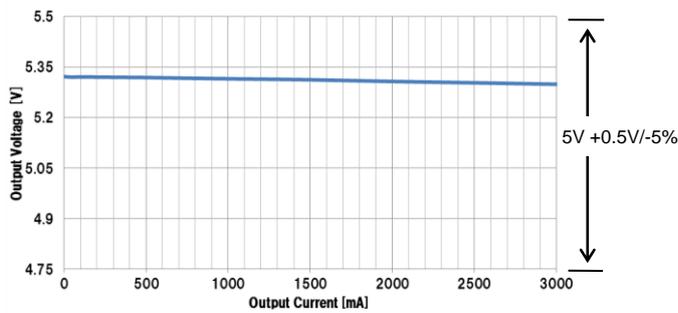
(Unless otherwise specified, TA = 25°C, VIN = PVIN = 24V)

Efficiency vs. Output Current

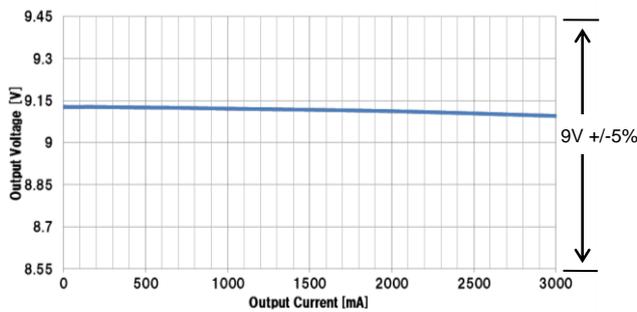


Load Regulation

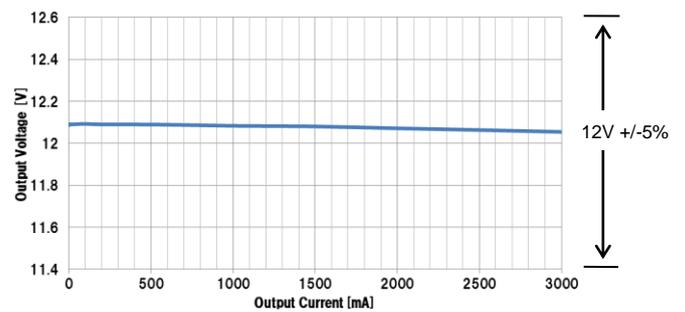
VOUT=5.3V



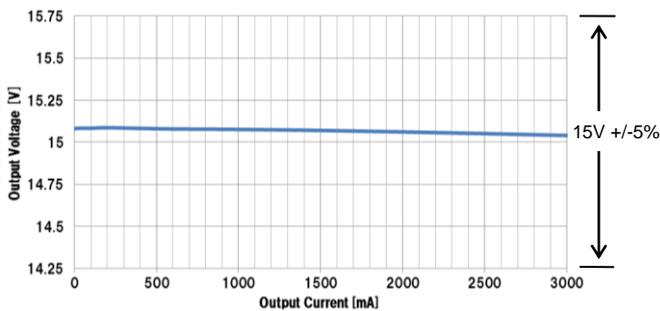
VOUT=9.15V



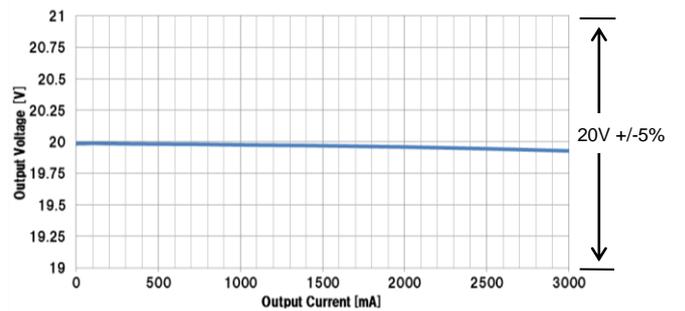
VOUT=12.1V



VOUT=15.1V



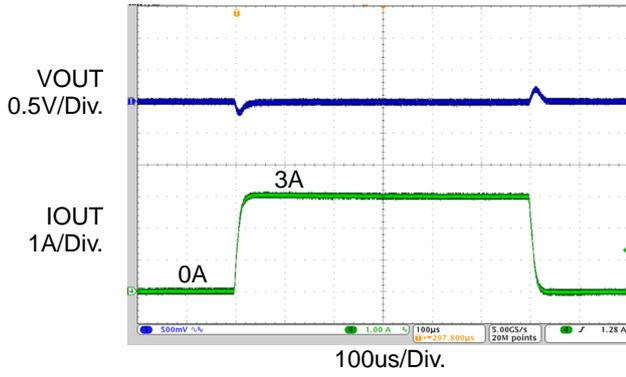
VOUT=20V



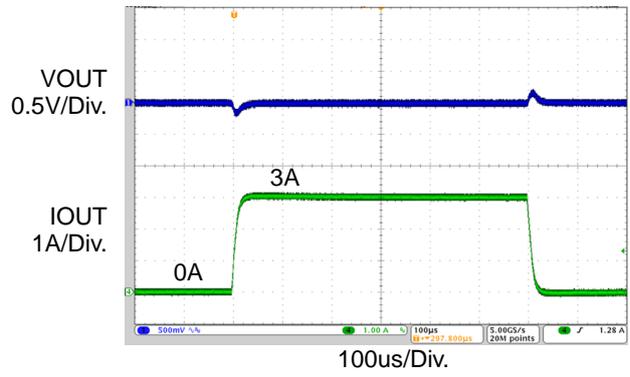
Load Step Transient Waveforms

L = 6.8uH, CIN = 20uF, COU T = 44uF

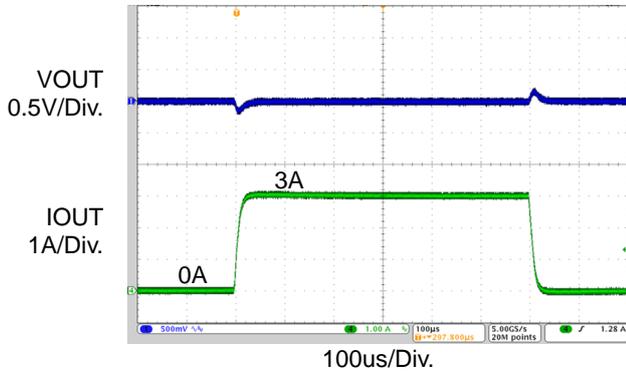
VOUT=5.3V



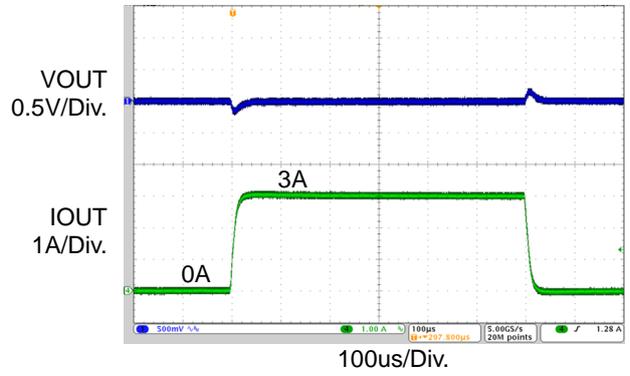
VOUT=9.15V



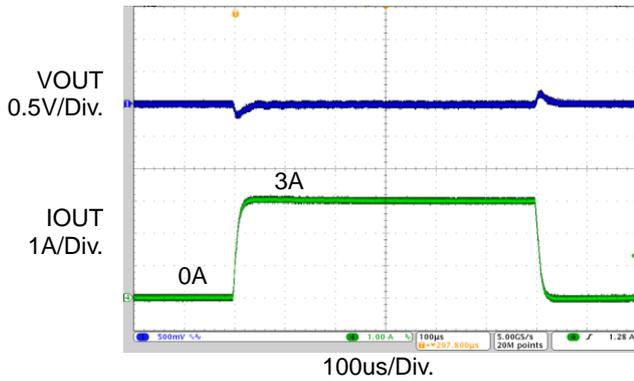
VOUT=12.1V



VOUT=15.1V

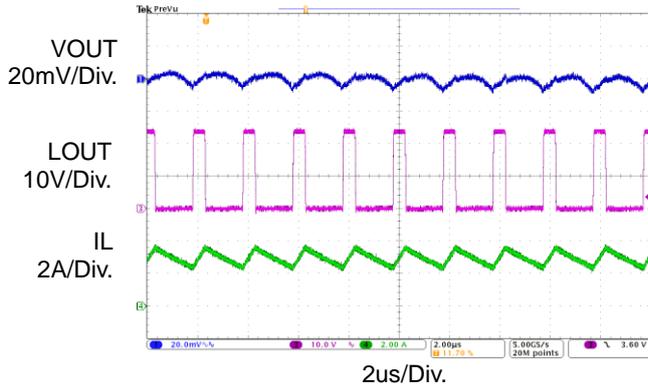


VOUT=20V



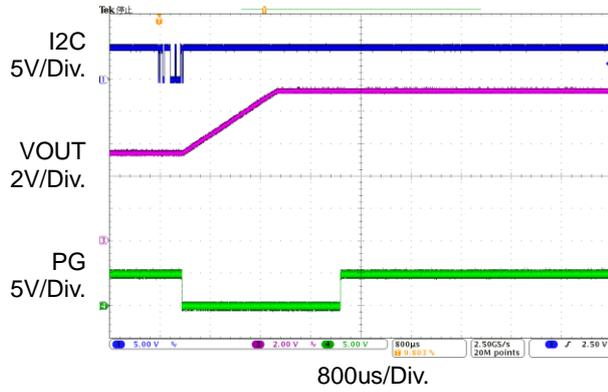
Operation Waveforms

VOUT = 5.3V, IOUT = 3A

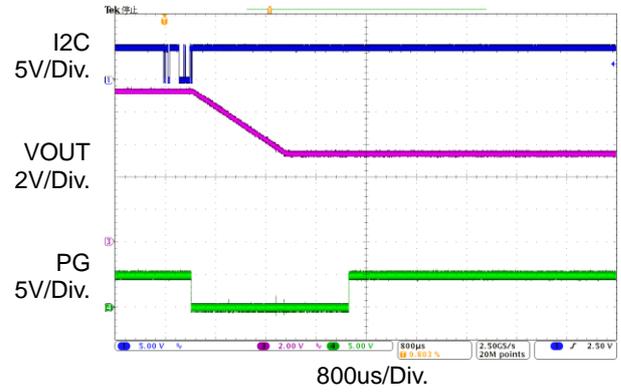


Output Voltage Changing Waveforms

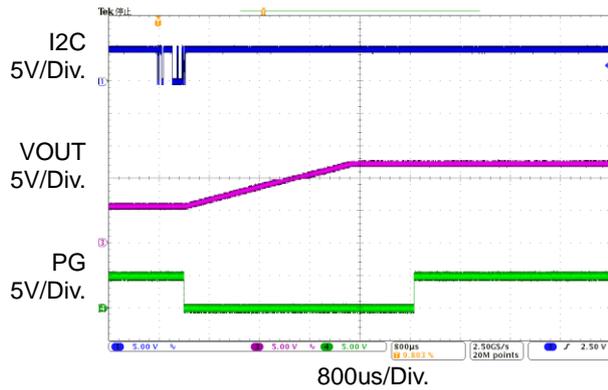
VOUT : 5.3V to 9.15V



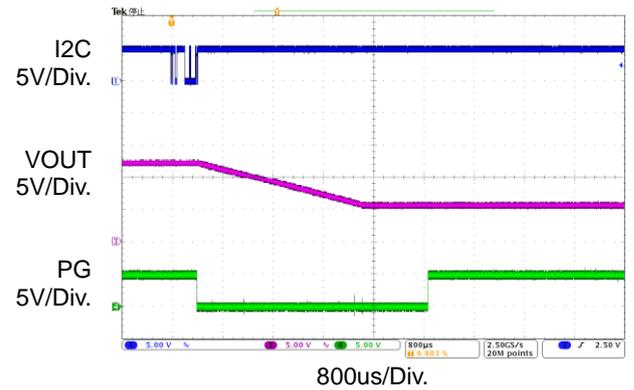
VOUT : 9.15V to 5.3V



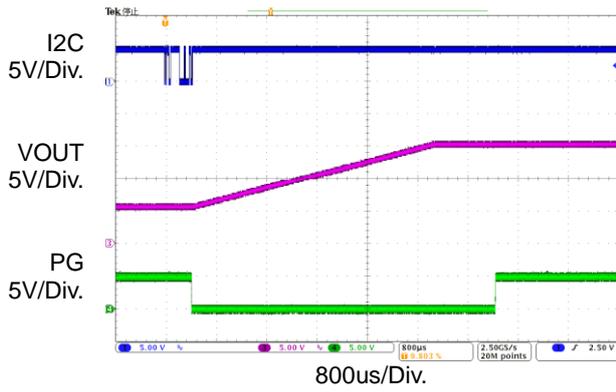
VOUT : 5.3V to 12.1V



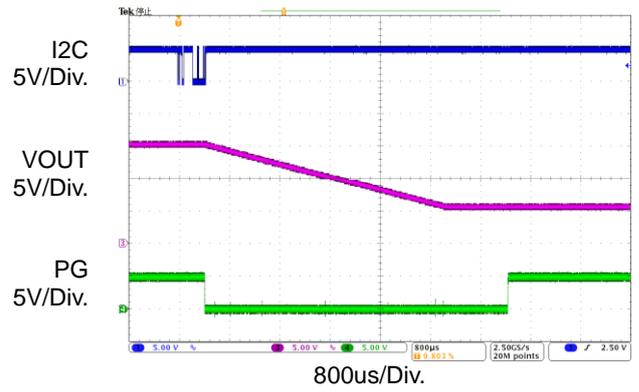
VOUT : 12.1V to 5.3V



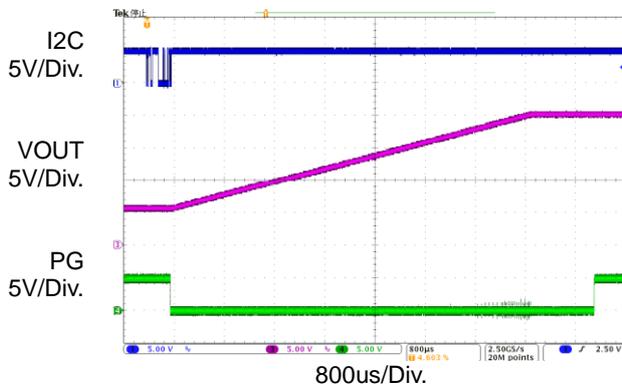
VOUT : 5.3V to 15.1V



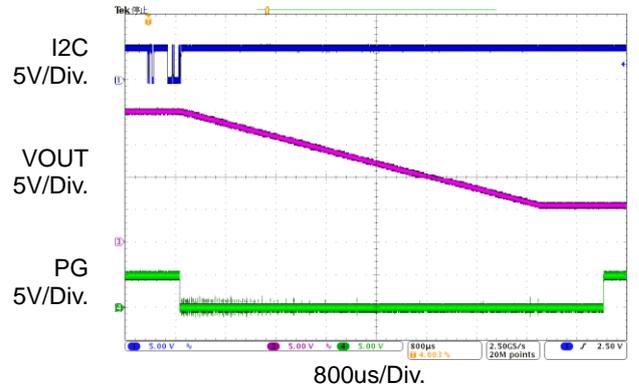
VOUT : 15.1V to 5.3V



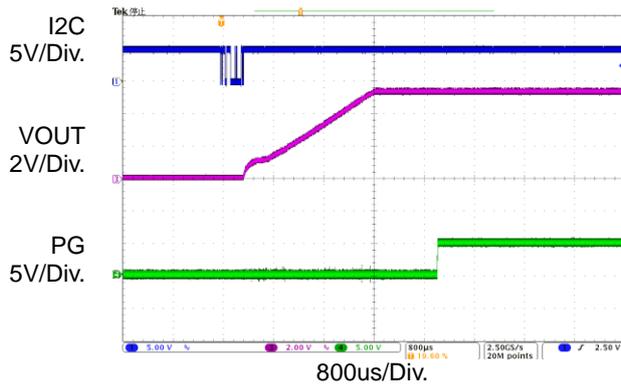
VOUT : 5.3V to 20V



VOUT : 20V to 5.3V

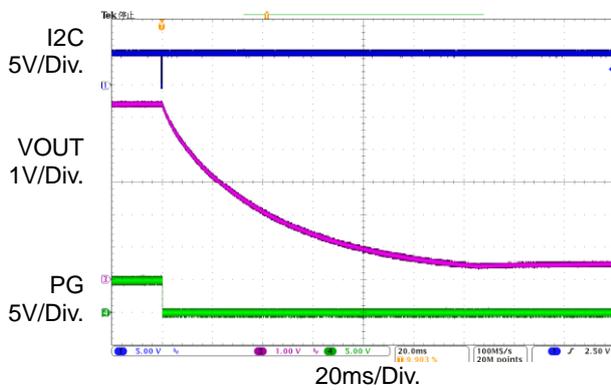


Start-up Waveforms

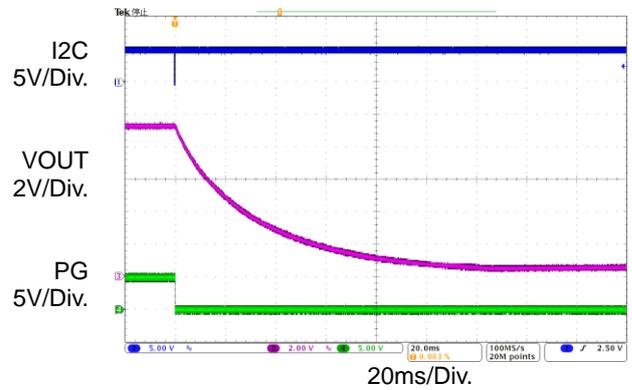


Shutdown Waveforms

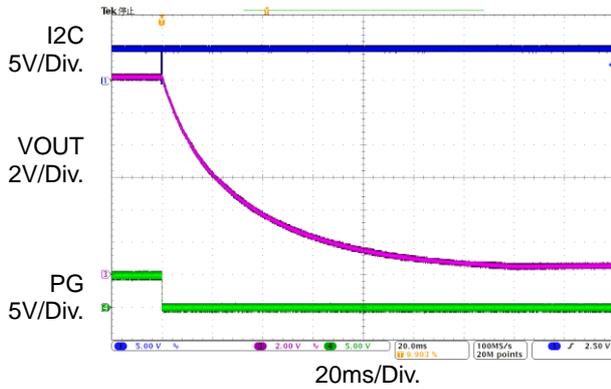
VOUT = 5.3V



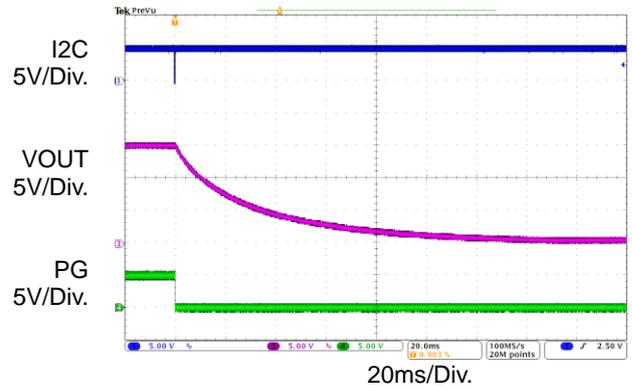
VOUT = 9.15V



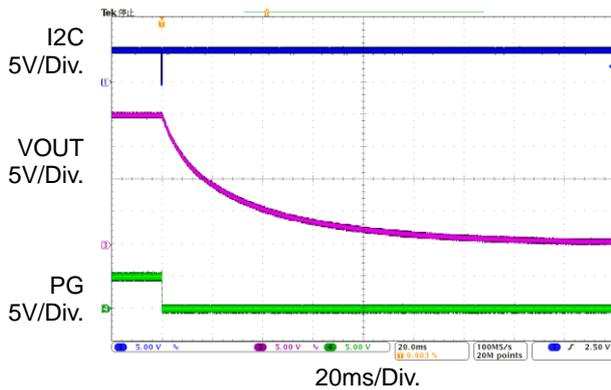
VOUT = 12.1V



VOUT = 15.1V



VOUT = 20V



Operation mode

Mode	EN pin	I2C	DC/DC
OFF	Low	Not acceptable	Stop
STBY	High	Acceptable	Stop
ON	High	Acceptable	Operation

- OFF Mode

The IC stops the operation when EN pin is Low.

- STBY Mode

The IC starts the operation and become this mode when EN pin become High. The IC can receive I2C signal in this mode. DC/DC doesn't output the voltage yet.

- ON Mode (DC/DC_ON)

The IC status is normal operation.

The DC/DC starts to output the voltage when receiving ON signal through I2C at STBY mode, so VBUS starts to output the voltage.

The DC/DC output voltage (VBUS) is changed to OFF when receiving OFF signal through I2C, then the IC becomes Standby mode.

Explain of Operation

- Output voltage
5.3V, 9.15V, 12.1V, 15.1V, 20V are set by I2C.
The voltages are higher because of the line impedance (about 100m Ω) like a load switch on VBUS.
- Output current setting
0.5A, 1A, 1.5A, 2A, 2.5A, 3A are set by I2C.
- Soft start
This function decreases sudden change of the output voltage at rising and changing output voltage.
- Discharge circuit
When operation mode is changed from ON mode to STBY mode, the discharge switch becomes on and the capacitor connected to the output are discharged. After discharging, LOUT pin become HiZ.

Protection Circuit View

Protection circuit	Function	Operation status on protection			Reset
		Common circuit	DC/DC Output	Signal output pin status	
Over voltage protection (OVP)	Detect over voltage at output	Operation	Stop (Latch)	INT=L	By I2C
Over current protection (OCP)	Detect over current at output Operate when the output current is over "output current setting"	Operation	Stop (Latch)	INT=L	By I2C
Over temperature protection (OTP)	Detect over temperature of IC chip	Operation	Stop (Latch)	INT=L	By I2C
Short circuit protection (SCP)	Detect output voltage drop due to short circuit, etc.	Operation	Stop (Latch)	INT=L	By I2C
Watch Dog Timer (WDT)	Detect abnormal operation of the system. Provide single "L" pulse from MR pin at detecting to reset MCU, etc.	Operation	Stop (Latch)	Provide single "L" pulse from MR pin	By I2C
Under voltage lockout circuit (UVLO_VIN)	Detect drop of VIN	Stop	Stop	-	Recover automatically as Standby mode when EN="H"

Note OVP : Over Voltage Protection
 OCP : Over Current Protection
 OTP : Over Temperature Protection
 SCP : Short Circuit Protection
 UVLO : Under Voltage Lockout Circuit
 WDT : Watch Dog Timer

Notes on Use

- Pattern Writing

Separate the ground of control signals from the ground of power line so that these grounds do not have a common impedance as much as possible.

Place the VREG capacitor near the VREG pin to reduce the noise into the pin.

Keep the pattern lines for large current (PVIN, LOUT and PGND) broad and short as much as possible to lower the characteristic impedance and shorten the current loop. Providing sufficient vias is preferable when using vias.

Do not allow feedback current (switching current) to flow under the IC.

Place the PVIN capacitor near the PVIN pin to reduce the noise into the pin.

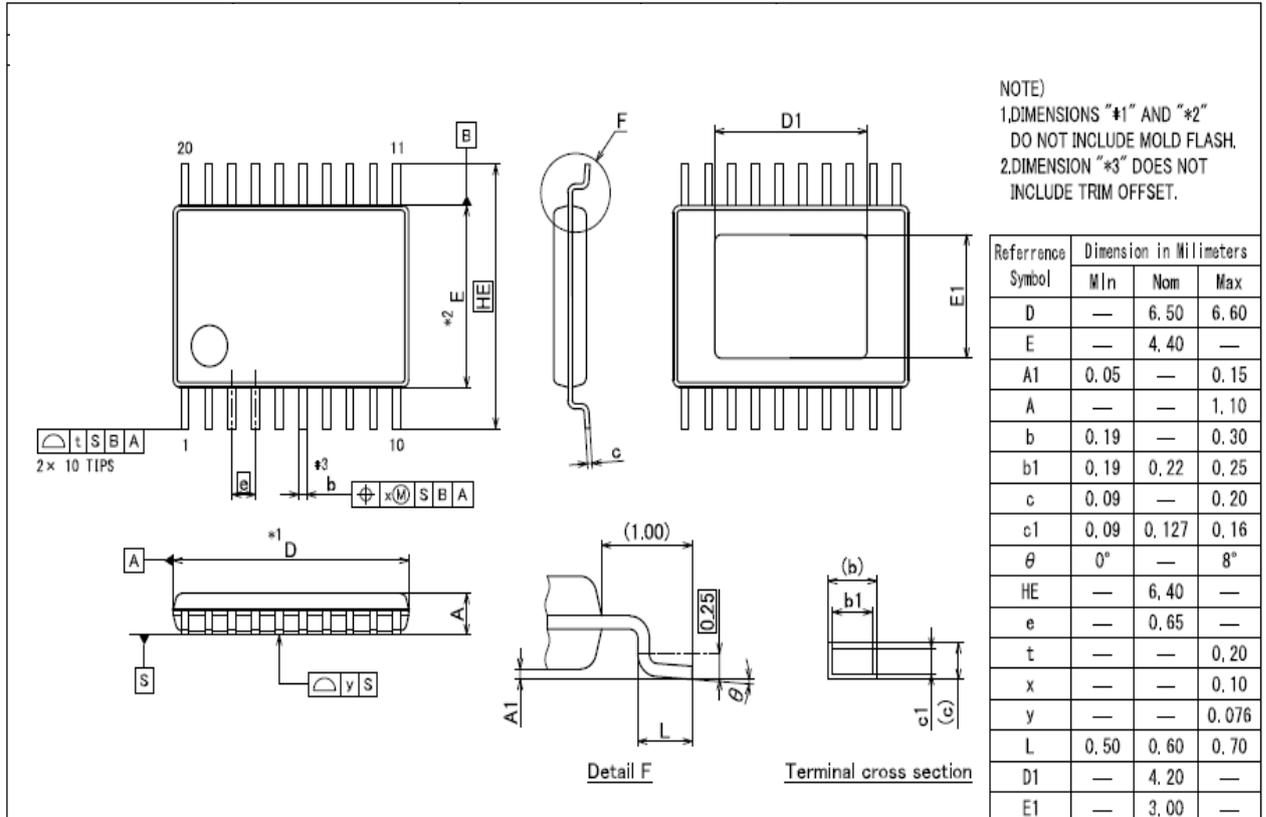
- Connection of Exposed PAD

HTSSOP package has an Exposed PAD on the bottom to improve radiation performance. At mounting it on the board, connect this Exposed PAD to GND. In addition, providing sufficient vias is preferable at Exposed PAD.

Package Dimensions

20pin HTSSOP

Renesas code : PTSP0020JF-A



Revision History	RAA230161 Data Sheet
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Rev.	Date	Description	
		Page	Summary
1.00	Dec. 4. 2017	-	First Edition issued.

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