

RJH60D1DPP-A0

600V - 10A - IGBT
Power Switching

R07DS1458EJ0110
Rev.1.10
Mar.01.20

Features

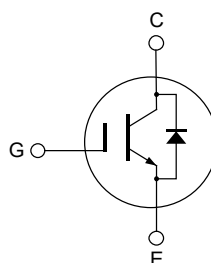
- Trench gate and thin wafer technology
- Built in fast recovery diode (100 ns typ.) in one package
- Low collector to emitter saturation voltage
 $V_{CE(sat)} = 1.9 \text{ V typ. (at } I_C = 10 \text{ A, } V_{GE} = 15 \text{ V, } T_C = 25^\circ\text{C)}$
- High speed switching
 $t_f = 75 \text{ ns typ. (at } V_{CC} = 300 \text{ V, } V_{GE} = 15 \text{ V, } I_C = 10 \text{ A, } R_g = 5 \Omega, \text{ inductive load)}$
- Short circuit withstand time (5 $\mu\text{s typ.}$)
- Applications: Inverter
- Quality grade: Standard

Key Performance

Type	V_{CES}	I_C	$V_{CE(sat)}, T_C=25^\circ\text{C}$	T_j
RJH60D1DPP-A0	600 V	10 A	1.9 V	150 $^\circ\text{C}$

Outline

RENESAS Package code: PRSS0003AP-A
(Package name: TO-220FPA)



1. Gate
2. Collector
3. Emitter

Absolute Maximum Ratings

(T_c = 25 °C)

Item		Symbol	Ratings	Unit
Collector to emitter voltage		V _{CES}	600	V
Gate to emitter voltage		V _{GES}	±30	V
Collector current	T _c = 25 °C	I _C	20	A
	T _c = 100 °C	I _C	10	A
Collector peak current		I _{C(peak)} ^{Note1}	40	A
Diode forward current		I _F	10	A
Diode forward peak current		I _{F(peak)} ^{Note1}	40	A
Collector power dissipation		P _C	30	W
Junction temperature		T _j	150	°C
Storage temperature		T _{stg}	–55 to +150	°C

Note: Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Notes: 1. PW ≤ 10 μs, duty cycle ≤ 1 %

Thermal Resistance Characteristics

(T_c = 25 °C)

Item	Symbol	Max. Value ^{Notes2}	Unit
Junction to case thermal resistance (IGBT)	R _{th(j-c)}	4.1	°C/W
Junction to case thermal resistance (Diode)	R _{th(j-c)}	7.2	°C/W

Notes: 2. Designed target value on Renesas measurement condition. (Not tested)

Electrical Characteristics

(T_c = 25 °C)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Collector to emitter leakage current	I _{CES}	—	—	5	μA	V _{CE} = 600 V, V _{GE} = 0 V
Gate to emitter leakage current	I _{GES}	—	—	±1	μA	V _{GE} = ±30 V, V _{CE} = 0 V
Gate to emitter threshold voltage	V _{GE(th)}	4.0	—	6.0	V	V _{CE} = 10 V, I _C = 1 mA
Collector to emitter saturation voltage	V _{CE(sat)}	—	1.9	2.5	V	I _C = 10 A, V _{GE} = 15 V Notes3
	V _{CE(sat)}	—	2.6	—	V	I _C = 20 A, V _{GE} = 15 V Notes3
Input capacitance	C _{ies}	—	275	—	pF	V _{CE} = 25 V
Output capacitance	C _{oes}	—	25	—	pF	V _{GE} = 0 V
Reverse transfer capacitance	C _{res}	—	8	—	pF	f = 1 MHz
Total gate charge	Q _g	—	13	—	nC	V _{GE} = 15 V
Gate to emitter charge	Q _{ge}	—	3	—	nC	V _{CE} = 300 V
Gate to collector charge	Q _{gc}	—	5	—	nC	I _C = 10 A
Turn-on delay time	t _{d(on)}	—	30	—	ns	V _{CC} = 300 V
Rise time	t _r	—	13	—	ns	V _{GE} = +15 V/–5 V
Turn-off delay time	t _{d(off)}	—	42	—	ns	I _C = 10 A
Fall time	t _f	—	75	—	ns	R _g = 5 Ω
Turn-on loss energy	E _{on}	—	0.10	—	mJ	Inductive load Notes4
Turn-off loss energy	E _{off}	—	0.13	—	mJ	
Total switching energy	E _{total}	—	0.23	—	mJ	
Short circuit withstand time	t _{sc}	3.0	5.0	—	μs	V _{GE} = 15 V, V _{CC} ≤ 360 V Notes5

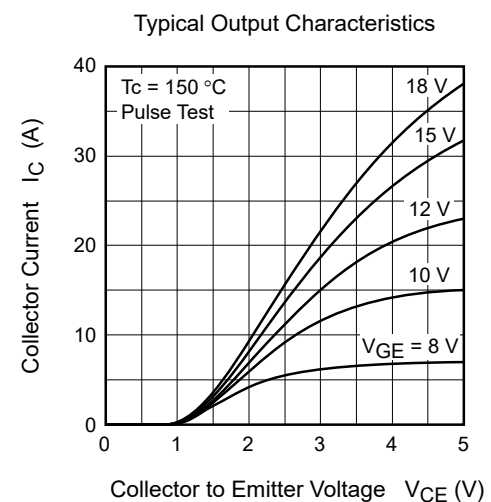
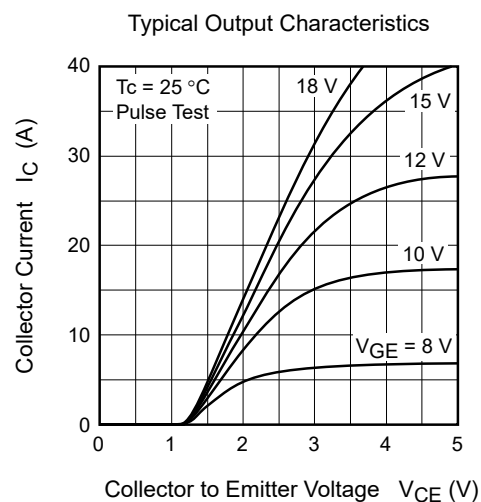
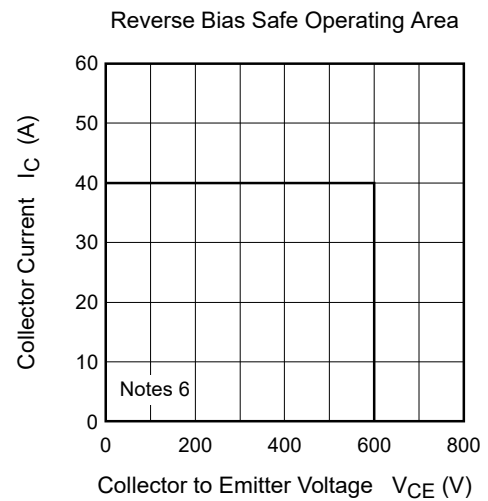
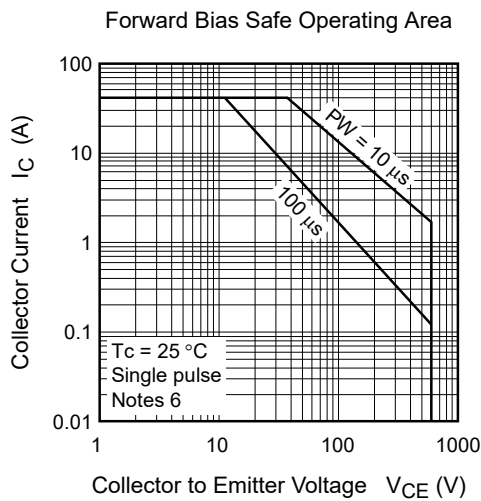
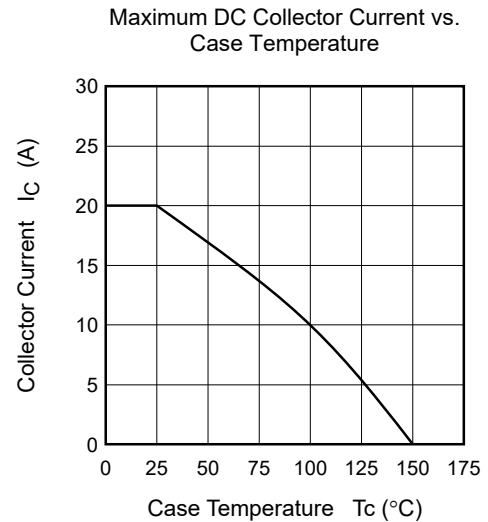
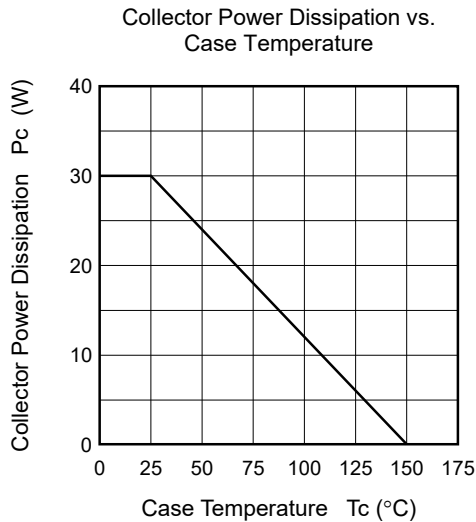
Diode forward voltage	V _F	—	1.4	1.9	V	I _F = 10 A Notes3
Diode reverse recovery time	t _{rr}	—	70	—	ns	I _F = 10 A, di _F /dt = 100 A/μs
Diode reverse recovery charge	Q _{rr}	—	0.11	—	μC	
Diode peak reverse recovery current	I _{rr}	—	3.5	—	A	

Notes: 3. Pulse test

4. Switching time test circuit and waveform are shown below.

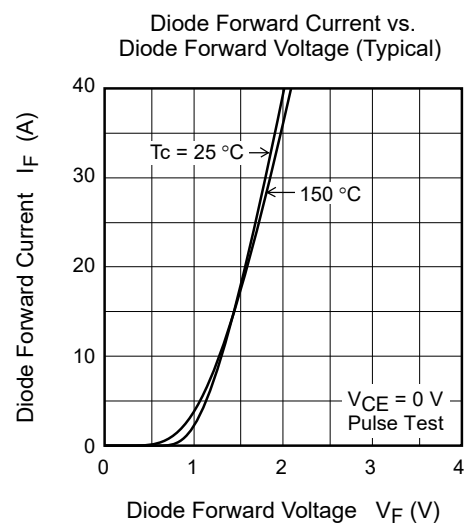
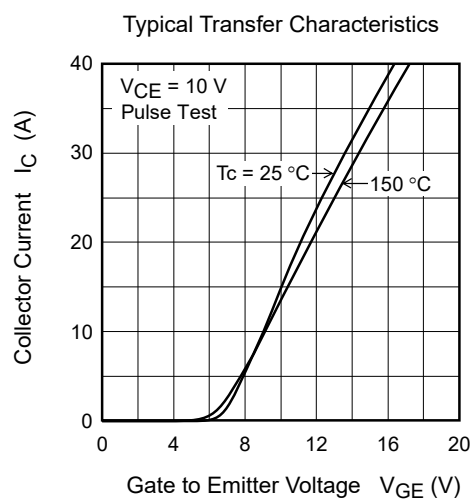
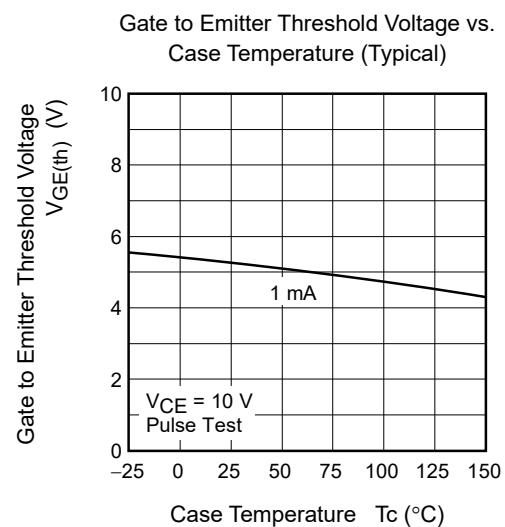
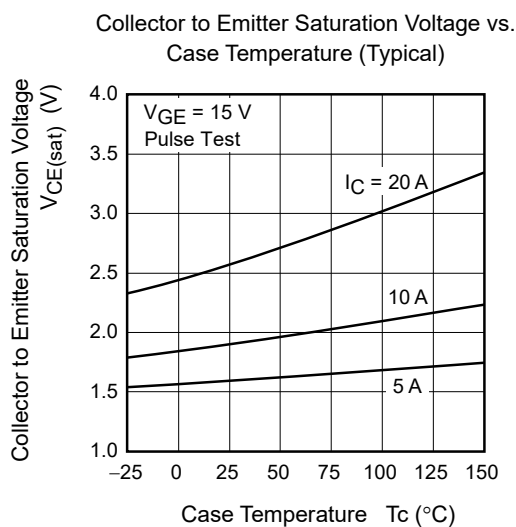
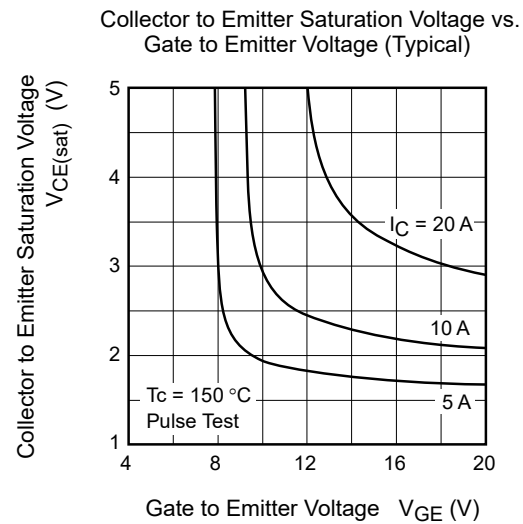
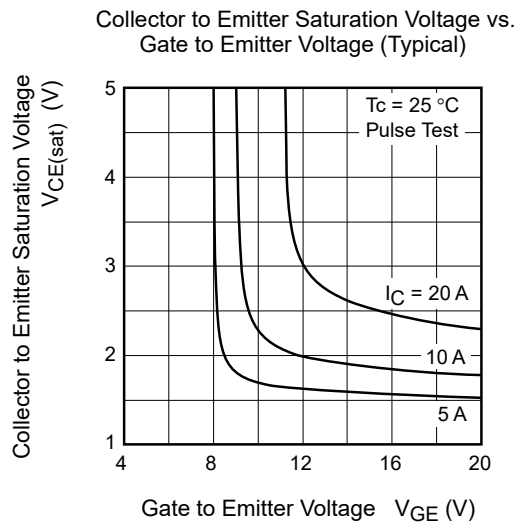
5. Designed target value on Renesas measurement condition. (Not tested)

Main Characteristics

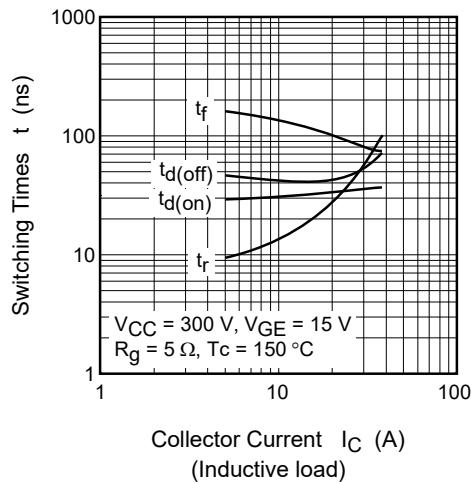


Notes: 6 Designed target value on Renesas measurement condition. (Not tested)

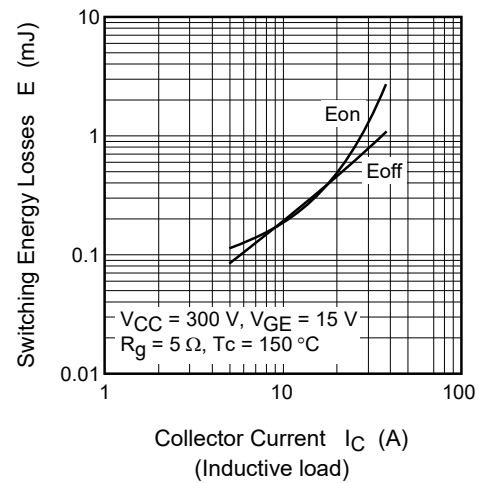
Renesas recommends that operating conditions are designed according to a document "Power MOS FET • IGBT Attention of Handling Semiconductor Devices".



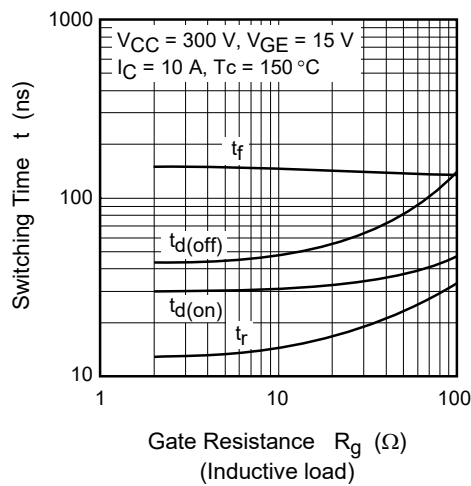
Switching Characteristics (Typical) (1)



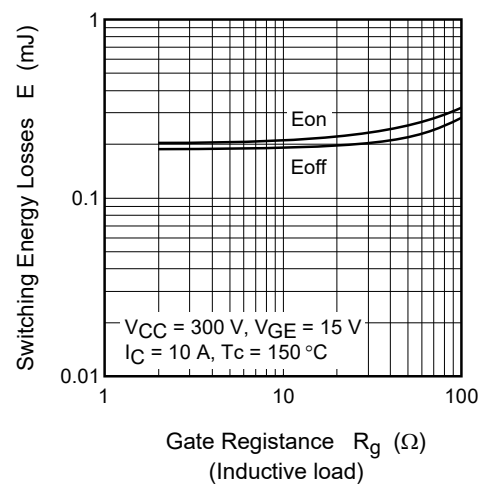
Switching Characteristics (Typical) (2)



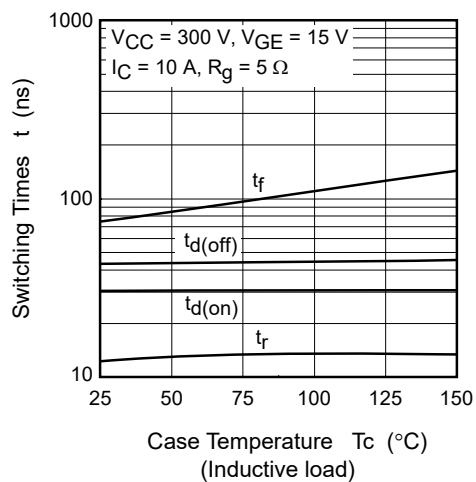
Switching Characteristics (Typical) (3)



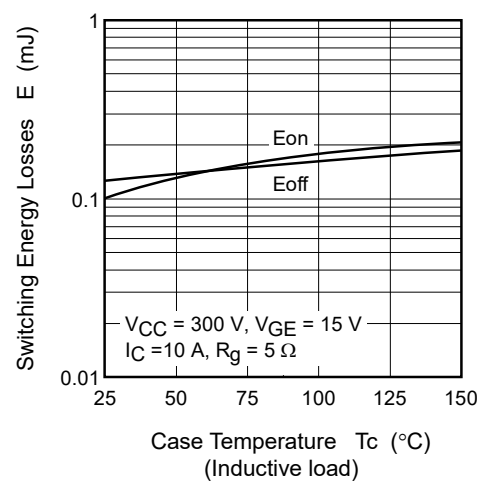
Switching Characteristics (Typical) (4)

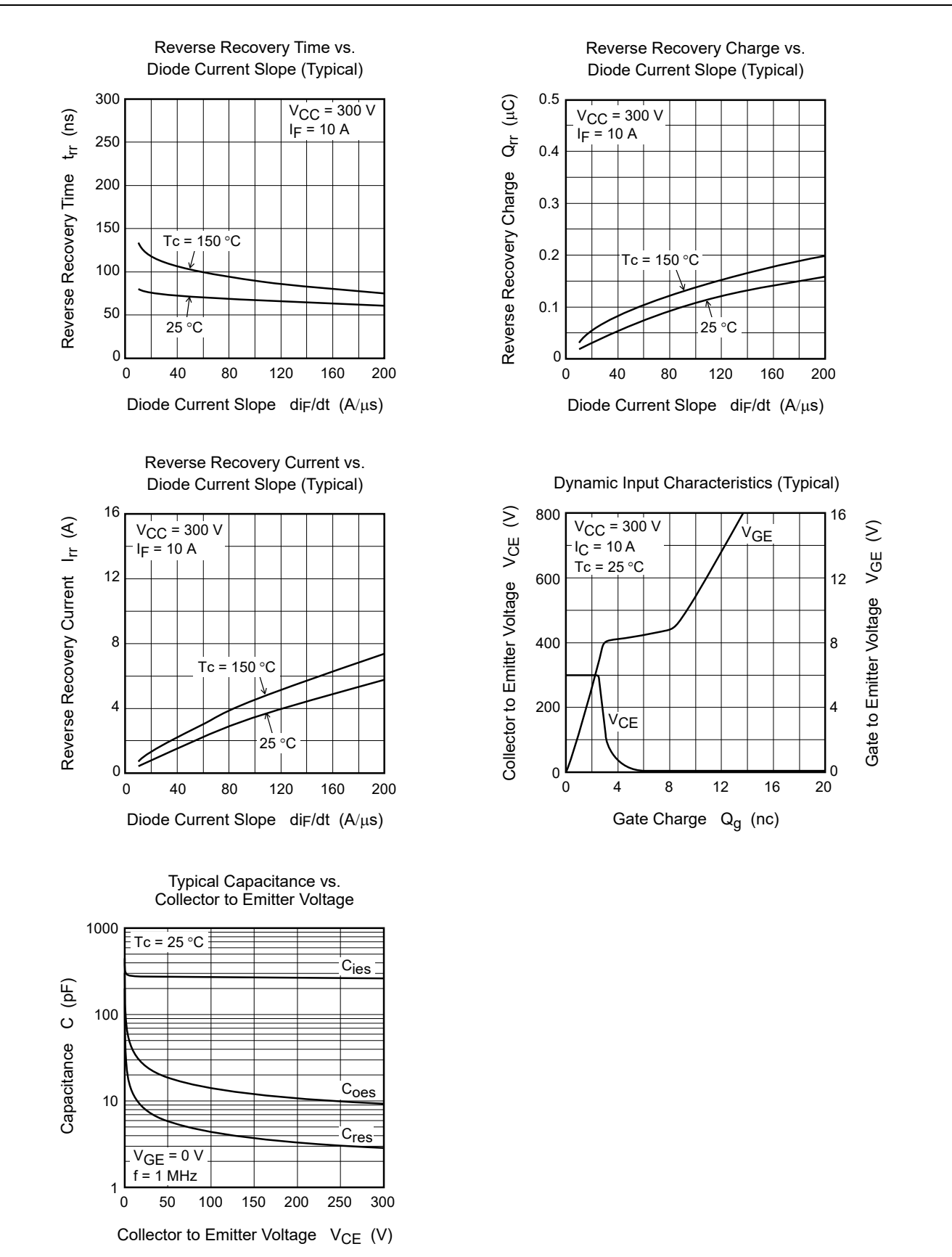


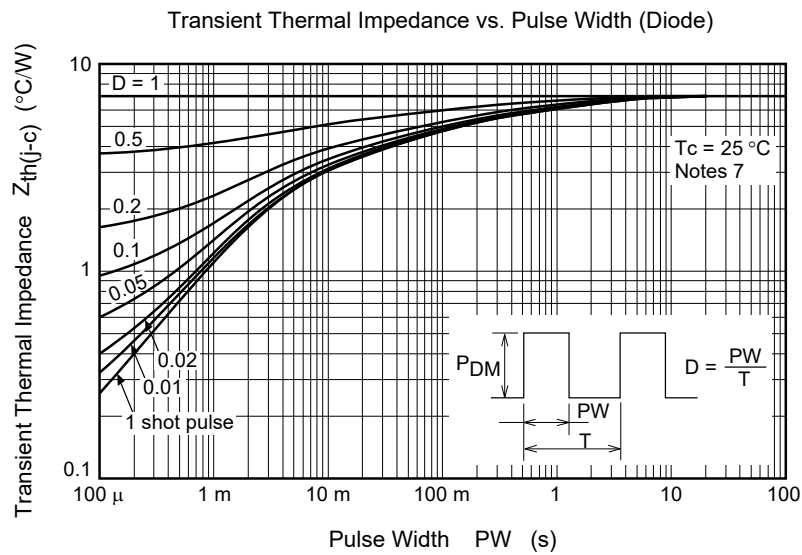
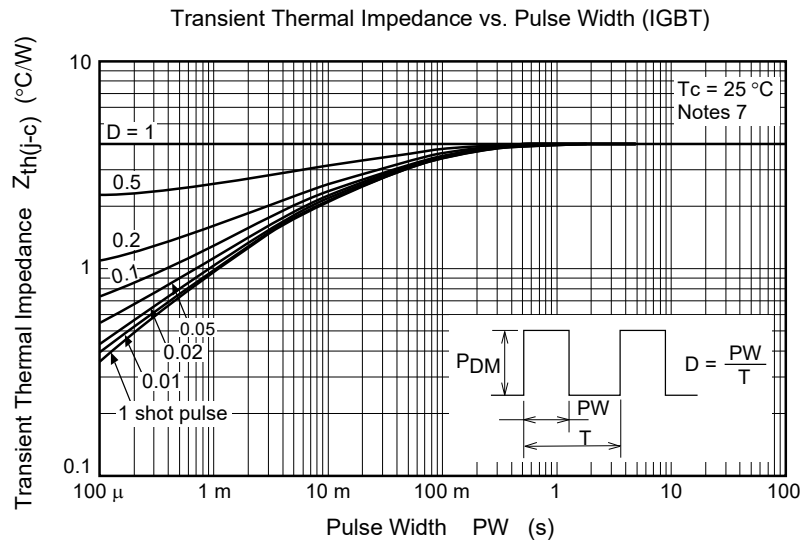
Switching Characteristics (Typical) (5)



Switching Characteristics (Typical) (6)

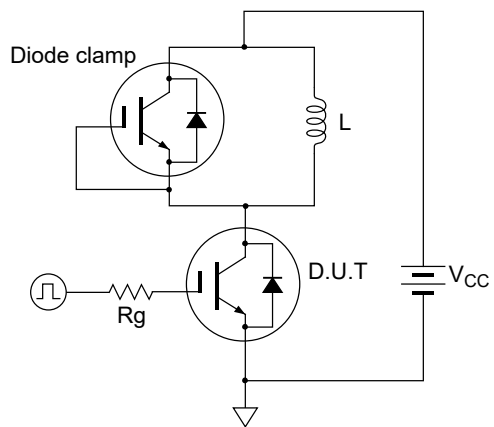




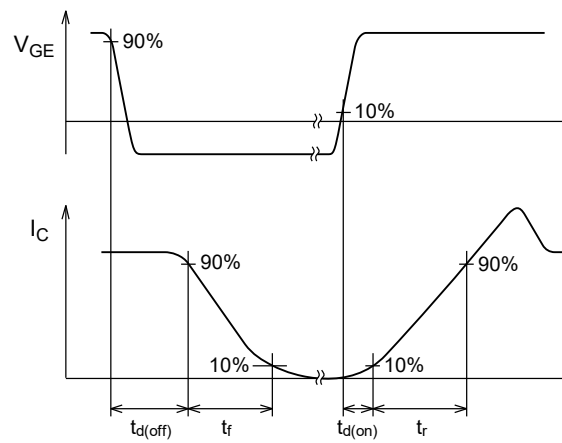


Notes: 7. Designed target value on Renesas measurement condition. (Not tested)

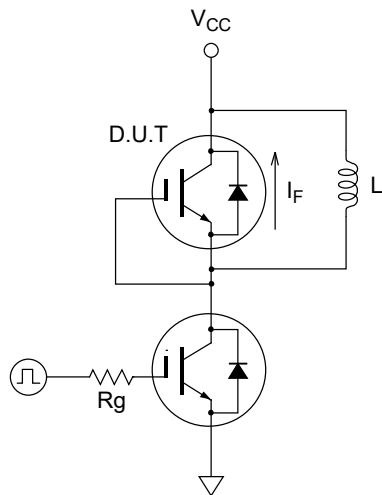
Switching Time Test Circuit



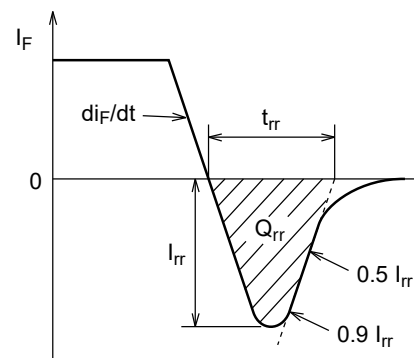
Waveform



Diode Reverse Recovery Time Test Circuit



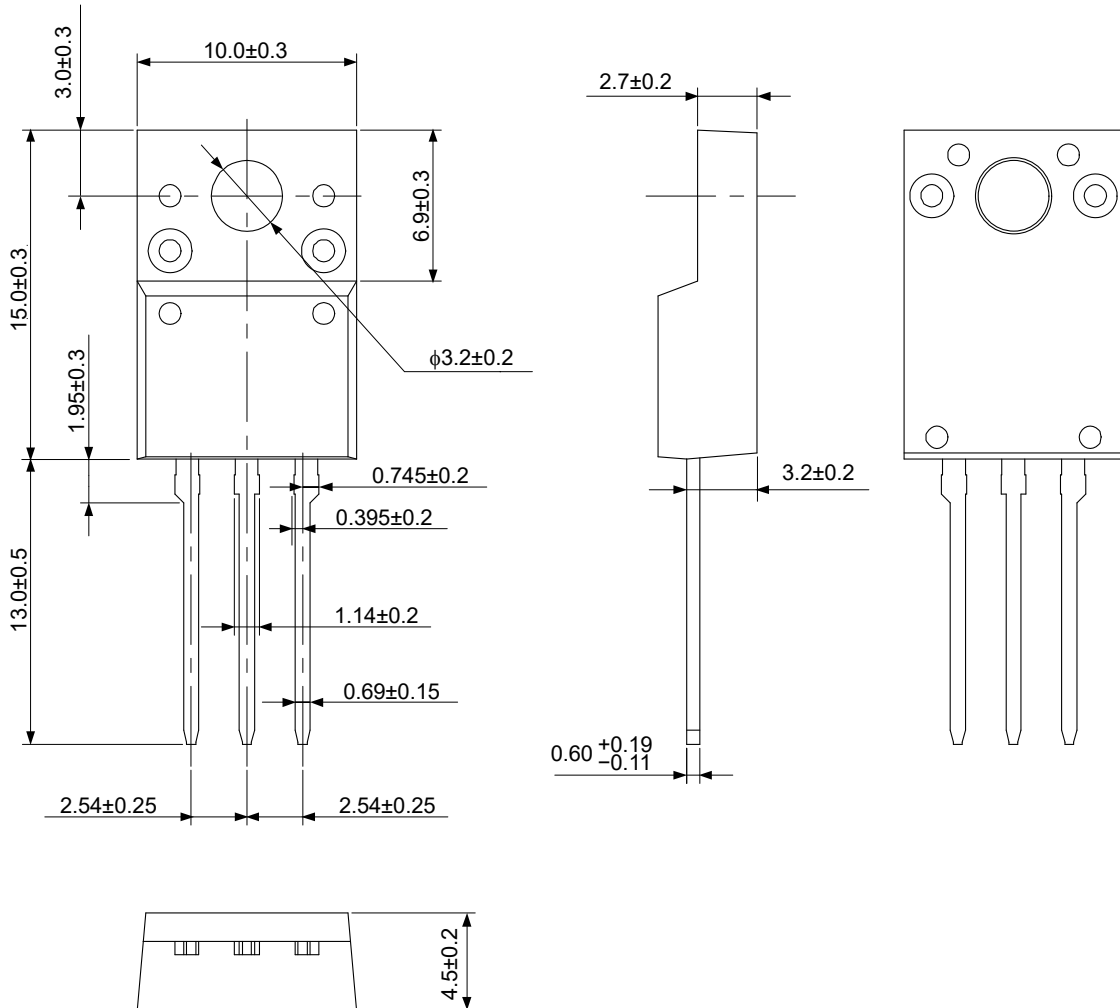
Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS (Typ) [g]
TO-220FPA	—	PRSS0003AP-A	TO-220FPA	1.65

Unit: mm



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Ordering Information

Orderable Part No.	Quantity	Shipping Container
RJH60D1DPP-A0#T2	2500 pcs	Box (Tube)

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