

RJH65S04DPQ-A0

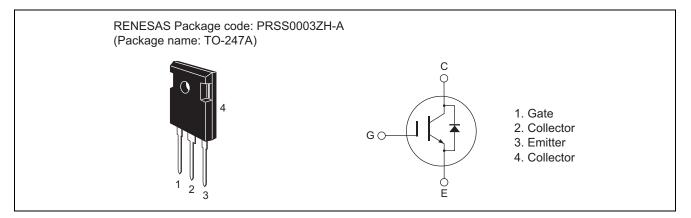
650V - 50A - IGBT Application: Inverter

R07DS0849EJ0001 Rev.0.01 Jul 06, 2012

Features

- Low collector to emitter saturation voltage $V_{CE(sat)} = 1.6$ V typ. (at $I_C = 50$ A, $V_{GE} = 15$ V, $Ta = 25^{\circ}C$)
- Built in fast recovery diode in one package
- Trench gate and thin wafer technology
- High speed switching $t_f = 80$ ns typ. (at $_{CC} = 300$ V, $V_{GE} = 15$ V, $I_C = 50$ A, $Rg = 10 \Omega$, $Tj = 125^{\circ}C$, inductive load)

Outline



Absolute Maximum Ratings

				$(Ta = 25^{\circ}C)$
Item		Symbol	Ratings	Unit
Collector to emitter voltage / diode reverse voltage		V _{CES} / V _R	650	V
Gate to emitter voltage		V _{GES}	±30	V
Collector current	Tc = 25°C	Ιc	100	A
	Tc = 100°C	Ιc	50	A
Collector to emitter diode forward current	Tc = 25°C	I _{DF}	100	A
	Tc = 100°C	I _{DF}	50	A
Collector dissipation		Pc ^{Note1}	378	W
Junction temperature		Tj	150	°C
Storage temperature		Tstg	–55 to +150	°C

Notes: 1. Value at $Tc = 25^{\circ}C$



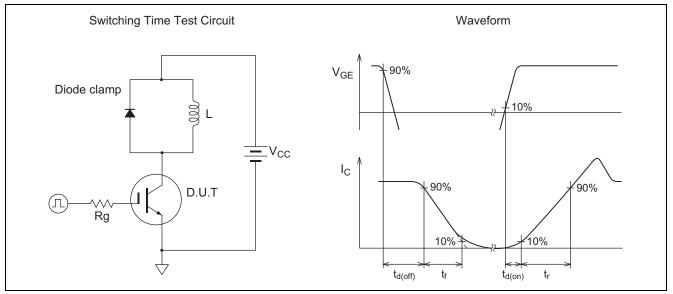
Electrical Characteristics

						$(Ta = 25^{\circ}C)$	
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Collector to emitter breakdown voltage	V _{BR(CES)}	650	—	—	V	$I_{C} = 10 \ \mu A, \ V_{GE} = 0$	
Zero gate voltage collector current / Diode reverse current	I _{CES} / I _R	—	—	1	μΑ	$V_{CE} = 650 \text{ V}, \text{ V}_{GE} = 0$	
Gate to emitter leak current	I _{GES}	_	—	±1	μΑ	$V_{GE} = \pm 30 \text{ V}, \text{ V}_{CE} = 0$	
Gate to emitter cutoff voltage	V _{GE(off)}	5.0	—	6.8	V	$V_{CE} = 10 \text{ V}, \text{ I}_{C} = 1 \text{ mA}$	
Collector to emitter saturation voltage	V _{CE(sat)}	_	1.60	1.95	V	$I_{C} = 50 \text{ A}, V_{GE} = 15 \text{ V}^{\text{Note2}}$	
Input capacitance	Cies	_	4.5	_	nF	V _{CE} = 25 V	
Output capacitance	Coes	_	0.2	—	nF	$V_{GE} = 0$	
Reveres transfer capacitance	Cres	_	0.15	—	nF	f = 1 MHz	
Turn-on delay time	t _{d(on)}	_	30	—	ns	$\label{eq:V_CC} \begin{array}{l} V_{CC} = 300 \ \text{V}, \ V_{GE} = \pm 15 \ \text{V} \\ I_C = 50 \ \text{A}, \ \text{Rg} = 10 \ \Omega \\ T_j = 125^\circ\text{C} \\ \end{array}$	
Rise time	tr	_	30	—	ns		
Turn-off delay time	t _{d(off)}	_	200	—	ns		
Fall time	t _f	_	80	_	ns	(Inductive load) Note3	
Short circuit withstand time	t _{sc}	10	—	—	μS	$\label{eq:V_CC} \begin{array}{l} V_{CC} \leq 360 \ V, \ V_{GE} = 15 \ V \\ Tj = 150^\circ C \end{array}$	

FRD forward voltage	VF	_	1.7	 V	$I_F = 50 \text{ A}^{\text{Note2}}$
FRD reverse recovery time	trr	—	90	 ns	$I_F = 50 \text{ A}, \text{ di}_F/\text{dt} = 1500 \text{ A}/\mu\text{s}$
	trr	—	180	 ns	$I_F = 50 \text{ A}, \text{ di}_F/\text{dt} = 100 \text{ A}/\mu\text{s}$

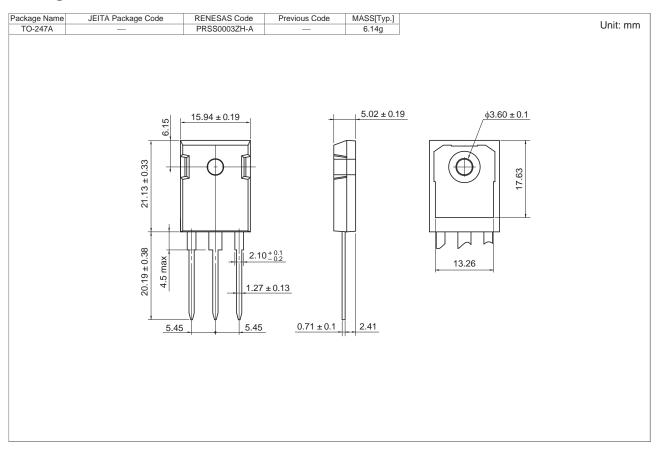
Notes: 2. Pulse test

3. Switching time test circuit and waveform are shown below.



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Package Dimension





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Renesas Electronics Corporation

http://www.renesas.com

Renesas Electronics America Inc.

2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.

Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited

1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada

Tei: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited

Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K

Tei: +44-1628-585-100, Fax: +444-1628-585-900

Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Disseldorf, Germany

Tei: +49-211-65030, Fax: +449-11-6503-1327

Renesas Electronics (Shanghal) Co., Ltd.

Th Fibor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China

Tei: +86-10-8235-1155, Fax: +862-10-8235-7679

Renesas Electronics (Shanghal) Co., Ltd.

Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong

Tei: +862-2886-9318, Fax: +852-2886-9022/9044

Penesas Electronics Taiwan Co., Ltd.

Tash, No, 33, Fu Shing North Road, Taipei, Taiwan

Tei: +862-28175-9800, Fax: +862-28175-9870

Renesas Electronics Singapore Ple. Ld.

1 harbourfront Avenue, 406-10, keppel Bay Tower, Singapore 098632

Tei: +805-28175-9800, Fax: +862-28175-98070

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