

RJU1C06DWA

1200V - 150A - Fast Recovery Diode

R07DS1549EJ0100

Rev.1.00

Oct.25th.2024

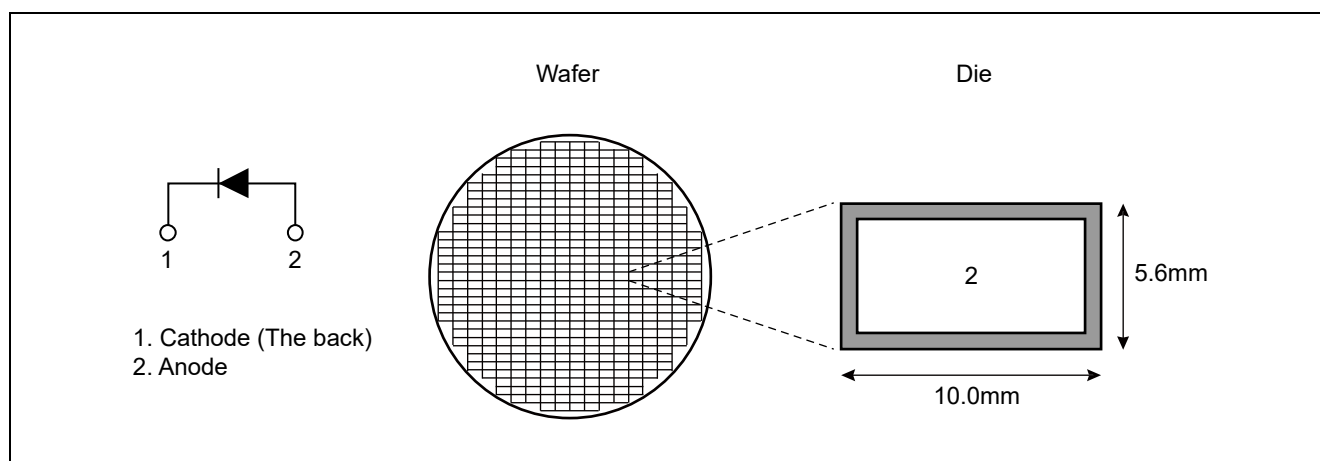
Features

- Fast Recovery Diode technology
- Low forward voltage
 $V_F = 1.8 \text{ V typ. (at } I_F = 150 \text{ A, } T_j = 25 \text{ °C)}$
- Applications: Inverter
- Quality grade: Standard

Key performance

Product name	V_{CE}	I_c	Die size	Package
RJU1C06DWA	1200 V	150 A	56.0 mm ² (10.0 mm x 5.6 mm)	Unsaun wafer

Outline



Mechanical Parameters

Parameter	value
Die size	56.0 mm ² (10.0 mm x 5.6 mm)
Anode pad size	4.37 mm x 8.77 mm
Die thickness	130 μm
Wafer size	200 mm
Passivation front side	Polyimide
Pad metallization	AlSi – 5.5 μm
Backside metallization	Ni / Au - 0.6 μm / 0.1 μm
Die attach recommendation	Solder
Wire bond recommendation	Al wire ≤ 500 μm
Recommended storage environment	Stored in original container, in dry air or nitrogen. < 6 month after packing, at an ambient temperature of 20 to 30 °C, dew-point under -30 °C

Absolute Maximum Ratings

(T_j = 25 °C unless otherwise noted)

Item	Symbol	Ratings	Unit
Maximum reverse voltage	V _R	1200	V
Forward current	I _F Notes1	—	A
Pulse forward current	I _{F(pulse)} Notes 2, 3	300	A
Junction temperature	T _j Notes4	175	°C

Notes: 1. Depends on thermal properties of assembly, T_j ≤ 175 °C.

2. Not subject to product test – verified by design/characterization.

3. PW = 10 μs, Duty < 1 %

4. Please use this device in the thermal conditions which the junction temperature does not exceed 175 °C. IGBT Application Note is disclosed about reliability test and application condition up to T_j = 175 °C.

5. Continuous heavy condition (e.g. high temperature/voltage/current or high variation of temperature) may affect a reliability even if it is within the absolute maximum ratings. Please consider derating condition for appropriate reliability in reference Renesas Semiconductor Reliability Handbook (Recommendation for Handling and Usage of Semiconductor Devices) and individual reliability data.

Electrical Characteristics 1

(Tested on wafer, T_j = 25 °C unless otherwise noted)

Item	Symbol	Test Conditions	Min	Typ	Max	Unit
Breakdown voltage	V _{(BR)R}	I _R = 100 μA	1200	—	—	V
Reverse leakage current	I _R	V _R = 1200 V	—	—	100	μA
Forward voltage	V _F	I _F = 100 A	—	1.60	2.00	V

Notes: 6. The characteristic items specified in this table guarantee the electrical characteristics in the wafer state but do not the characteristic fluctuations or characteristic defects that occur in the processes after assembling.

7. This value excludes the influence of contact resistance due to probing.

Electrical Characteristics 2

(Not subject to production test, designed target value, T_j = 25 °C unless otherwise noted)

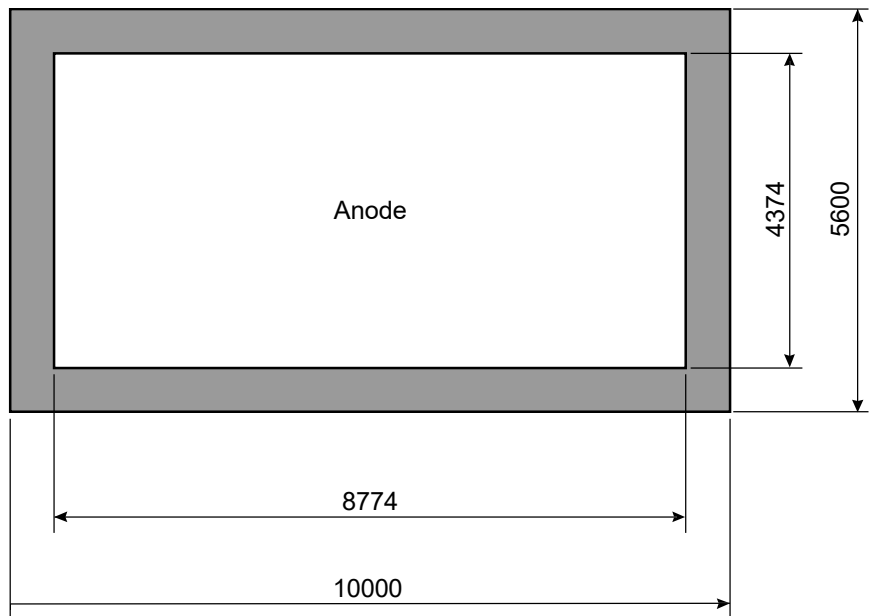
Item	Symbol	Test Conditions	Min	Typ	Max	Unit
Forward voltage	V _F Notes 8, 9	I _F = 150 A	—	1.80	2.20	V

Notes: 8. Designed target value on Renesas measurement condition.

9. This value is influenced by parasitic inductance and assembly condition

10. Switching characteristics is depending strongly on module design and mounting technology and can therefore not be specified for a bare die.

Die Dimension



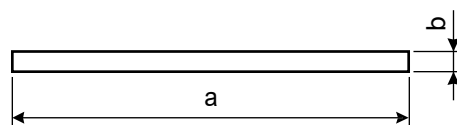
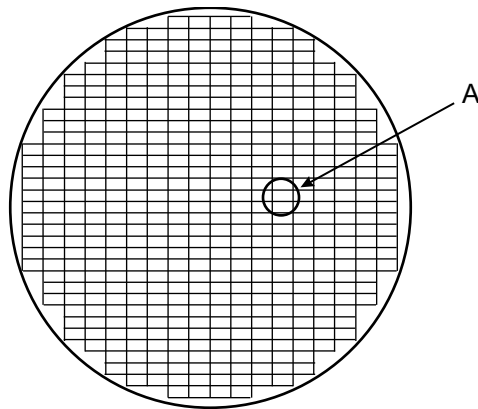
Unit: μm
Die thickness: 130μm
Back side: Cathode

Note 1.

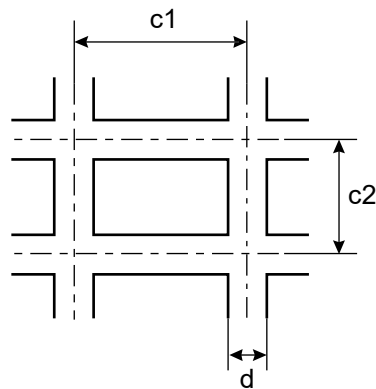
Illustration	Definition
Part of white	Bonding area (AlSi)
Part of gray	Final passivation (Polymide)

Note 2. Recognition, target and any other patterns which are not related to FRD operation, may be changed without notice.

Wafer Dimension



A Enlargement



Item	Symbol	Dimensions (mm)
Wafer diameter	a	200
Wafer thickness	b	0.13
Chip pitch	c1	10.000
	c2	5.600
Scribe grid	d	0.076

Ordering Information

Please contact your Renesas sales representative for sample requests.

Delivery Form	Ordering Part Number	Ordering Quantity Unit
Unsawn wafer	RJU1C06DWA-00#W0	11650 (25wafers)

Note. The order quantities indicate the maximum quantity of chips for each part number, and the actual quantity of chips shipped will be reduced due to yield. These is also a possibility that the number of wafers may decrease during the manufacturing process. The quantity shipped will be indicated on the label as the number of good chips.

Revision History

Rev.	Date	Description	
		Page	Summary
1.00	2024.10.25		First edition

All documents should contain the following section break and paragraph as the last item. The footers of this document refer to the paragraph in order to reference the last page of the document.

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