

RNA55A125FLP

CMOS System Reset IC

R03DS0053EJ0301

Rev.3.01

Mar 01, 2013

Description

RNA55A125 is microcomputer and system reset signal to be generated.

Detection voltage is set with external resistors can be, the internal reference voltage is 1.25 V.

Although the method is used to generate an internal counter when reset delay time, can freely set by an external resistor.

Features

- Reset detection voltage: 1.25 V \pm 2.0%
- Delay time: 200 ms ($R_{OSC} = 1\text{ M}\Omega$ setting)
- Circuit current: 10 μ A
- Open drain output
- Wide supply voltage range: 1.8 V to 5.5 V

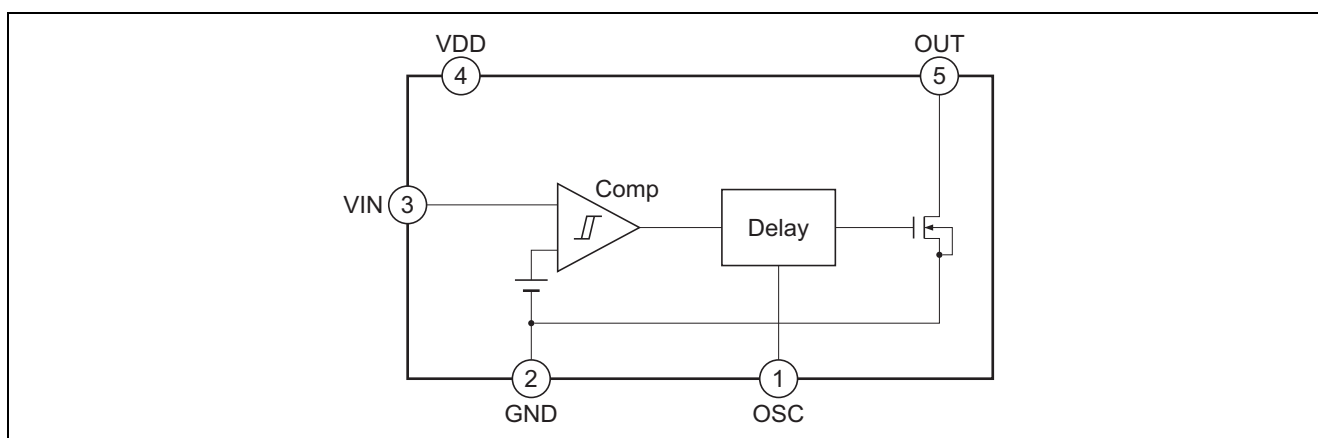
Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)	Surface Treatment
RNA55A125FLPH1	MPAK-5	PLSP0005ZB-A	LP	H (3,000 pcs/reel)	1 (Sn/Bi)

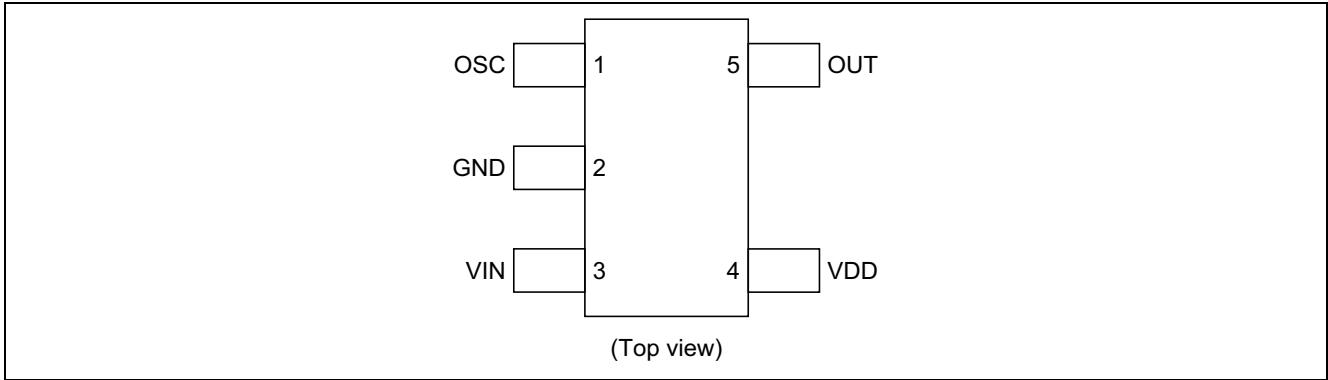
Application

- Power supply voltage monitoring for microprocessors
- Computers and notebook computers
- Digital still camera, digital video camera, and PDA
- Industrial equipment

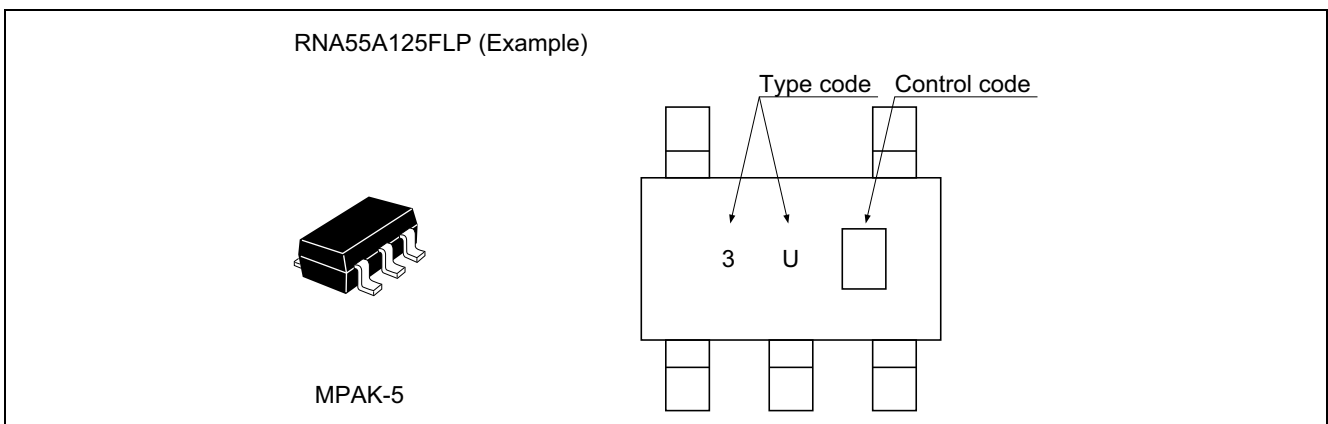
Block Diagram



Pin Arrangement



Marking Indication

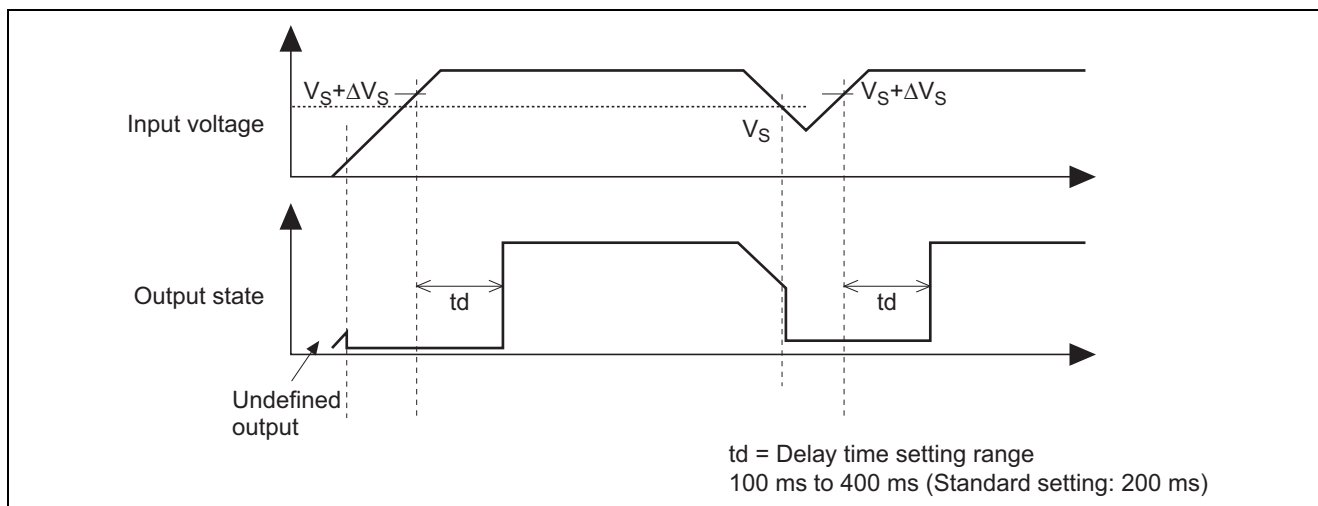


Control code	Starting in January "A", "B", "C", "D", "E", "F", "G", "H", "J", "K", "L", "M"
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Pin Description

Pin No.	Pin Name	I/O	Function
1	OSC	—	<ul style="list-style-type: none"> Connect a resistor for setting the delay time. 200 ms is obtained is 1 MΩ. Range of resistance: 500(kΩ) to 2(MΩ)
2	GND	—	<ul style="list-style-type: none"> Ground
3	VIN	I	<ul style="list-style-type: none"> Input pin for reset detection. Supply voltage applied to resistor divider.
4	VDD	—	<ul style="list-style-type: none"> Sourcing power-supply voltage.
5	OUT	O	<ul style="list-style-type: none"> Open drain output Low will be output when the reset detection Range pull-up resistance: 2.2(kΩ) to 100(kΩ)

Operational Waveform Diagram



Absolute Maximum Ratings

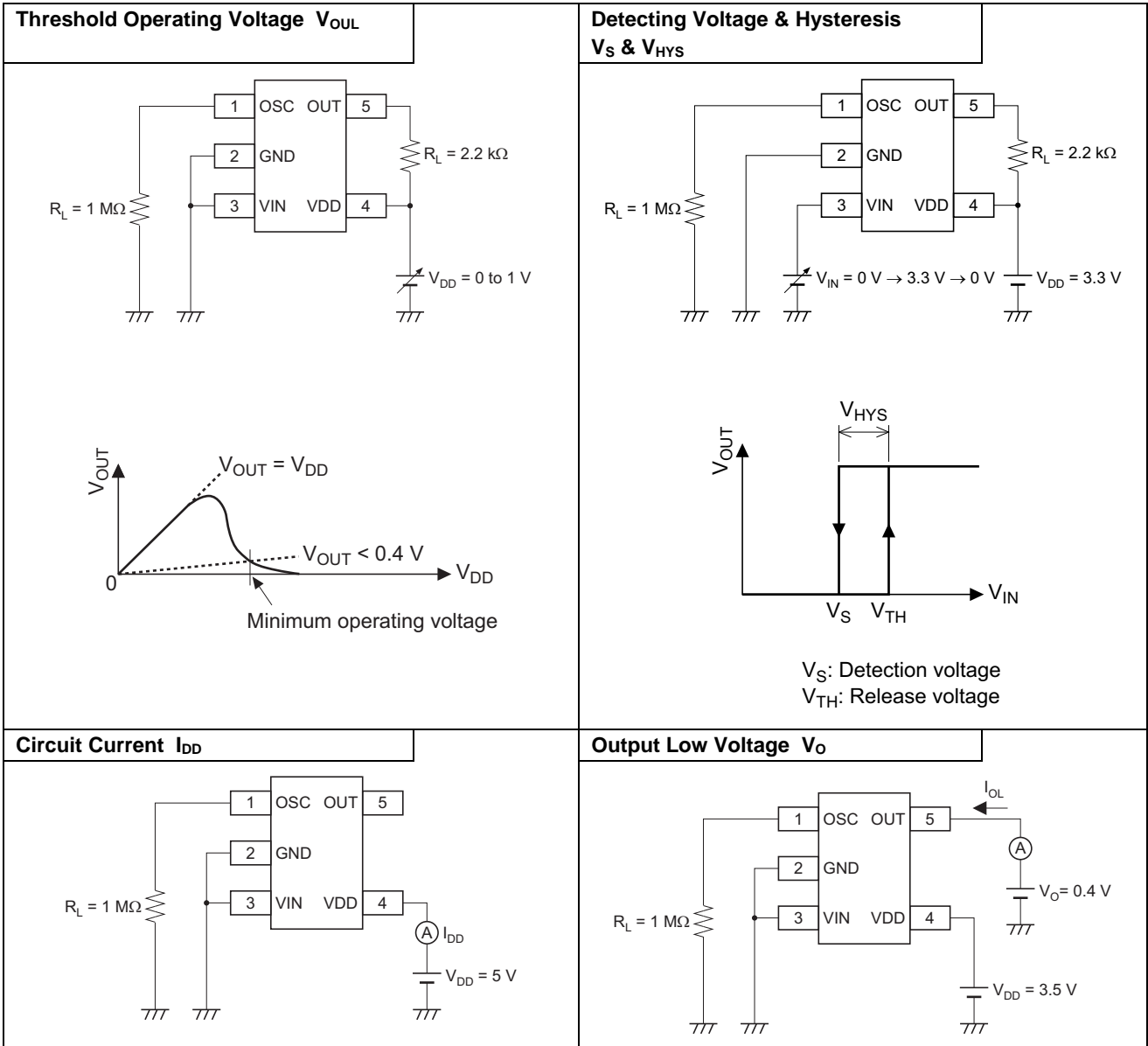
Item	Symbol	Ratings	Unit
Supply voltage	V_{DD}	6.5	V
Output voltage (open-drain type)	V_{OUT}	-0.3 to +6.5	V
Input voltage	V_{IN}	-0.3 to V_{DD}	V
Output current	I_{OUT}	6	mA
Power dissipation	P_d	120 ($T_a = 25^\circ\text{C}$)	mW
Operating temperature	T_{opr}	-40 to +85	$^\circ\text{C}$
Storage temperature	T_{stg}	-40 to +125	$^\circ\text{C}$

Electrical Characteristics

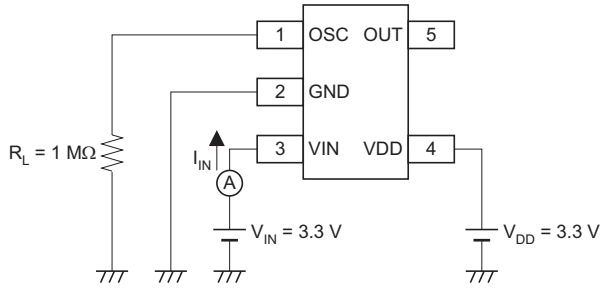
($V_{DD} = 3.3\text{ V}$, $T_a = 25^\circ\text{C}$, unless otherwise noted)

Item	Symbol	Min	Typ	Max	Unit	Test Conditions
Threshold operating voltage	V_{OUL}	—	—	0.9	V	$R_L = 2.2\text{ (k}\Omega\text{)}$, $V_{OUT} < 0.4\text{ V}$
Circuit current	I_{DD}	—	10	20	μA	$V_{DD} = 5.0\text{ V}$, $V_{OUT} = \text{Hi (}V_{DD}\text{)}$
Detecting voltage	V_S	1.225	1.25	1.275	V	
Detecting voltage temperature coefficient	$V_S/\Delta T$	—	± 100	—	ppm/ $^\circ\text{C}$	
Hysteresis voltage	V_{HYS}	3	5	8	%	
Output low voltage	V_{OL}	—	0.2	0.4	V	$I_{OUT} = 4\text{ mA}$, $V_{DD} = 3.5\text{ V}$
Input leakage current	I_{in}	-10	0	10	nA	
Output leakage current	I_{LK}	—	—	30	nA	$V_{OUT} = V_{DD} = 5.0\text{ V}$
Delay time	t_d	140	200	260	ms	$R_{OSC} = 1\text{ M}\Omega$

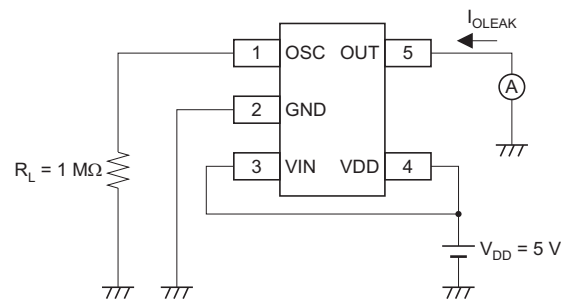
Test Circuit



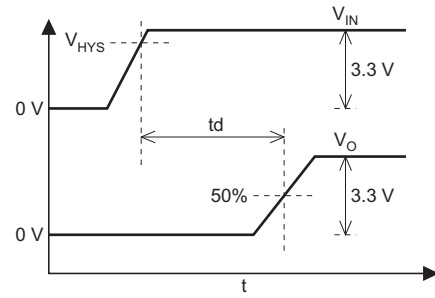
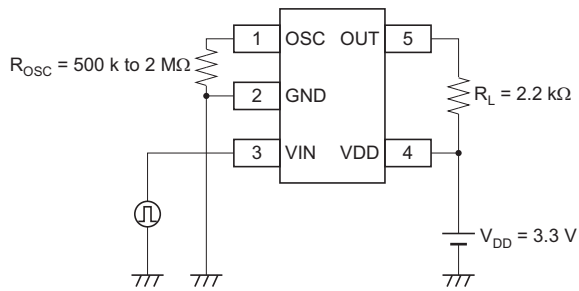
Input Leakage Current I_{in}



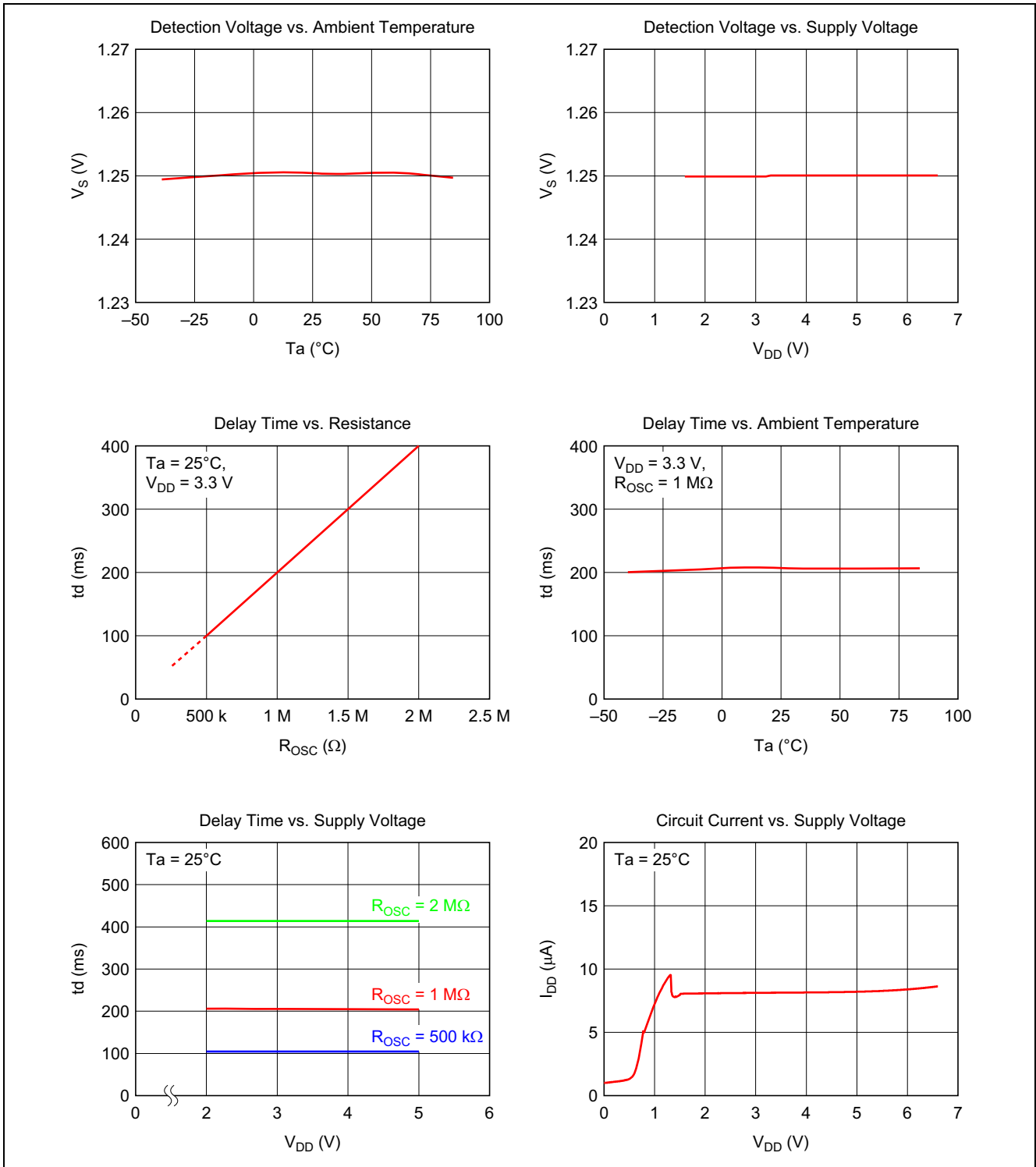
Output Leakage Current I_{LK}

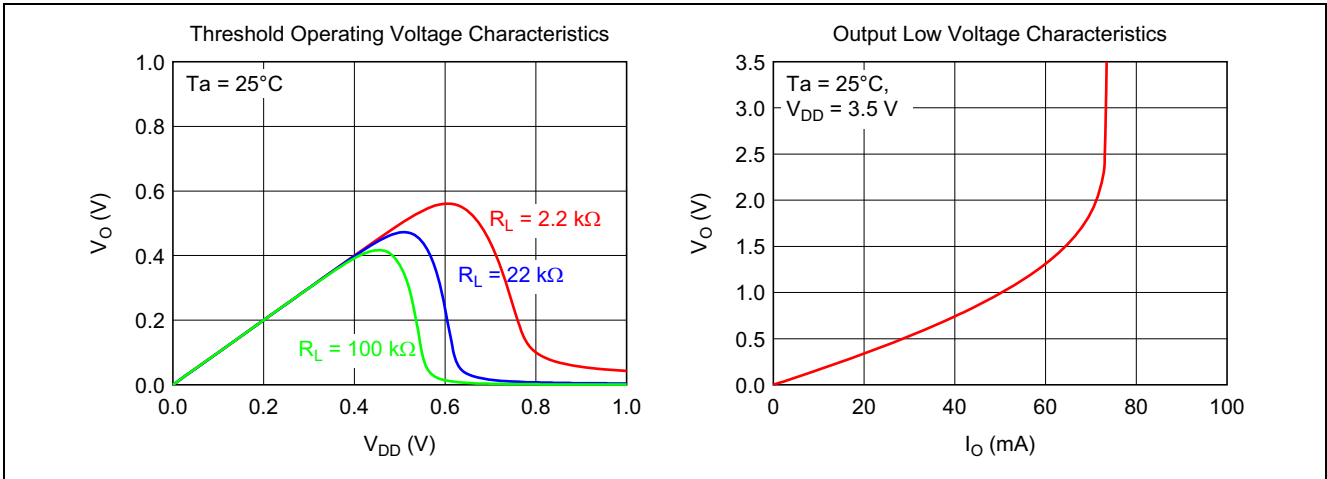


Delay Time t_d

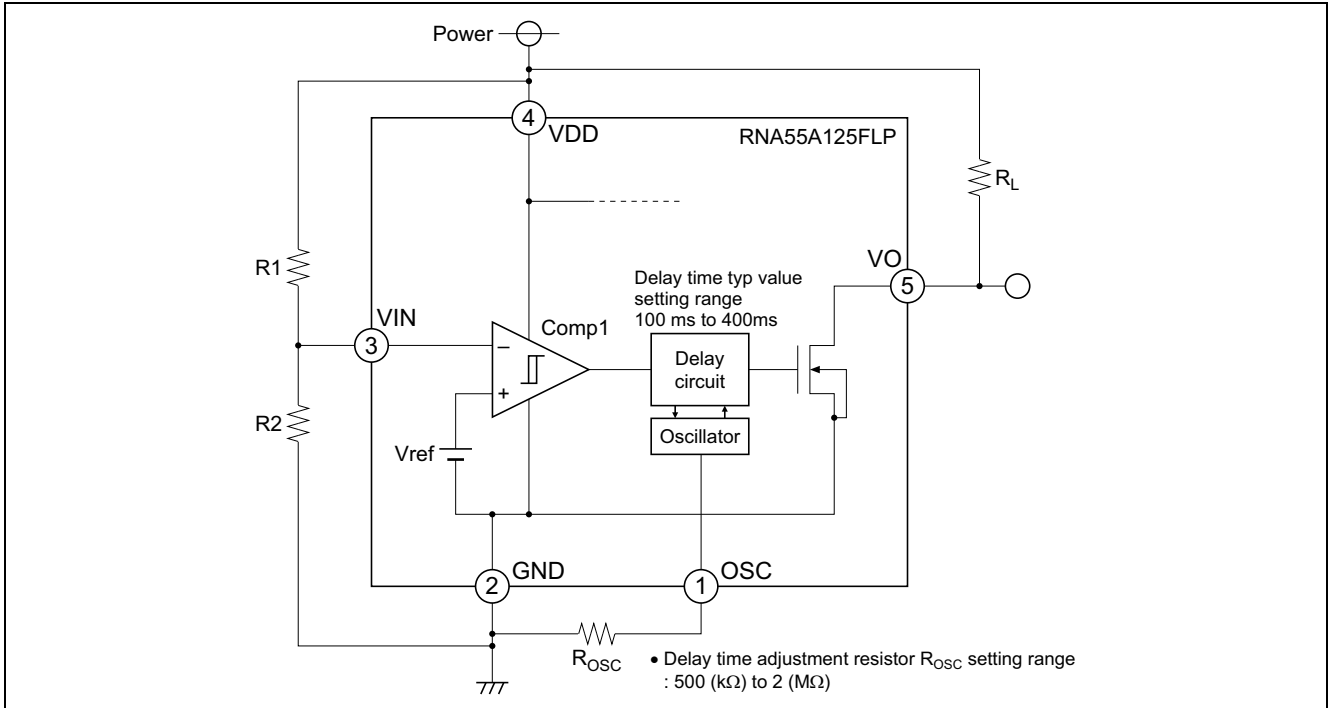


Characteristic Curves





Block Diagram



Detection Voltage Power Setting Information

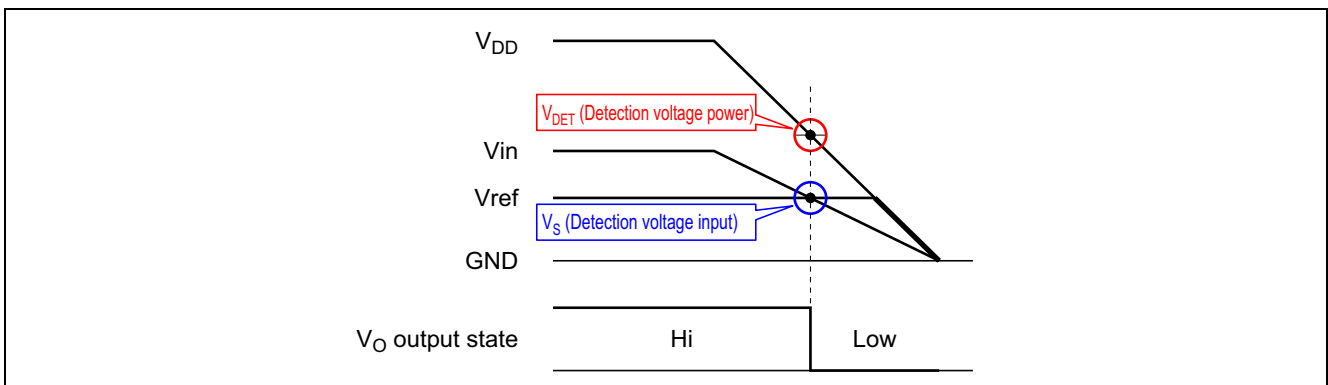
[Way of thinking]

Detection voltage input is V_S

Detection voltage power is V_{DET}

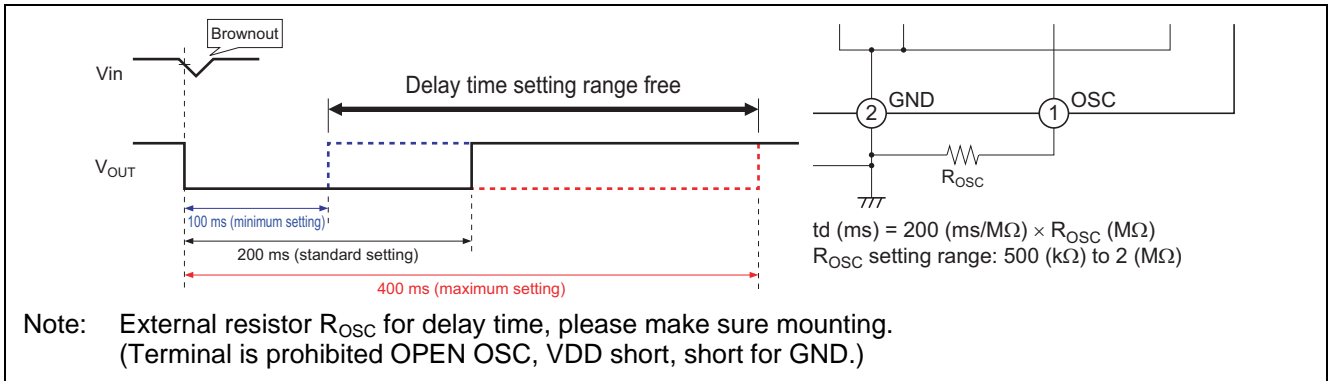
$$V_{DET} = V_S \times (R1 + R2) / R2$$

can write. In addition, $V_S = 1.25$ V it is (typ).



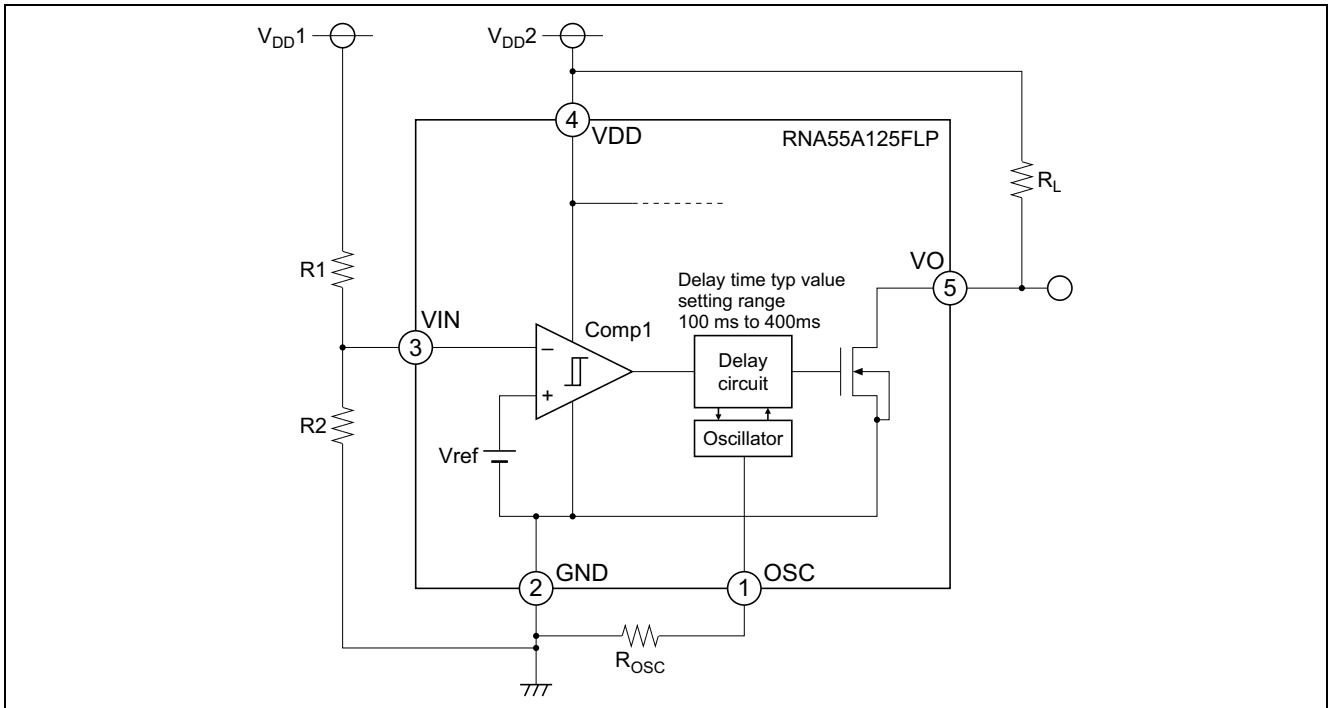
Delay Time Setting Information

Delay circuit of product, using a counter and oscillator scheme, within setting range, it can be set freely using (R_{OSC}) features an external resistor.



Input Terminal Setting

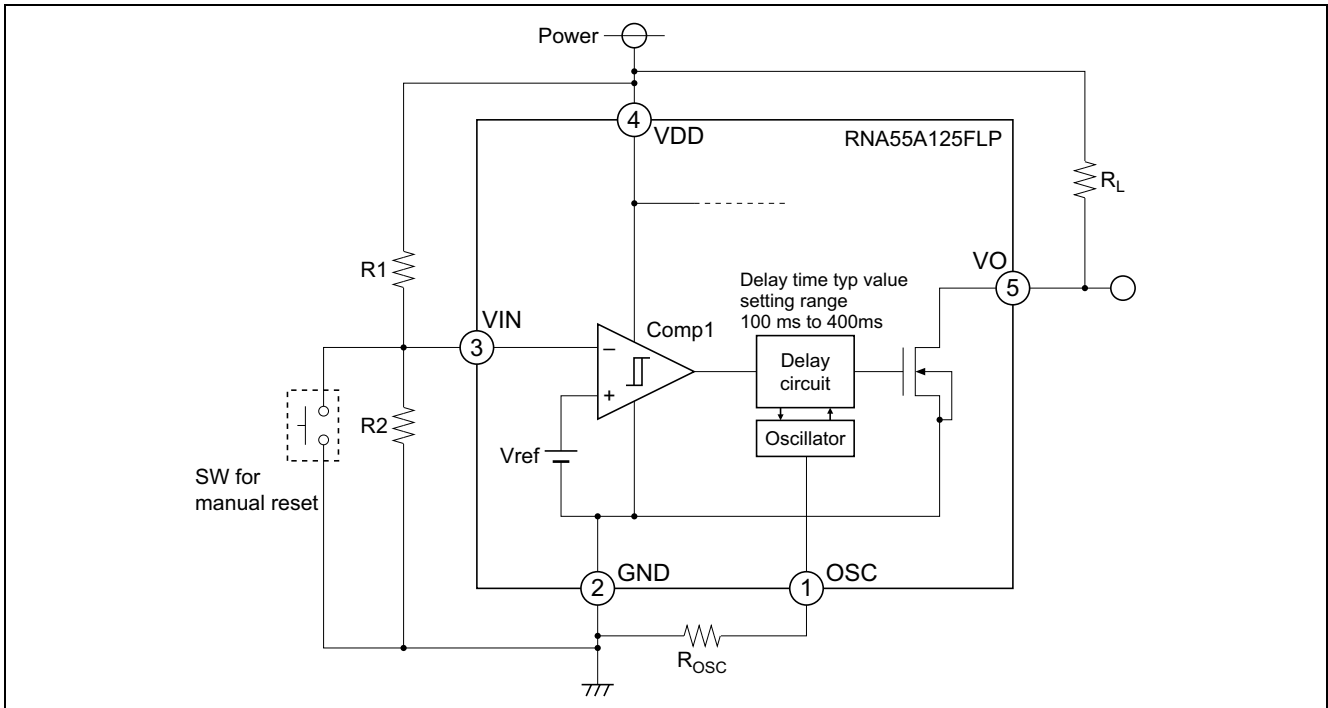
V_{in} is, it is assumed that the input divided voltage of V_{DD} is basically.
It is also possible to use two independent power supply as shown in the figure below.



Use a Manual Reset

Provided that short-circuit path between Vin to GND terminals as shown below, you form manual reset circuit is possible.

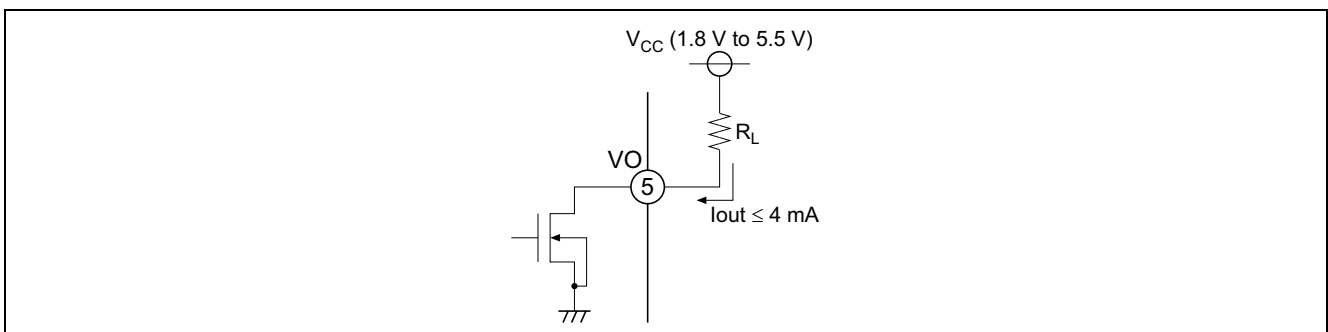
(There is a manual reset by applying a voltage of less than Vref (1.25 V) to terminal Vin.)



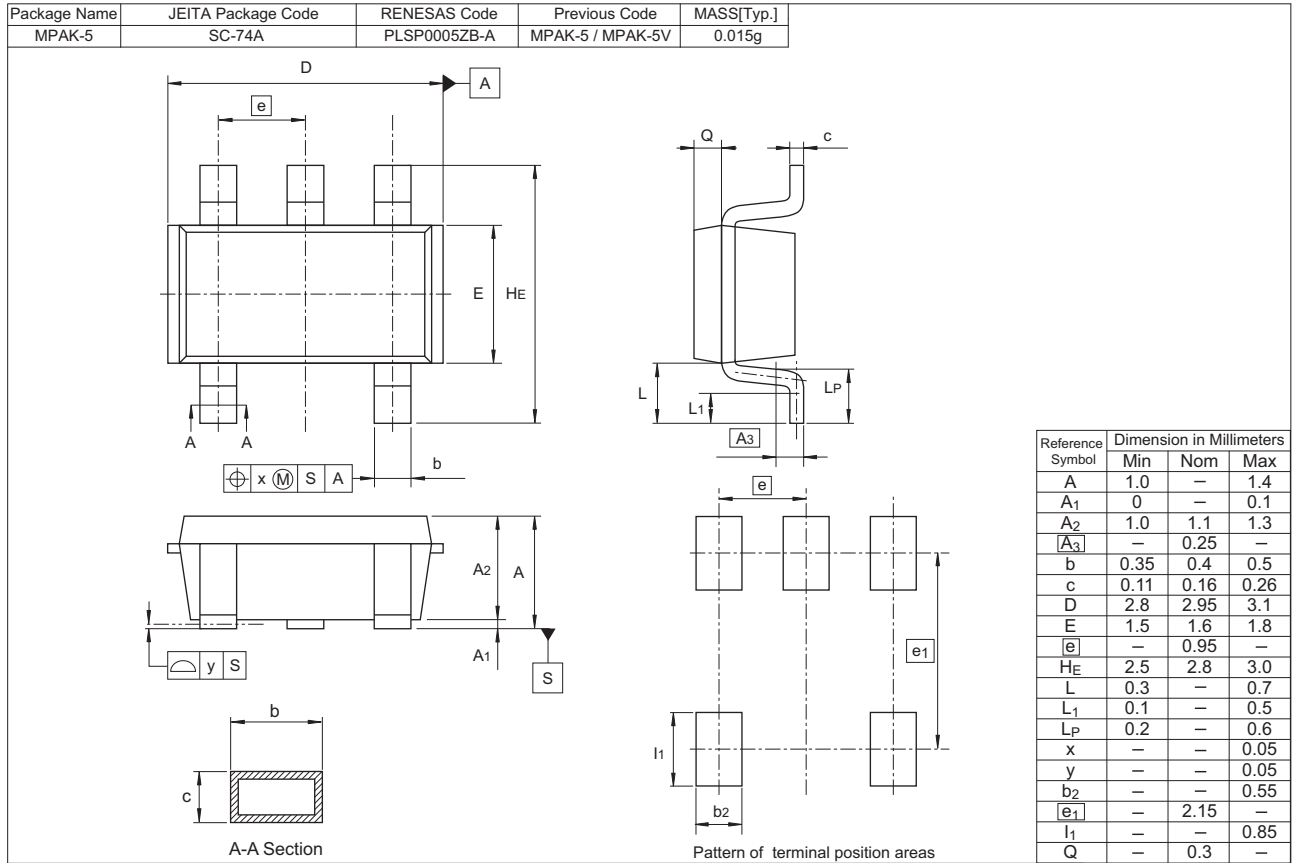
Output Load

Since open-drain type, the output terminal without depending on the power supply voltage although you can set the output voltage level H, please observe the following notes.

- Should be in the range (1.8 V ~ 5.5 V) value of the supply voltage within the recommended range. In addition, the absolute maximum ratings over 6.5 V, so that I do not even for a moment is applied note.
- For the R_L output pull-up resistor, the output current of the L level (I_{out} output inflow current) is set as a guideline wish below 4 mA. Do not exceed the absolute maximum rating (6 mA) even for a moment also to set.



Package Dimensions



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Renesas Electronics America Inc.
2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A.
Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited
1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada
Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: +44-1628-651-700, Fax: +44-1628-651-804

Renesas Electronics Europe GmbH
Arcadiastrasse 10, 40472 Düsseldorf, Germany
Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 Zhichunlu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-8235-1155, Fax: +86-10-8235-7679

Renesas Electronics (Shanghai) Co., Ltd.
Unit 204, 205, AZIA Center, No.1233 Lujiazui Ring Rd., Pudong District, Shanghai 200120, China
Tel: +86-21-5877-1818, Fax: +86-21-6887-7858 / -7898

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 16/F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2886-9318, Fax: +852 2886-9022/9044

Renesas Electronics Taiwan Co., Ltd.
13F, No. 363, Fu Shing North Road, Taipei, Taiwan
Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd.
80 Bendemeer Road, Unit #06-02 Hyflux Innovation Centre Singapore 339949
Tel: +65-6213-0200, Fax: +65-6213-0300

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-3390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd.
11F., Samik Laviel' or Bldg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea
Tel: +82-2-558-3737, Fax: +82-2-558-5141