To our customers,

Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

MOS FIELD EFFECT TRANSISTOR

μPA1856

P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

DESCRIPTION

The μPA1856 is a switching device which can be driven directly by a 2.5-V power source.

The μPA1856 features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

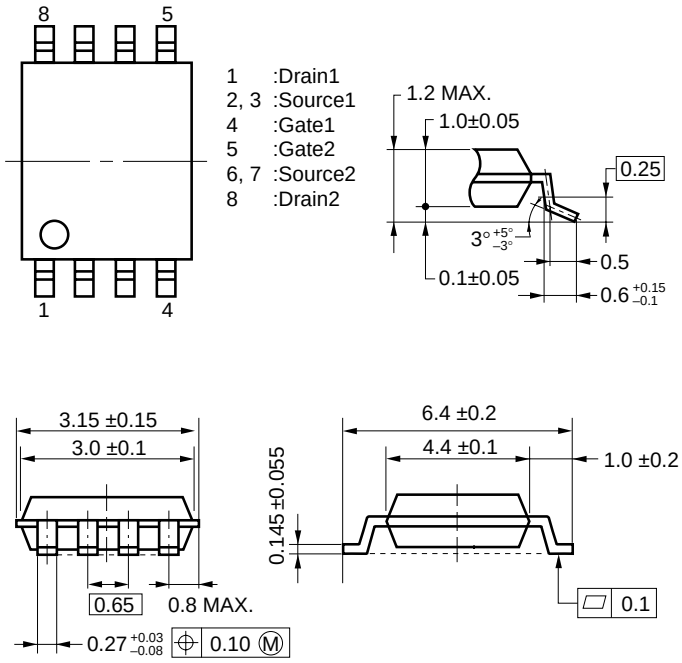
FEATURES

- Can be driven by a 2.5-V power source
- Low on-state resistance
  $R_{DS(on)1}$ = 45 mΩ MAX. (V_{GS} = –4.5 V, I_D = –2.5 A)
  $R_{DS(on)2}$ = 48 mΩ MAX. (V_{GS} = –4.0 V, I_D = –2.5 A)
  $R_{DS(on)3}$ = 72 mΩ MAX. (V_{GS} = –2.7 V, I_D = –2.5 A)
  $R_{DS(on)4}$ = 77 mΩ MAX. (V_{GS} = –2.5 V, I_D = –2.5 A)

ORDERING INFORMATION

PART NUMBER	PACKAGE
μPA1856GR-9JG	Power TSSOP8

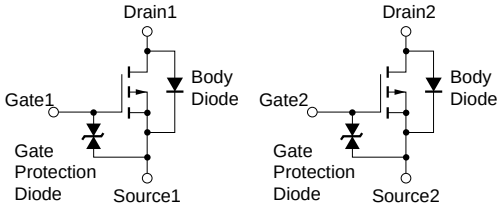
PACKAGE DRAWING (Unit: mm)



ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

Drain to Source Voltage	V_{DSS}	–20	V
Gate to Source Voltage	V_{GSS}	∓12	V
Drain Current (DC)	$I_{D(DC)}$	∓4.5	A
Drain Current (pulse) Note1	$I_{D(pulse)}$	∓18	A
Total Power Dissipation Note2	P_T	2.0	W
Channel Temperature	T_{ch}	150	°C
Storage Temperature	T_{stg}	–55 to +150	°C

EQUIVALENT CIRCUIT



Notes 1. PW ≤ 10 μs, Duty Cycle ≤ 1%

2. Mounted on ceramic substrate of 5000 mm^2 x 1.1 mm

Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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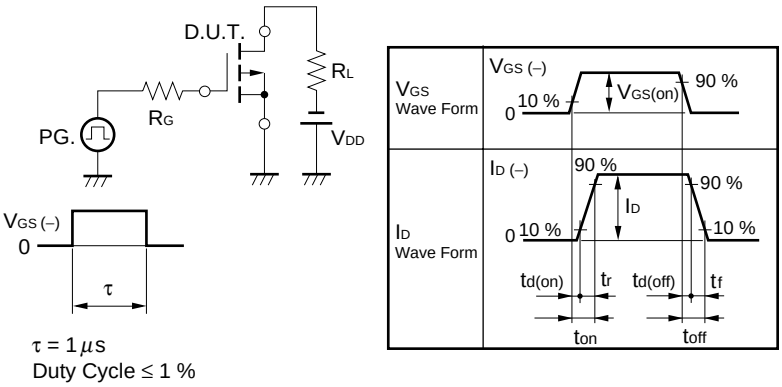
Date Published May 2001 NS CP(K)
Printed in Japan

The mark ★ shows major revised points.

ELECTRICAL CHARACTERISTICS (T_A = 25°C)

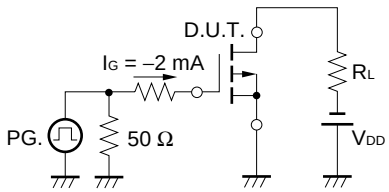
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS} = –20 V, V_{GS} = 0 V			–10	μA
Gate Leakage Current	I_{GSS}	V_{GS} = ±12 V, V_{DS} = 0 V			∓10	μA
Gate Cut-off Voltage	$V_{GS(off)}$	V_{DS} = –10 V, I_D = –1 mA	–0.5	–1.1	–1.5	V
Forward Transfer Admittance	$\|y_{fs}\|$	V_{DS} = –10 V, I_D = –2.5 A	3	8.8		S
Drain to Source On-state Resistance	$R_{DS(on)1}$	V_{GS} = –4.5 V, I_D = –2.5 A		37	45	mΩ
	$R_{DS(on)2}$	V_{GS} = –4.0 V, I_D = –2.5 A		39	48	mΩ
	$R_{DS(on)3}$	V_{GS} = –2.7 V, I_D = –2.5 A		52	72	mΩ
	$R_{DS(on)4}$	V_{GS} = –2.5 V, I_D = –2.5 A		57	77	mΩ
Input Capacitance	C_{iss}	V_{DS} = –10 V		700		pF
Output Capacitance	C_{oss}	V_{GS} = 0 V		208		pF
Reverse Transfer Capacitance	C_{rss}	f = 1 MHz		100		pF
Turn-on Delay Time	$t_{d(on)}$	V_{DD} = –10 V		300		ns
Rise Time	t_r	I_D = –2.5 A		528		ns
Turn-off Delay Time	$t_{d(off)}$	V_{GS} = –4.0 V		242		ns
Fall Time	t_f	R_G = 10 Ω		698		ns
Total Gate Charge	Q_G	V_{DS} = –16 V		6.0		nC
Gate to Source Charge	Q_{GS}	I_D = –4.5 A		2.1		nC
Gate to Drain Charge	Q_{GD}	V_{GS} = –4.0 V		2.8		nC
Diode Forward Voltage	$V_{F(S-D)}$	I_F = 4.5 A, V_{GS} = 0 V		0.86		V
Reverse Recovery Time	t_{rr}	I_F = 4.5 A, V_{GS} = 0 V		32		ns
Reverse Recovery Charge	Q_{rr}	di/dt = 10 A/μs		2.2		nC

TEST CIRCUIT 1 SWITCHING TIME



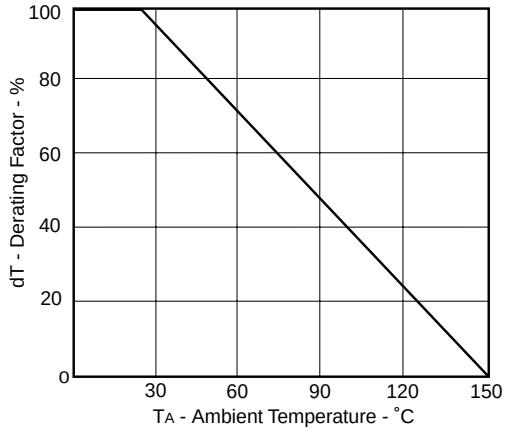
τ = 1 μs
Duty Cycle ≤ 1 %

TEST CIRCUIT 2 GATE CHARGE

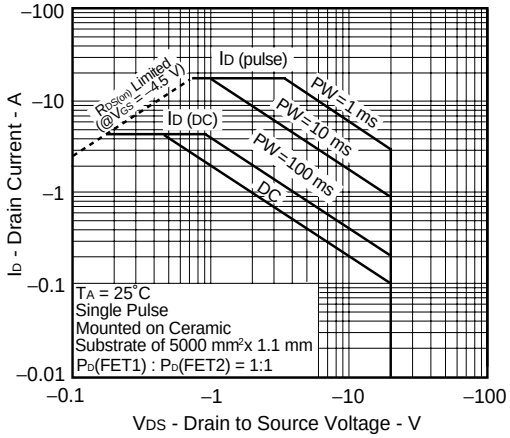


TYPICAL CHARACTERISTICS (T_A = 25°C)

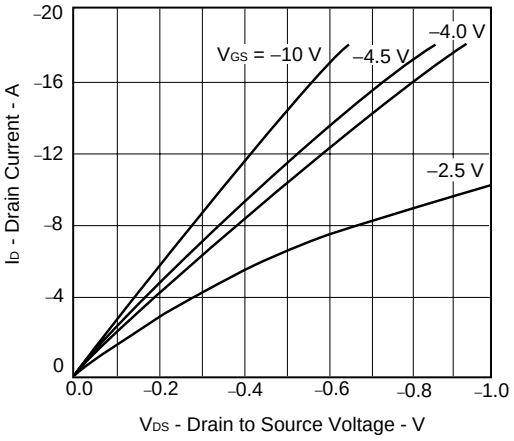
DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



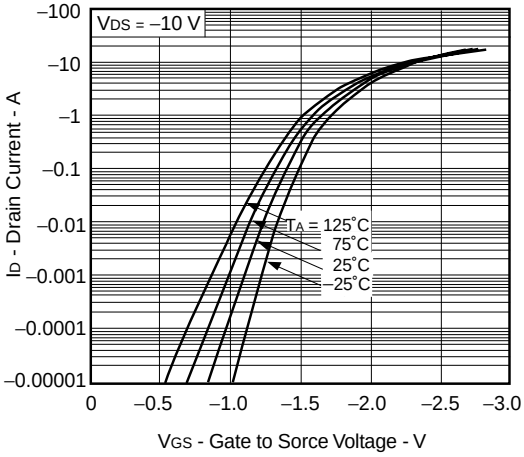
★ FORWARD BIAS SAFE OPERATING AREA



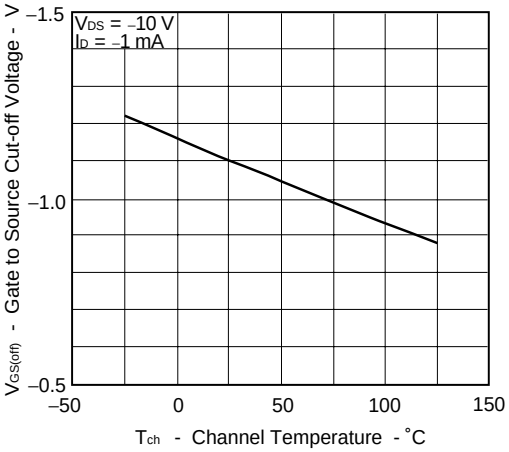
DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



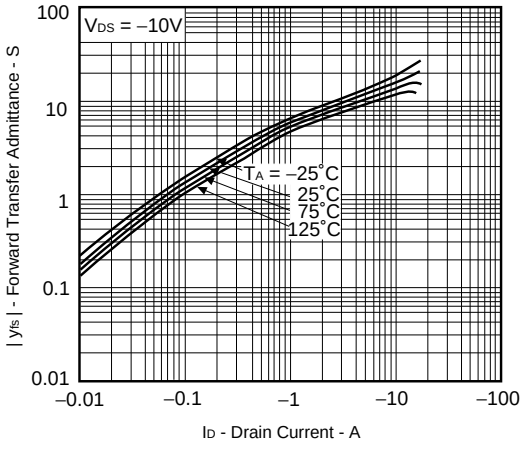
TRANSFER CHARACTERISTICS



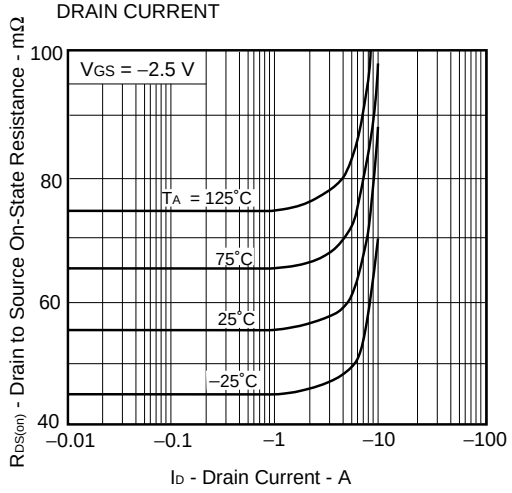
GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



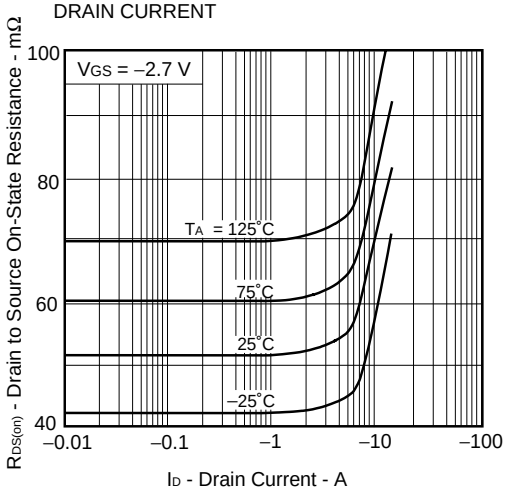
FORWARD TRANSFER ADMITTANCE Vs. DRAIN CURRENT



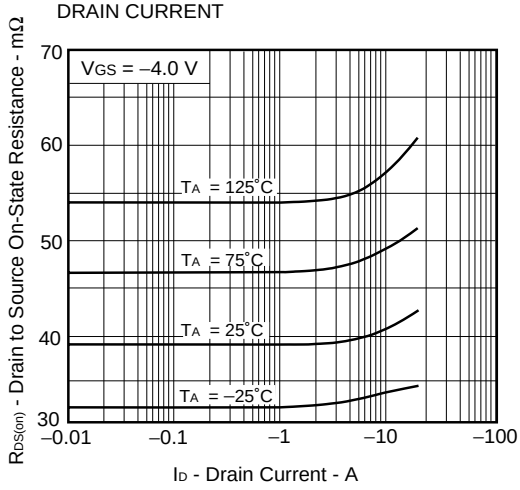
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



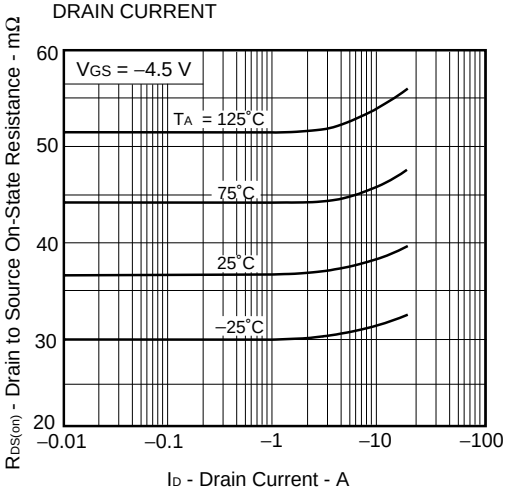
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



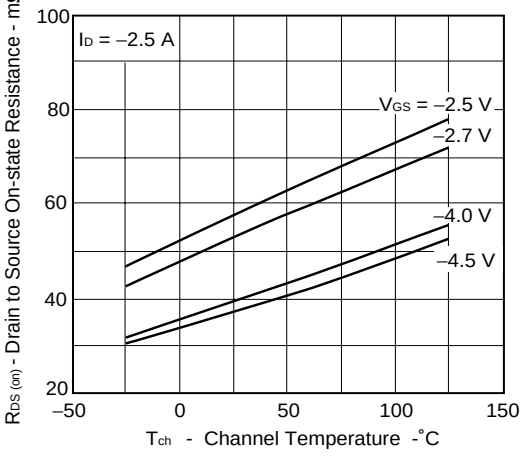
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



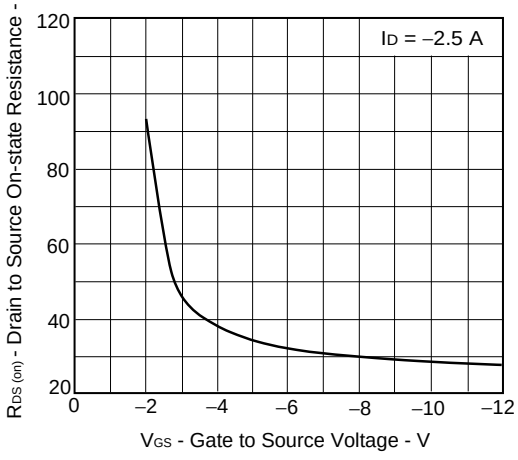
DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



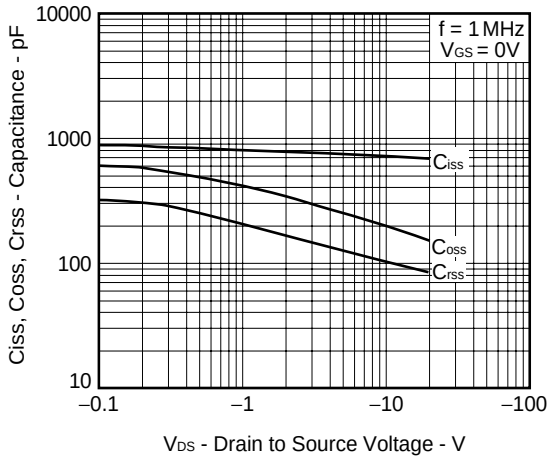
DRAIN TO SOURCE ON STATE RESISTANCE vs. CHANNEL TEMPERATURE



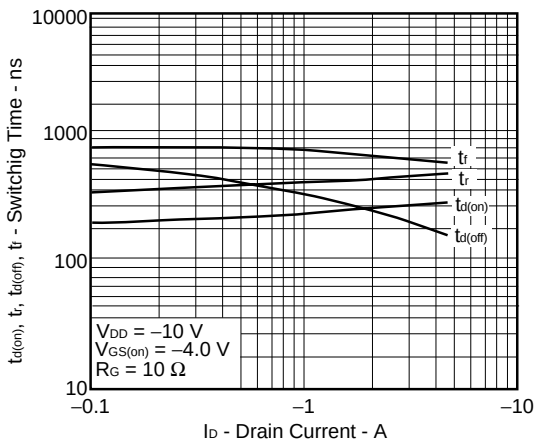
DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



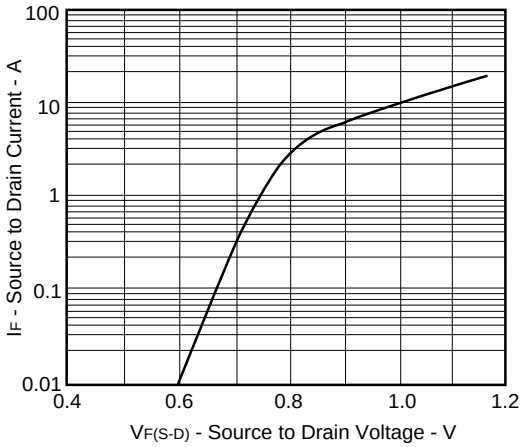
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



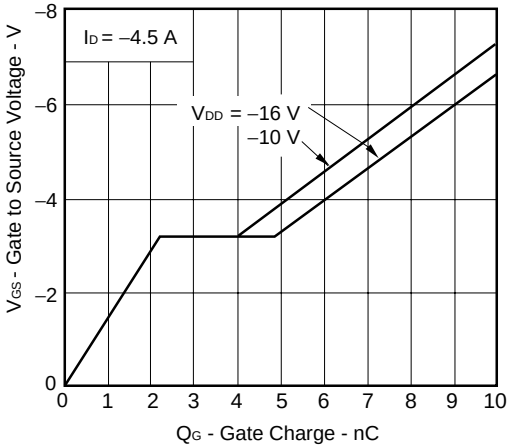
SWITCHING CHARACTERISTICS



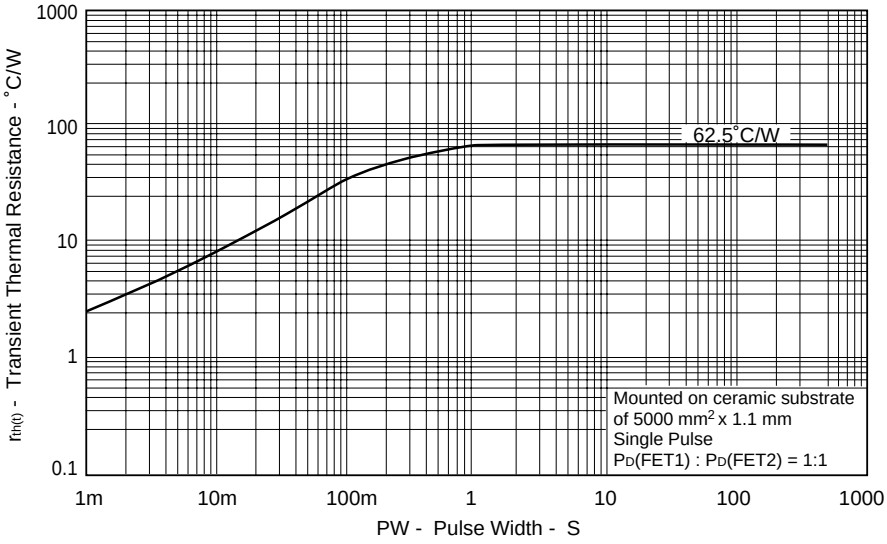
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



DYNAMIC INPUT CHARACTERISTICS



★ TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



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