

# $\mu$ PA2373T1P

Dual (Drain common), N-channel MOSFET

24V, 6A, 23.0mΩ

R07DS0674EJ0101 Rev.1.01 Aug 19, 2013

### **DESCRIPTION**

The  $\mu$ PA2373T1P is a switching device, which can be driven directly by a 2.5 V power source.

The  $\mu$ PA2373T1P features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on. Best suite for single cell LiB application.

#### **FEATURES**

- 2.5 V drive available
- Low on-state resistance
  - $R_{SS(on)1} = 23.0 \text{ m}\Omega$  MAX. ( $V_{GS} = 4.5 \text{ V}$ ,  $I_S = 3.0 \text{ A}$ )
    - $R_{SS(on)2}$  = 24.0 m $\Omega$  MAX. ( $V_{GS}$  = 4.0 V,  $I_S$  = 3.0 A)
    - $R_{SS(on)3}$  = 25.0 m $\Omega$  MAX. ( $V_{GS}$  = 3.8 V,  $I_S$  = 3.0 A)
    - ---  $R_{SS(on)4} = 30.0 \text{ m}\Omega \text{ MAX.}$  (V<sub>GS</sub> = 3.1 V, I<sub>S</sub> = 3.0 A)
    - ---  $R_{SS(on)4}$  = 39.0 mΩ MAX. ( $V_{GS}$  = 2.5 V,  $I_S$  = 3.0 A)
- Built-in G-S protection diode against ESD

#### ORDERING INFORMATION

Part No.	Lead Plating	Packing	Package
μPA2373T1P-E4-A*1	Ni/Au	Reel 5000 p/reel	4-pin EFLIP-LGA

Note: \*1. Pb-free (This product does not contain Pb in the external electrode and other parts.)

## ABSOLUTE MAXIMUM RATINGS $(T_A = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Source to Source Voltage (V <sub>GS</sub> = 0 V)	V <sub>SSS</sub>	24.0	V
Gate to Source Voltage (V <sub>DS</sub> = 0 V)	$V_{GSS}$	±12.0	V
Source Current (DC) *1	I <sub>S(DC)</sub>	±6.0	А
Source Current (pulse) *2	I <sub>S(pulse)</sub>	±60	А
Total Power Dissipation (2 units) *1	P <sub>T1</sub>	1.3	W
Channel Temperature	T <sub>ch</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

Note: \*1. Mounted on ceramic board of 50 cm<sup>2</sup> ×1.0 mmt

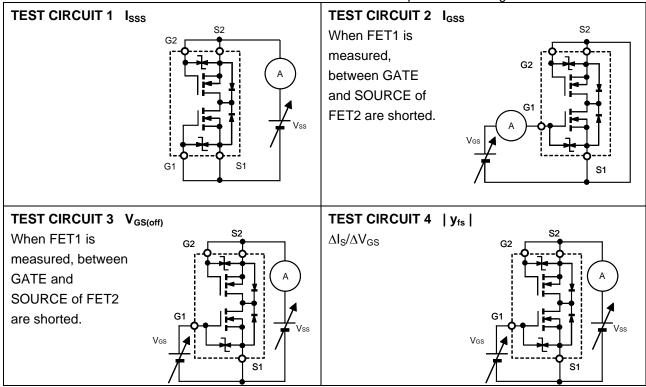
\*2. PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

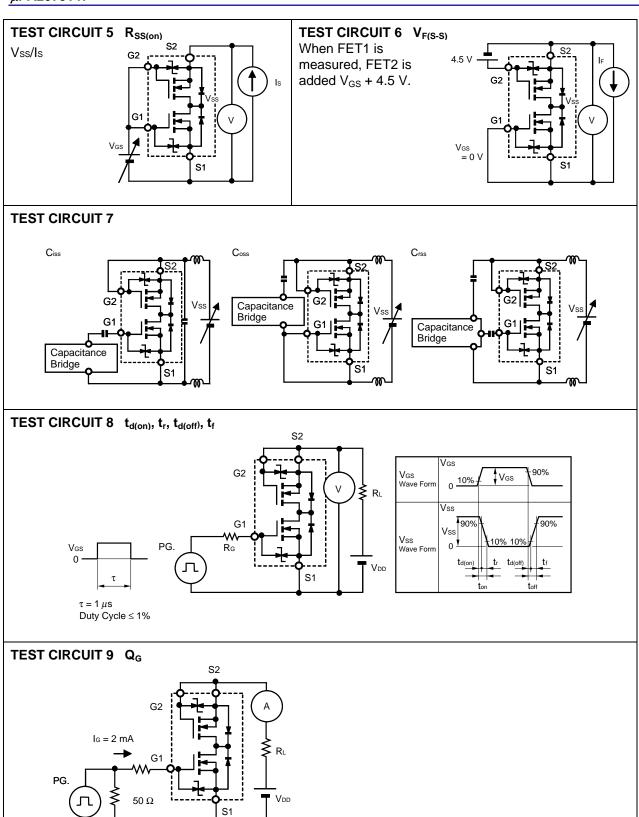
## ELECTRICAL CHARACTERISTICS ( $T_A = 25$ °C)

Characteristics	Symbol	MIN.	TYP.	MAX.	Unit	Test Conditions
Zero Gate Voltage Source Current	I <sub>SSS</sub>			1	μΑ	V <sub>SS</sub> = 24 V, V <sub>GS</sub> = 0 V, TEST CIRCUIT 1
Gate Leakage Current	I <sub>GSS</sub>			±10	μΑ	$V_{GS} = \pm 12 \text{ V}, V_{SS} = 0 \text{ V}, \text{TEST CIRCUIT 2}$
Gate to Source Cut-off Voltage	$V_{GS(off)}$	0.5	0.9	1.5	V	$V_{SS} = 10 \text{ V}, I_S = 1.0 \text{ mA}, TEST CIRCUIT 3}$
Forward Transfer Admittance *1	y <sub>fs</sub>	3.5			S	V <sub>SS</sub> = 5 V, I <sub>S</sub> = 1.5 A, TEST CIRCUIT 4
Source to Source On-state	R <sub>SS(on)1</sub>	14	19	23	mΩ	$V_{GS} = 4.5 \text{ V}, I_S = 3.0 \text{ A}, \text{TEST CIRCUIT 5}$
Resistance *1	R <sub>SS(on)2</sub>	14.4	20	24	mΩ	$V_{GS} = 4.0 \text{ V}, I_{S} = 3.0 \text{ A}, \text{TEST CIRCUIT 5}$
	R <sub>SS(on)3</sub>	14.6	20.5	25	mΩ	$V_{GS} = 3.8 \text{ V}, I_S = 3.0 \text{ A}, \text{TEST CIRCUIT 5}$
	R <sub>SS(on)4</sub>	16	23	30	mΩ	$V_{GS} = 3.1 \text{ V}, I_S = 3.0 \text{ A}, \text{TEST CIRCUIT 5}$
	R <sub>SS(on)5</sub>	19	27	39	mΩ	$V_{GS} = 2.5 \text{ V}, I_S = 3.0 \text{ A}, \text{TEST CIRCUIT 5}$
Input Capacitance	C <sub>iss</sub>		1230		pF	V <sub>SS</sub> = 10 V,
Output Capacitance	Coss		370		pF	$V_{GS} = 0 V$ ,
Reverse Transfer Capacitance	C <sub>rss</sub>		282		pF	f = 1.0 MHz, TEST CIRCUIT 7
Turn-on Delay Time	t <sub>d(on)</sub>		6.2		μS	$V_{DD} = 20 \text{ V}, I_{S} = 6.0 \text{ A},$
Rise Time	t <sub>r</sub>		36		μS	$V_{GS} = 4.0 \text{ V},$
Turn-off Delay Time	t <sub>d(off)</sub>		37		μS	$R_G = 6.0 \Omega$ ,
Fall Time	t <sub>f</sub>		61		μS	TEST CIRCUIT 8
Total Gate Charge	$Q_G$		22		nC	$V_{DD} = 19.2 \text{ V}, V_{G1S1} = 4.0 \text{ V}, I_S = 6.0 \text{ A},$ TEST CIRCUIT 9
Body Diode Forward Voltage *1	$V_{F(S-S)}$		0.9		V	I <sub>F</sub> = 6.0 A, V <sub>GS</sub> = 0 V, TEST CIRCUIT 6

Note: \*1. Pulsed test

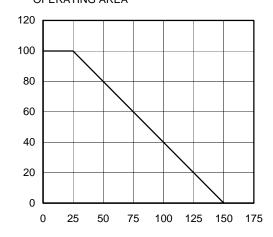
Both the FET1 and the FET2 are measured. Test circuits are example of measuring the FET1 side.





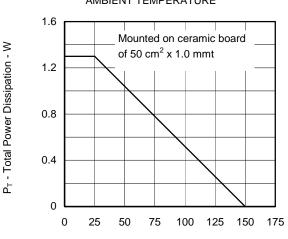
- Percentage of Rated Power - %

## DERATING FACTOR OF FORWARD BIAS SAFE OPERATING AREA



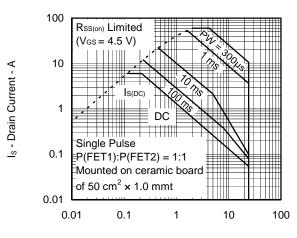
#### T<sub>A</sub> - Ambient Temperature - °C

## TOTAL POWER DISSIPATION vs. AMBIENT TEMPERATURE



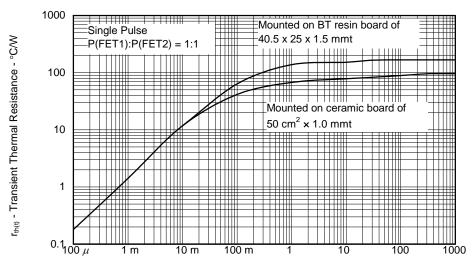
 $T_{\text{A}}$  - Ambient Temperature -  $^{\circ}\text{C}$ 

#### FORWARD BIAS SAFE OPERATING AREA



 $V_{\text{SS}}$  - Drain to Source Voltage - V

#### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH



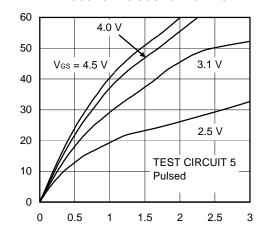
PW - Pulse Width - s

Is - Source Current - A

V<sub>GS(off)</sub> - Gate to Source Cut-off Voltage - V

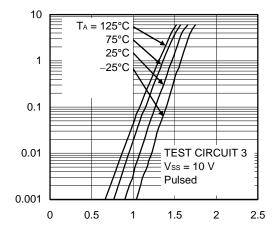
R<sub>SS(on)</sub> - Source to Source On-state Resistance - mΩ

## SOURCE CURRENT vs. SOURCE TO SOURCE VOLTAGE



V<sub>SS</sub> - Source to Source Voltage - V

## FORWARD TRANSFER CHARACTERISTICS

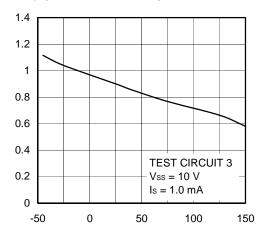


Is - Source Current - A

 $y_{fs} \mid$  - Forward Transfer Admittance - S

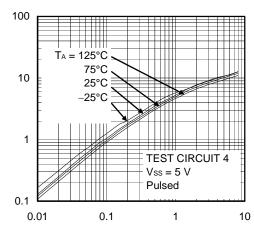
V<sub>GS</sub> - Gate to Source Voltage - V

# GATE TO SOURCE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



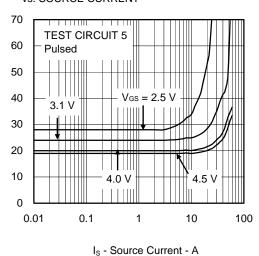
T<sub>ch</sub> - Channel Temperature - °C

## FORWARD TRANSFER ADMITTANCE vs. SOURCE CURRENT

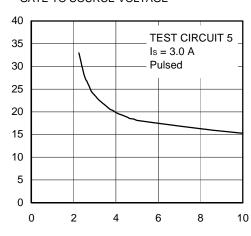


Is - Source Current - A

# SOURCE TO SOURCE ON-STATE RESISTANCE vs. SOURCE CURRENT



# SOURCE TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



V<sub>GS</sub> - Gate to Source Voltage - V

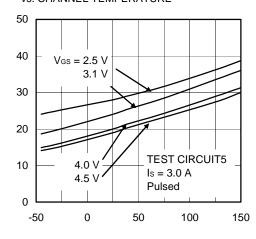
 $R_{SS(on)}$  - Source to Source On-state Resistance -  $m\Omega$ 

R<sub>SS(on)</sub> - Source to Source On-state Resistance - mΩ

 $t_{d(on)}, t_r, t_{d(off)}, t_f$  - Switching Time -  $\mu$  s

IF - Diode Forward Current - A

## SOURCE TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



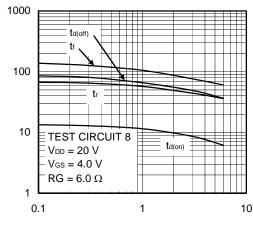
T<sub>ch</sub> - Channel Temperature - °C

# 10000 Ld. 90 1000 Ciss Coss TEST CIRCUIT 7 VGs = 0 V f = 1.0 MHz 100 0.1 1 10 100

CAPACITANCE vs. SOURCE TO SOURCE VOLTAGE

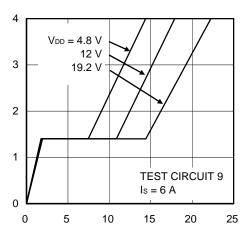
V<sub>SS</sub> - Source to Source Voltage - V

#### SWITCHING CHARACTERISTICS



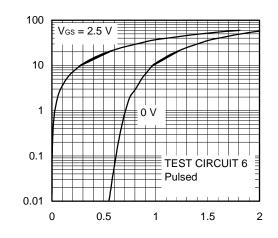
Is - Source Current - A

### DYNAMIC INPUT CHARACTERISTICS



Q<sub>G</sub> - Gate Charge - nC

## SOURCE TO SOURCE DIODE FORWARD VOLTAGE

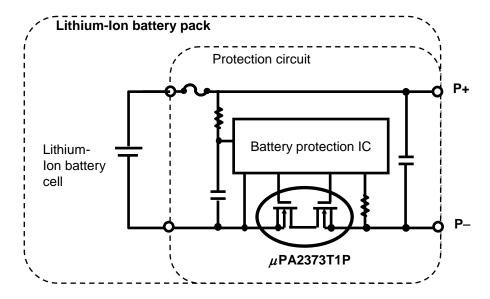


 $V_{F(S\text{-}S)}$  - Source to Source Voltage - V

V<sub>GS</sub> - Gate to Source Voltage - V

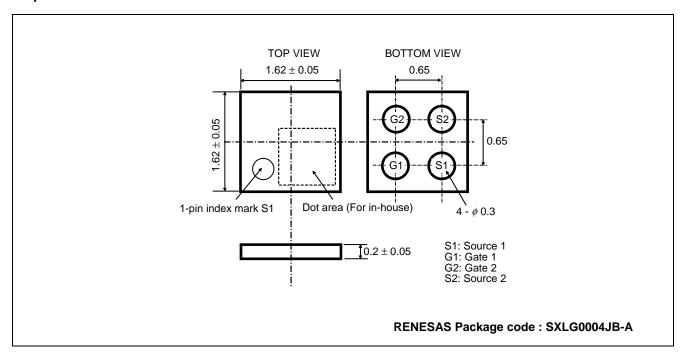
## Example of application circuit

## LI-ion battery (1 cell) protection circuit

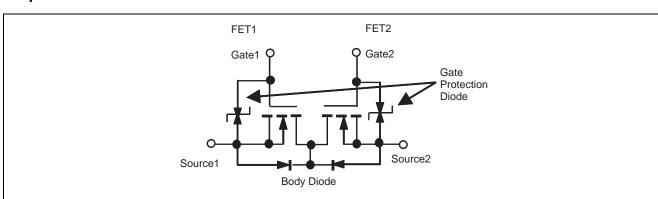


## PACKAGE DRAWINGS (UNIT: mm)

## 4-pin EFLIP-LGA



## **Equivalent Circuit**



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

## **USAGE CAUTIONS**

When you use this device, in order to prevent a customer's hazard and damage, use it with understanding the following contents. If used exceeding recommended conditions, there is a possibility of causing the device and characteristic degradation.

- 1. This device is very thin device and should be handled with caution for mechanical stress. The distortion applied to the device should become below 2000 × 10<sup>-6</sup>. If the distortion exceeds 2000 × 10<sup>-6</sup>, the characteristic of a device may be degraded and it may result in failure.
- 2. Please do not damage the device when you handle it. The use of metallic tweezers has the possibility of giving the wound. Mounting with the nozzle with clean point is recommended.
- 3. When you mount the device on a substrate, carry out within our recommended soldering conditions of infrared reflow. If mounted exceeding the conditions, the characteristic of a device may be degraded and it may result failure.
- 4. When you wash the device mounted the board, carry out within our recommended conditions. If washed exceeding the conditions, the characteristic of a device may be degraded and it may result in failure.
- 5. When you use ultrasonic wave to substrate after the device mounting, prevent from touching a resonance directly. If it touches, the characteristic of a device may be degraded and it may result in failure.
- 6. Only the epoxy resin of the semiconductor grade is recommended as coating material.
- 7. Please refer to Figure 2 as an example of the Mounting Pad. Optimize the land pattern in consideration of density, appearance of solder fillets, common difference, etc in an actual design.
- 8. The marking side of this device is an internal electrode. Please neither contact with terminals of other parts nor take out the electrode.

Figure 1 Recommended soldering conditions of INFRARED REFLOW

Maximum temperature (Package's surface temperature)

Time at maximum temperature

Time of temperature higher than 220°C

Preheating time at 160 to 180°C

Maximum number of reflow processes

Maximum chlorine content of rosin flux (Mass percentage)

: 260°C or below
: 10 s or less
: 60 s or less
: 60 to 120 s
: 3 times
: 0.2% or less

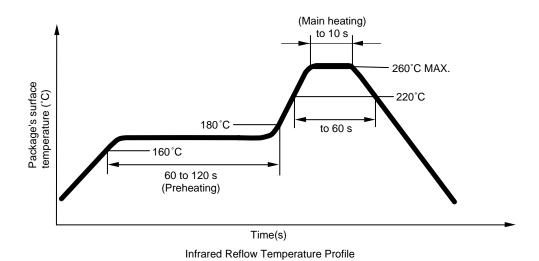


Figure 2 The example of the Mounting Pad (Unit : mm)

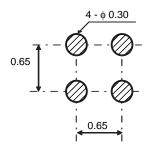
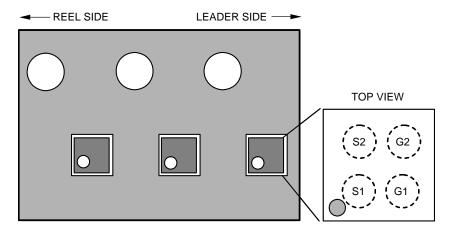


Figure 3 The unit orientation



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