

UPC177GR-9LG, UPC339GR-9LG

Single Power Supply Quad Comparator

DESCRIPTION

UPC177GR-9LG, UPC339GR-9LG comparators are designed for single power supply operation. The features include low-voltage operation, common-mode input voltage range from V^- (GND) level, open-collector output and low current consumption. Furthermore, these products can operate with both power supplies and can be widely used for various voltage comparison application.

Depending on the usage and operating ambient temperature range, the UPC177GR-9LG is design for extended temperature and UPC339GR-9LG is design for general purposes.

Along with this series of lineup are dual type comparators, UPC277GR-9LG, UPC277MP-KAA, and UPC393GR-9LG with the same circuit configuration are also available.

FEATURES

- Input Offset Voltage ±1 mV (TYP.)
- Input Bias Current 20 nA (TYP.)
- Voltage Gain 200000 (TYP.)
- Pulse Response Time 1.6 μs (TYP.)
- Output Sink Current 16 mA (TYP.)
- A wired OR is possible as the output is an open collector.
- Low Voltage Operation $V^+ - V^- : +2 \sim +32 \text{ V}$
- Small Package (Half the footprint size of 14-pin plastic SOP (1.27 mm pitch))

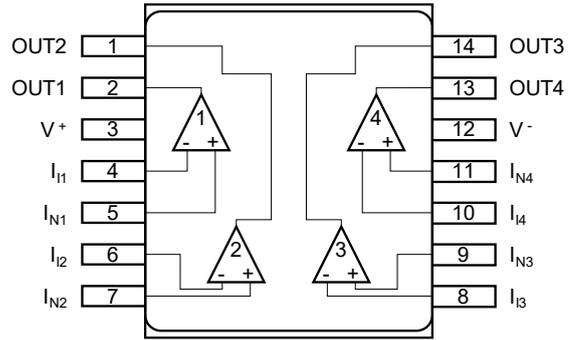
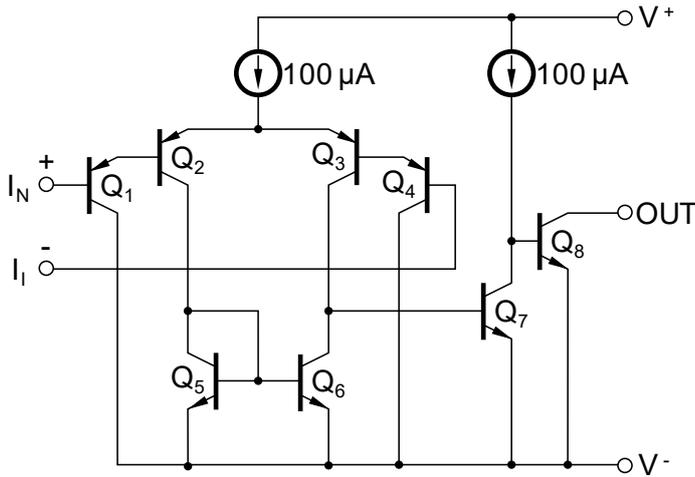
ORDERING INFORMATION

Order Name ⁽¹⁾	Selected Grade	Package
UPC177GR-9LG-A	Standard	14-pin plastic TSSOP (5.72 mm (225))
UPC339GR-9LG-A	Standard	14-pin plastic TSSOP (5.72 mm (225))

- (1) Order names containing E1 or E2 indicate that the packaging format is embossed taping.
Pin 1 of E1 is on draw-out side, and pin 1 of E2 is at take-up side.

EQUIVALENT CIRCUIT (1/4 Circuit)

PIN CONFIGURATION (Marking side)



ABSOLUTE MAXIMUM RATINGS (T_A = 25 °C)

Parameter	Symbol	UPC177GR-9LG	UPC339GR-9LG	Unit
Power Supply Voltage ^{Note 1}	V ⁺ - V ⁻	-0.3 ~ +36		V
Differential Input Voltage	V _{ID}	±36		V
Input Voltage ^{Note 2}	V _I	V ⁻ - 0.3 ~ V ⁻ + 36		V
Output Applied Voltage ^{Note 3}	V _O	V ⁻ - 0.3 ~ V ⁻ + 36		V
Total Power Dissipation ^{Note 4}	P _T	550		mW
Output Short Circuit Duration (vs. GND) ^{Note 5}	t _s	Indefinite		s
Operating Ambient Temperature	T _A	-40 ~ +125	-40 ~ +85	°C
Storage Temperature	T _{stg}	-55 ~ +150	-55 ~ +125	°C

- [Note]**
- Note that reverse connections of the power supply may damage the ICs.
 - This is the input voltage range that can be applied to the input terminal without any characteristics degradation or breakdown. It can be applied regardless of the supply voltage. Do not apply the voltage below V⁻(GND)-0.3V. The comparator electrical characteristics will operate normally when the input voltage is operating within the common-mode input voltage range.
 - Voltage range that can be applied externally to the output terminal without deteriorating or damage to the product. It can be applied regardless of the power supply. Caution not to exceed the ratings, including transient conditions such as when the power supply is ON/OFF.
 - This is the value when mounting the glass epoxy board (size 100 mm x 100 mm, thickness 1 mm, and copper foil only on one side with 15% solid wiring of the board area). Please take note that depending on the operating ambient temperature, each product following conditions and de-rating rate as below:
 UPC177GR-9LG : For T_A > 71 °C, de-rate at -7.0 mW/°C
 (Junction-Ambient Thermal Resistance, R_{th(J-A)} = 144 °C /W)
 UPC339GR-9LG : For T_A > 46 °C, de-rate at -7.0 mW/°C
 (Junction-Ambient Thermal Resistance, R_{th(J-A)} = 144 °C /W)
 - A short circuit to the V⁺ side may destroy the IC. Please use the total loss and de-rating value from Note 4.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Power Supply Voltage (Dual Supply)	V^\pm	± 1		± 16	V
Power Supply Voltage ($V^- = \text{GND}$)	V^+	+2		+32	V

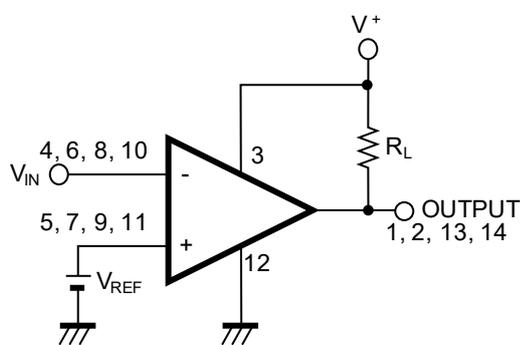
ELECTRICAL CHARACTERISTICS

UPC177GR-9LG, UPC339GR-9LG ($T_A = 25^\circ\text{C}$, $V^+ = +5\text{V}$, $V^- = \text{GND}$)

Parameter	Symbol	MIN.	TYP.	MAX.	Unit	Test Condition
Input Offset Voltage	V_{IO}		± 1	± 5	mV	$V_O = 1.4\text{V}$, $V_{REF} = 1.4\text{V}$, $R_S = 0\ \Omega$
Input Offset Current	I_{IO}		± 5	± 50	nA	$V_O = 1.4\text{V}$
Input Bias Current ^{Note 6}	I_B		20	250	nA	$V_O = 1.4\text{V}$
Large Signal Voltage Gain	A_v		200000			$R_L = 15\text{k}\Omega$
Circuit Current ^{Note 7}	I_{CC}		1.1	2	mA	$R_L = \infty$, $I_O = 0\text{A}$
Common Mode Input Voltage Range	V_{ICM}	0		$V^+ - 1.5$	V	
Output Saturation Voltage	V_{OL}		0.2	0.4	V	$V_{IN(-)} = 1\text{V}$, $V_{IN(+)} = 0\text{V}$, $I_{O\text{SINK}} = 4\text{mA}$
Output Sink Current	$I_{O\text{SINK}}$	6	16		mA	$V_{IN(-)} = 1\text{V}$, $V_{IN(+)} = 0\text{V}$, $V_O \leq 1.5\text{V}$
Output Leakage Current	$I_{O\text{LEAK}}$		0.1		nA	$V_{IN(+)} = 1\text{V}$, $V_{IN(-)} = 0\text{V}$, $V_O = 5\text{V}$
Pulse Response Time ^{Note 8}			1.6		μs	$R_L = 5.1\text{k}\Omega$, $V_{RL} = 5\text{V}$

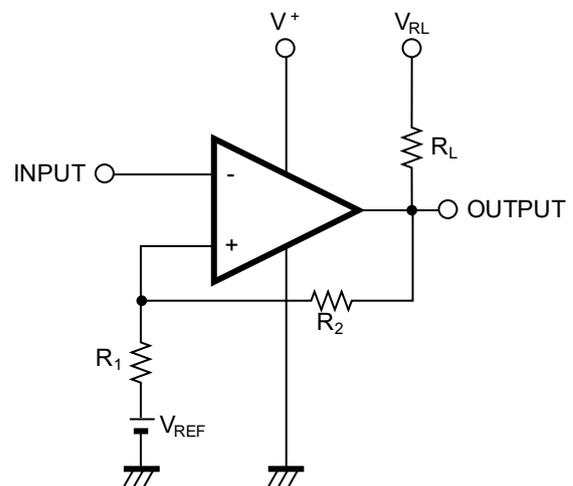
- [Note] 6. Input bias currents flow out from IC. Because each currents are base current of PNP-transistor on input stage.
 7. This current flows irrespective of the existence of use.
 8. Response time can be decreased if the overdrive voltage rebel is up.

TYPICAL APPLICATION CIRCUIT EXAMPLE



$$V_{REF} : V^- \sim V^+ - 1.5 [\text{V}]$$

Comparator with Hysteresis



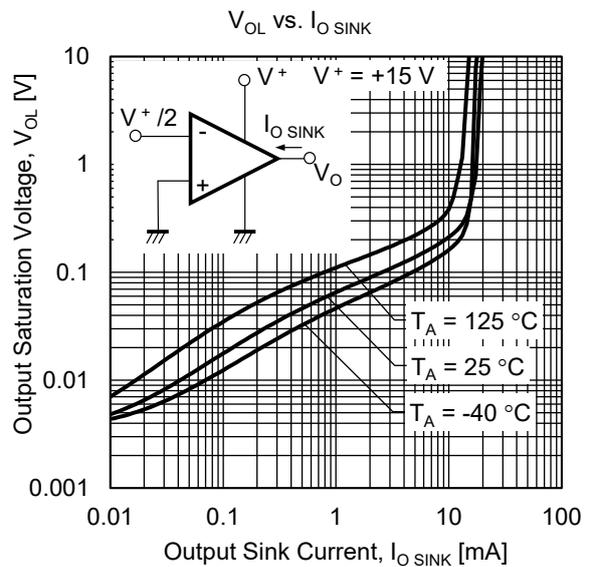
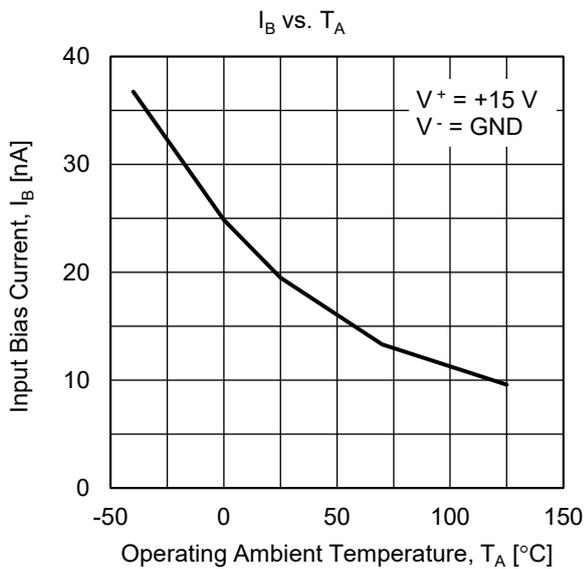
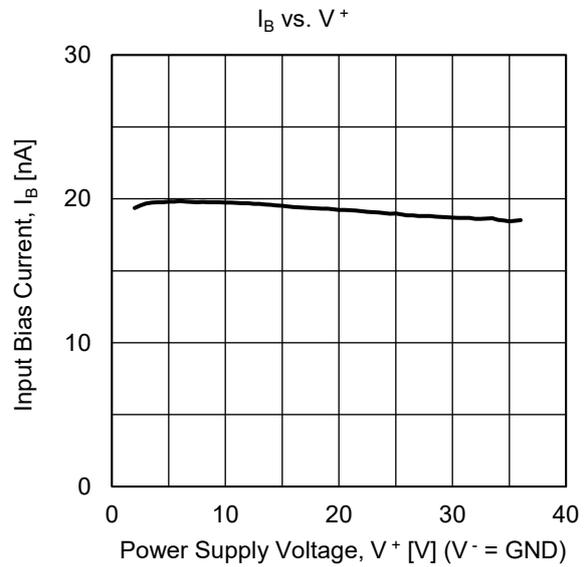
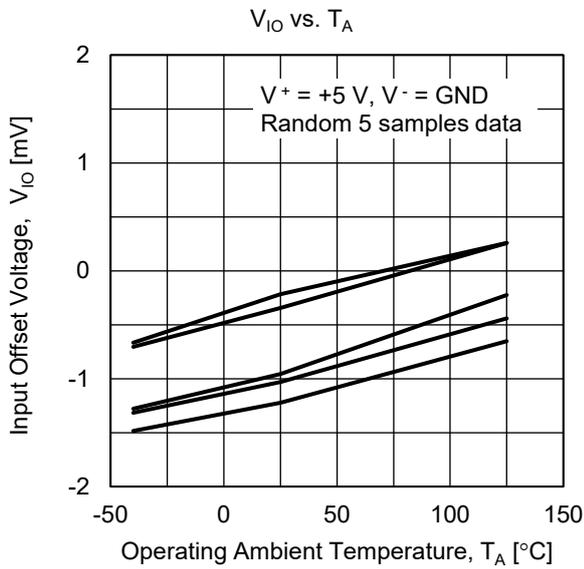
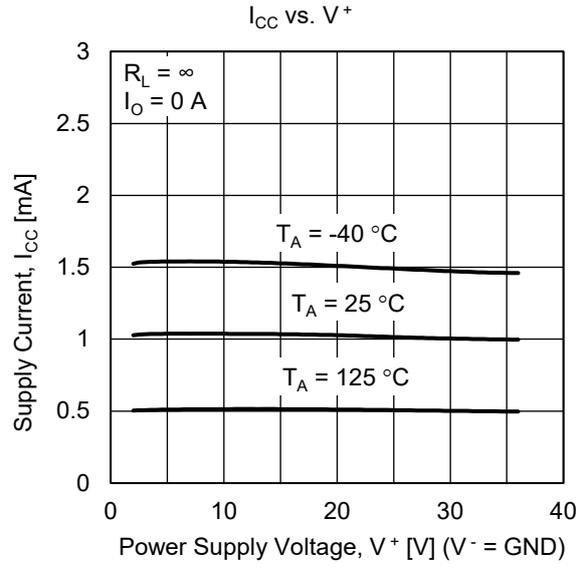
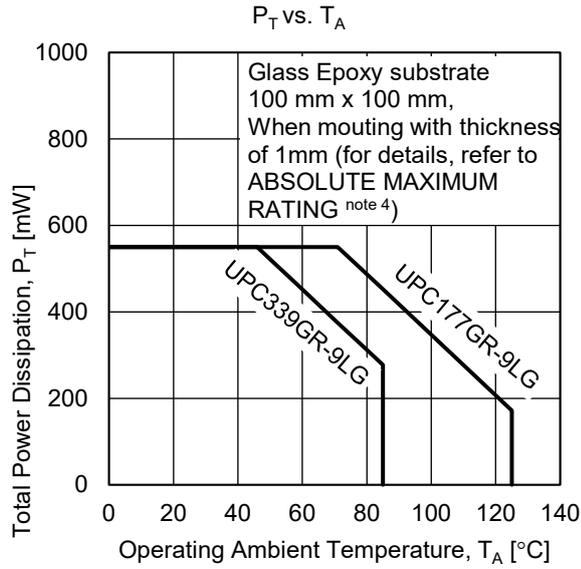
• Threshold Voltage

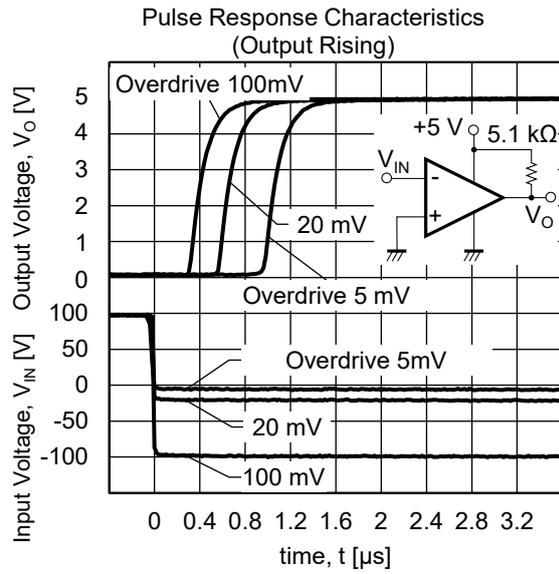
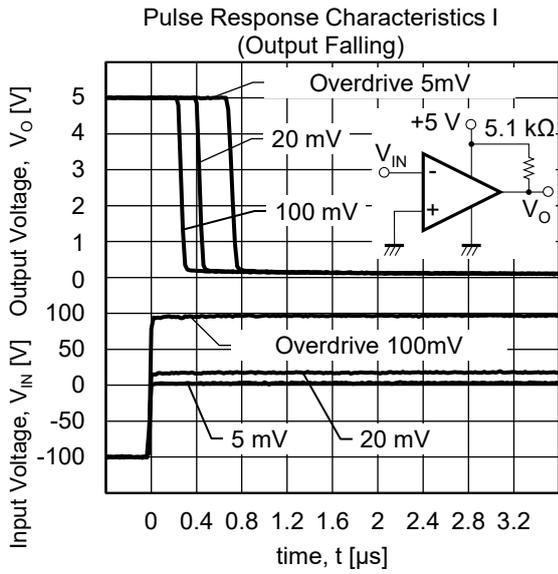
$$V_{TH(High)} \doteq V_{REF} + \frac{R_1}{R_L + R_2 + R_1} (V_{RL} - V_{REF})$$

$$V_{TH(Low)} \doteq V_{REF} - \frac{R_1}{R_1 + R_2} (V_{REF} - V_{OL})$$

$$(V_{RL} > V_{REF} > V_{OL})$$

ELECTRICAL CHARACTERISTICS CURVE ($T_A = 25\text{ }^\circ\text{C}$, TYP.) (Reference Value)



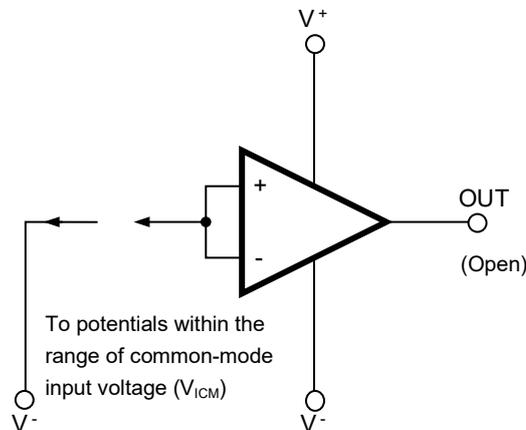


USE WITH PRECAUTIONS

Managing unused circuits

If there is an unused circuit, the following connection is recommended.

Example of unused circuit process



- **Ratings of input/output pin voltage**

When the voltage of input/output pin exceeds the absolute maximum rating, the parasitic diode within the IC may conduct, causing characteristics degradation or damage. In addition, if the input pin is lower than V^- , or the output pin exceeds the power supply voltage, it is recommended to make a clamping circuit using a diode with low forward voltage (e.g.: Schottky diode) as protection.

- **Range of common-mode input voltage**

When the supply voltage does not meet the condition of electrical characteristics, the range of common-mode input voltage is as follows.

$$V_{ICM} \text{ (TYP.)}: V^- \sim V^+ - 1.5 \text{ [V]} \text{ (} T_A = 25 \text{ }^\circ\text{C)}$$

During designing, do include some tolerance by considering temperature characteristics etc.

- **Regarding Input Current**

The Input Bias Current [I_B] specified in the electrical characteristics table, is the average value of current flowing through the +input terminal [I_N] and the current flowing through the -input terminal [I_I] in the balanced state of the differential amplifier circuit of the input stage (with negative feedback).

Therefore, since the differential amplifier circuit of the input stage is not balanced during comparison operation (in the case of comparator operation), the input current flows twice as much towards the low potential terminal.

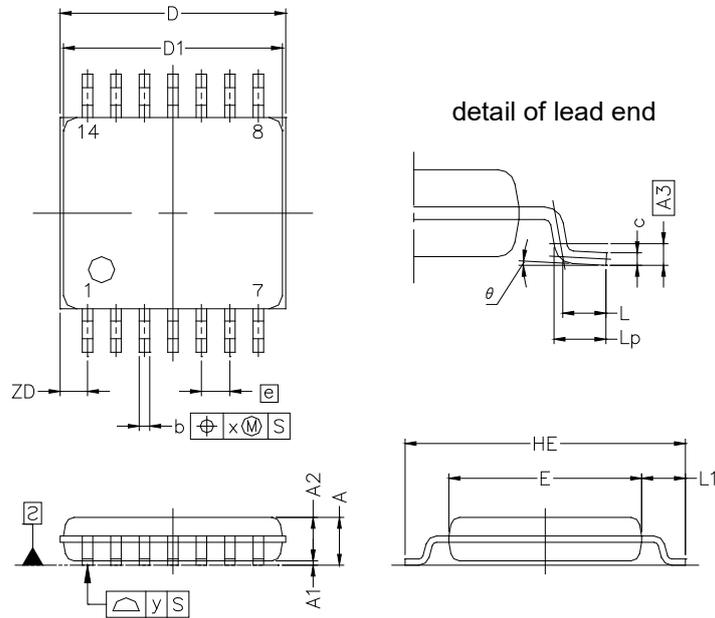
- **Handling of ICs**

When stress is added to the ICs due to warpage or bending of a board, the characteristic may fluctuates due to piezoelectric (piezo) effect. Therefore, pay attention to warpage or bending of a board.

PACKAGE DRAWINGS

14-PIN PLASTIC TSSOP

JEITA Package code	RENESAS code	Previous code	MASS(TYP.) [g]
P-TSSOP14-0225-0.65	PTSP0014JB-A	P14GR-65-9LG-1	—



Unit : mm

NOTE

Each lead centerline is located within 0.10 mm of its true position at maximum material condition.

ITEM	MILLIMETERS
D	5.15 ±0.15
D1	5.00 ±0.10
E	4.40 ±0.10
HE	6.40 ±0.20
A	1.20 MAX.
A1	0.10 ±0.05
A2	1.00 ±0.05
A3	0.25
b	0.24 ^{+0.06} _{-0.05}
c	0.145 ±0.055
L	0.5
Lp	0.60 ±0.15
L1	1.00 ±0.20
θ	3° ^{+5°} _{-3°}
e	0.65
x	0.10
y	0.10
ZD	0.625

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