

To our customers,

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## Old Company Name in Catalogs and Other Documents

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On April 1<sup>st</sup>, 2010, NEC Electronics Corporation merged with Renesas Technology Corporation, and Renesas Electronics Corporation took over all the business of both companies. Therefore, although the old company name remains in this document, it is a valid Renesas Electronics document. We appreciate your understanding.

Renesas Electronics website: <http://www.renesas.com>

April 1<sup>st</sup>, 2010  
Renesas Electronics Corporation

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## BIPOLAR ANALOG INTEGRATED CIRCUIT

# μPC3232TB

### 5 V, SILICON GERMANIUM MMIC MEDIUM OUTPUT POWER AMPLIFIER

#### DESCRIPTION

The μPC3232TB is a silicon germanium (SiGe) monolithic integrated circuit designed as IF amplifier for DBS tuners. This IC is manufactured using our 50 GHz  $f_{\max}$  UHS2 (Ultra High Speed Process) SiGe bipolar process.

#### FEATURES

- Low current :  $I_{CC} = 26.0$  mA TYP.
- Medium output power :  $P_{O(sat)} = +15.5$  dBm TYP. @  $f = 1.0$  GHz  
:  $P_{O(sat)} = +12.0$  dBm TYP. @  $f = 2.2$  GHz
- High linearity :  $P_{O(1\text{ dB})} = +11.0$  dBm TYP. @  $f = 1.0$  GHz  
:  $P_{O(1\text{ dB})} = +8.5$  dBm TYP. @  $f = 2.2$  GHz
- Power gain :  $G_P = 32.8$  dB MIN. @  $f = 1.0$  GHz  
:  $G_P = 33.5$  dB MIN. @  $f = 2.2$  GHz
- Gain flatness :  $\Delta G_P = 1.0$  dB TYP. @  $f = 1.0$  to  $2.2$  GHz
- Noise figure :  $NF = 4$  dB TYP. @  $f = 1.0$  GHz  
:  $NF = 4.1$  dB TYP. @  $f = 2.2$  GHz
- Supply voltage :  $V_{CC} = 4.5$  to  $5.5$  V
- Port impedance : input/output  $50\ \Omega$

#### APPLICATIONS

- IF amplifiers in LNB for DBS converters etc.

#### ORDERING INFORMATION

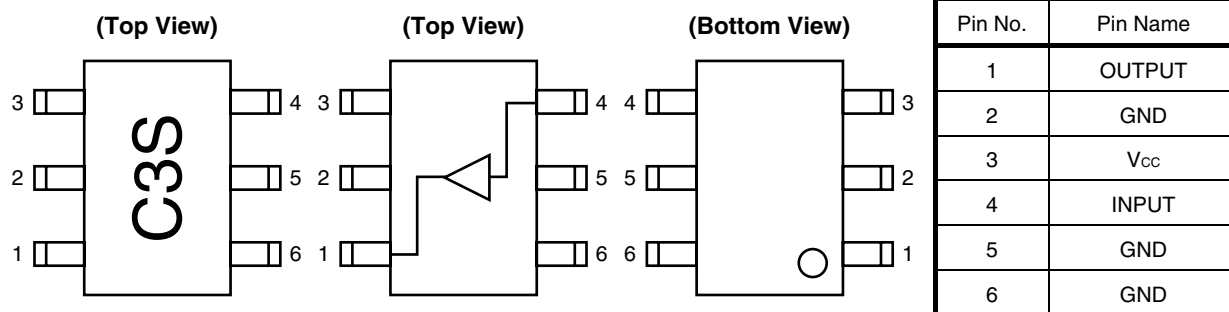
Part Number	Order Number	Package	Marking	Supplying Form
μPC3232TB-E3	μPC3232TB-E3-A	6-pin super minimold (Pb-Free)	C3S	<ul style="list-style-type: none"> <li>• Embossed tape 8 mm wide</li> <li>• Pin 1, 2, 3 face the perforation side of the tape</li> <li>• Qty 3 kpcs/reel</li> </ul>

**Remark** To order evaluation samples, please contact your nearby sales office  
Part number for sample order: μPC3232TB

**Caution** Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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# PIN CONNECTIONS



## PRODUCT LINE-UP OF 5 V-BIAS SILICON MMIC MEDIUM OUTPUT POWER AMPLIFIER (T<sub>A</sub> = +25°C, f = 1 GHz, V<sub>CC</sub> = V<sub>out</sub> = 5.0 V, Z<sub>S</sub> = Z<sub>L</sub> = 50 Ω)

Part No.	P <sub>O</sub> (sat) (dBm)	G <sub>P</sub> (dB)	NF (dB)	I <sub>CC</sub> (mA)	Package	Marking
$\mu$ PC2708TB	+10.0	15.0	6.5	26	6-pin super minimold	C1D
$\mu$ PC2709TB	+11.5	23.0	5.0	25		C1E
$\mu$ PC2710TB	+13.5	33.0	3.5	22		C1F
$\mu$ PC2776TB	+8.5	23.0	6.0	25		C2L
$\mu$ PC3223TB	+12.0	23.0	4.5	19		C3J
$\mu$ PC3225TB	+15.5 <sup>Note</sup>	32.5 <sup>Note</sup>	3.7 <sup>Note</sup>	24.5		C3M
$\mu$ PC3226TB	+13.0	25.0	5.3	15.5		C3N
$\mu$ PC3232TB	+15.5	32.8	4.0	26		C3S

**Note**  $\mu$ PC3225TB is f = 0.95 GHz

**Remark** Typical performance. Please refer to **ELECTRICAL CHARACTERISTICS** in detail.

# ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Conditions	Ratings	Unit
Supply Voltage	V <sub>CC</sub>	T <sub>A</sub> = +25°C	6.0	V
Total Circuit Current	I <sub>CC</sub>	T <sub>A</sub> = +25°C	45	mA
Power Dissipation	P <sub>D</sub>	T <sub>A</sub> = +85°C <b>Note</b>	270	mW
Operating Ambient Temperature	T <sub>A</sub>		−40 to +85	°C
Storage Temperature	T <sub>stg</sub>		−55 to +150	°C
Input Power	P <sub>in</sub>	T <sub>A</sub> = +25°C	0	dBm

**Note** Mounted on double-sided copper-clad 50 × 50 × 1.6 mm epoxy glass PWB

# RECOMMENDED OPERATING RANGE

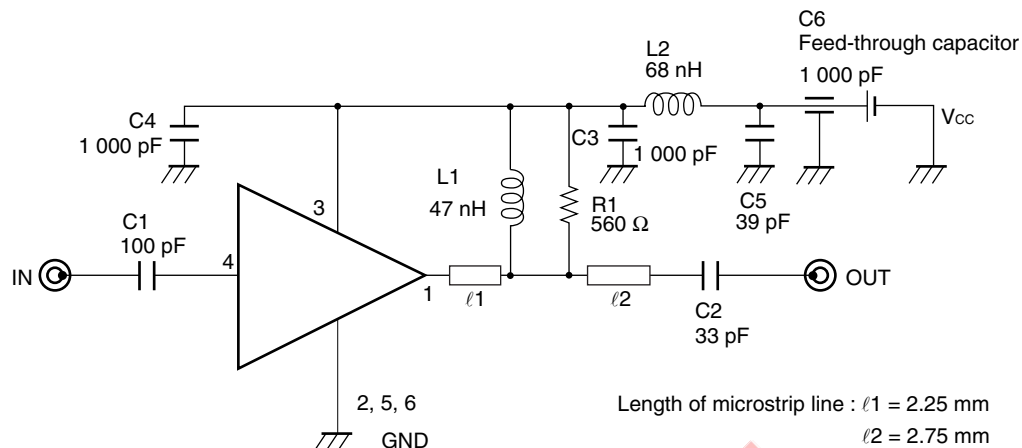
Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Supply Voltage	V <sub>CC</sub>		4.5	5.0	5.5	V
Operating Ambient Temperature	T <sub>A</sub>		−40	+25	+85	°C

Not recommended  
for new design

**ELECTRICAL CHARACTERISTICS ( $T_A = +25^\circ\text{C}$ ,  $V_{CC} = V_{out} = 5.0\text{ V}$ ,  $Z_S = Z_L = 50\ \Omega$ )**

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	$I_{CC}$	No input signal	20	26	32	mA
Power Gain 1	$G_{P1}$	$f = 0.25\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	29	31.5	34	dB
Power Gain 2	$G_{P2}$	$f = 1.0\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	30	32.8	35.5	
Power Gain 3	$G_{P3}$	$f = 1.8\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	31	33.8	37	
Power Gain 4	$G_{P4}$	$f = 2.2\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	30.5	33.5	36.5	
Power Gain 5	$G_{P5}$	$f = 2.6\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	29	32.2	35.5	
Power Gain 6	$G_{P6}$	$f = 3.0\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	27	30.7	34	
Gain Flatness	$\Delta G_P$	$f = 1.0\text{ to }2.2\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	—	1.0	—	dB
K factor 1	K1	$f = 1.0\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	—	1.3	—	—
K factor 2	K2	$f = 2.2\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	—	1.9	—	—
Saturated Output Power 1	$P_{O(sat)1}$	$f = 1.0\text{ GHz}$ , $P_{in} = 0\text{ dBm}$	+13	+15.5	—	dBm
Saturated Output Power 2	$P_{O(sat)2}$	$f = 2.2\text{ GHz}$ , $P_{in} = -5\text{ dBm}$	+9.5	+12	—	
Gain 1 dB Compression Output Power 1	$P_{O(1\text{ dB})1}$	$f = 1.0\text{ GHz}$	+8	+11	—	dBm
Gain 1 dB Compression Output Power 2	$P_{O(1\text{ dB})2}$	$f = 2.2\text{ GHz}$	+6	+8.5	—	
Noise Figure 1	NF1	$f = 1.0\text{ GHz}$	—	4	4.8	dB
Noise Figure 2	NF2	$f = 2.2\text{ GHz}$	—	4.1	4.9	
Isolation 1	ISL1	$f = 1.0\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	36	41	—	dB
Isolation 2	ISL2	$f = 2.2\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	38	45	—	
Input Return Loss 1	$RL_{in1}$	$f = 1.0\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	9.5	13	—	dB
Input Return Loss 2	$RL_{in2}$	$f = 2.2\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	10	14.5	—	
Output Return Loss 1	$RL_{out1}$	$f = 1.0\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	12	15.5	—	dB
Output Return Loss 2	$RL_{out2}$	$f = 2.2\text{ GHz}$ , $P_{in} = -35\text{ dBm}$	12	15	—	
Input 3rd Order Distortion Intercept Point 1	IIP <sub>31</sub>	$f_1 = 1\ 000\text{ MHz}$ , $f_2 = 1\ 001\text{ MHz}$	—	−9	—	dBm
Input 3rd Order Distortion Intercept Point 2	IIP <sub>32</sub>	$f_1 = 2\ 200\text{ MHz}$ , $f_2 = 2\ 201\text{ MHz}$	—	−15.5	—	
Output 3rd Order Distortion Intercept Point 1	OIP <sub>31</sub>	$f_1 = 1\ 000\text{ MHz}$ , $f_2 = 1\ 001\text{ MHz}$	—	+23.5	—	dBm
Output 3rd Order Distortion Intercept Point 2	OIP <sub>32</sub>	$f_1 = 2\ 200\text{ MHz}$ , $f_2 = 2\ 201\text{ MHz}$	—	+18	—	
2nd Order Intermodulation Distortion	IM <sub>2</sub>	$f_1 = 1\ 000\text{ MHz}$ , $f_2 = 1\ 001\text{ MHz}$ , $P_{out} = -5\text{ dBm/ tone}$	—	50	—	dBc
2nd Harmonic	2f <sub>0</sub>	$f_0 = 1.0\text{ GHz}$ , $P_{out} = -15\text{ dBm}$	—	70	—	dBc

## TEST CIRCUIT



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

## COMPONENTS OF TEST CIRCUIT FOR MEASURING ELECTRICAL CHARACTERISTICS

	Type	Value
R1	Chip Resistance	560 $\Omega$
L1	Chip Inductor	47 nH
L2	Chip Inductor	68 nH
C1	Chip Capacitor	100 pF
C2	Chip Capacitor	33 pF
C3, C4	Chip Capacitor	1 000 pF
C5	Chip Capacitor	39 pF
C6	Feed-through Capacitor	1 000 pF

## INDUCTOR FOR THE OUTPUT PIN

The internal output transistor of this IC, to output medium power. To supply current for output transistor, connect an inductor between the Vcc pin (pin 3) and output pin (pin 1). Select inductance, as the value listed above.

The inductor has both DC and AC effects. In terms of DC, the inductor biases the output transistor with minimum voltage drop to output enable high level. In terms of AC, the inductor makes output-port impedance higher to get enough gain. In this case, large inductance and Q is suitable (Refer to the following page).

## CAPACITORS FOR THE Vcc, INPUT AND OUTPUT PINS

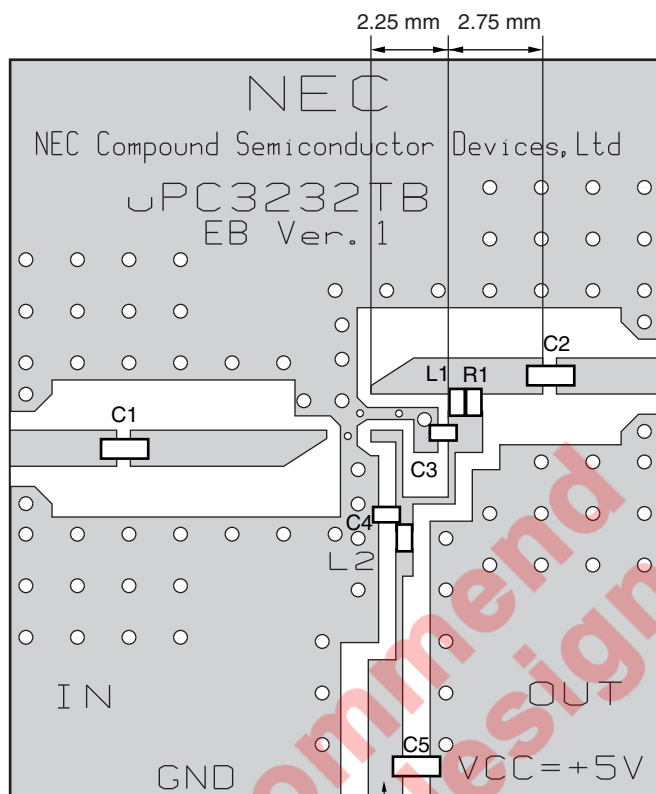
Capacitors of 1 000 pF are recommendable as the bypass capacitor for the Vcc pin and the coupling capacitors for the input and output pins.

The bypass capacitor connected to the Vcc pin is used to minimize ground impedance of Vcc pin. So, stable bias can be supplied against Vcc fluctuation.

The coupling capacitors, connected to the input and output pins, are used to cut the DC and minimize RF serial impedance. Their capacitances are therefore selected as lower impedance against a 50  $\Omega$  load. The capacitors thus perform as high pass filters, suppressing low frequencies to DC.

To obtain a flat gain from 100 MHz upwards, 1 000 pF capacitors are used in the test circuit. In the case of under 10 MHz operation, increase the value of coupling capacitor such as 10 000 pF. Because the coupling capacitors are determined by equation,  $C = 1/(2 \pi R f_c)$ .

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



C6: Feed-through Capacitor

COMPONENT LIST

	Value	Size
R1	560 $\Omega$	1005
L1	47 nH	1005
L2	68 nH	1005
C1	100 pF	1608
C2	33 pF	1608
C3, C4	1 000 pF	1005
C5	39 pF	1608
C6	1 000 pF	Feed-through Capacitor

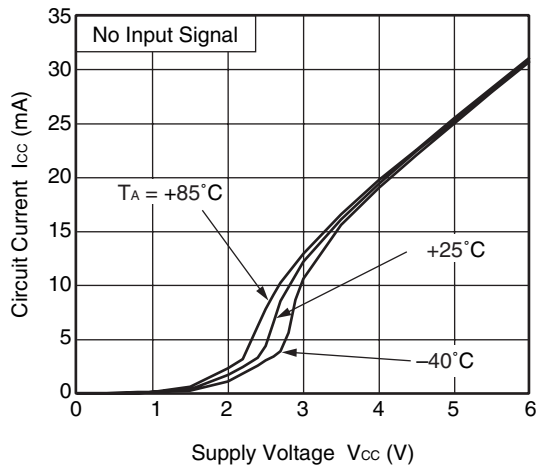
Notes

1. 19 × 21.46 × 0.51 mm double sided copper clad RO4003C (Rogers) board.
2. Back side: GND pattern
3. Au plated on pattern
4.  $\circ$   $\bigcirc$ : Through holes
5. L1, L2: FDK's products

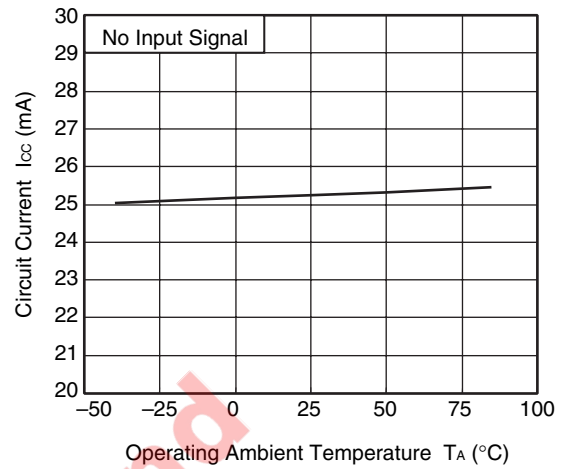


**TYPICAL CHARACTERISTICS ( $T_A = +25^\circ\text{C}$ ,  $V_{CC} = 5.0\text{ V}$ ,  $Z_S = Z_L = 50\ \Omega$ , unless otherwise specified)**

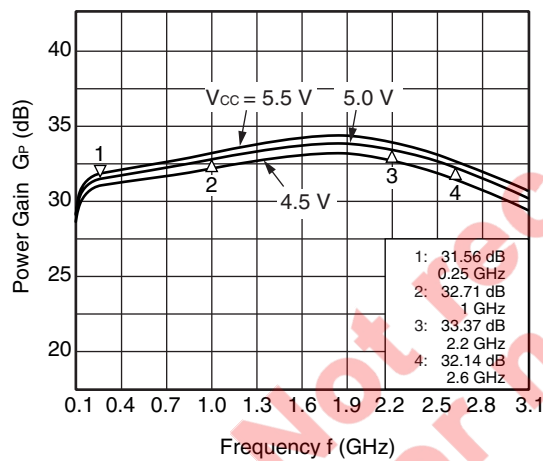
**CIRCUIT CURRENT vs. SUPPLY VOLTAGE**



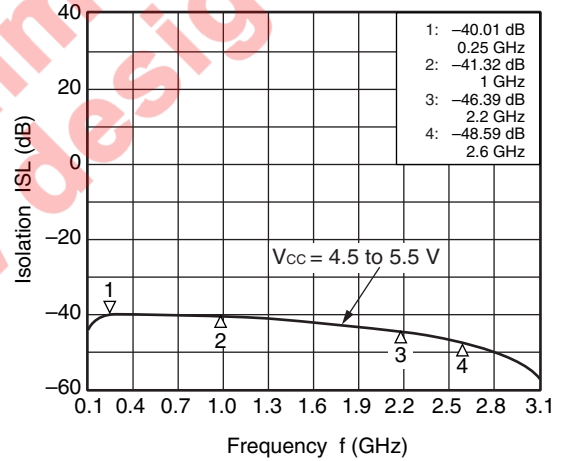
**CIRCUIT CURRENT vs. OPERATING AMBIENT TEMPERATURE**



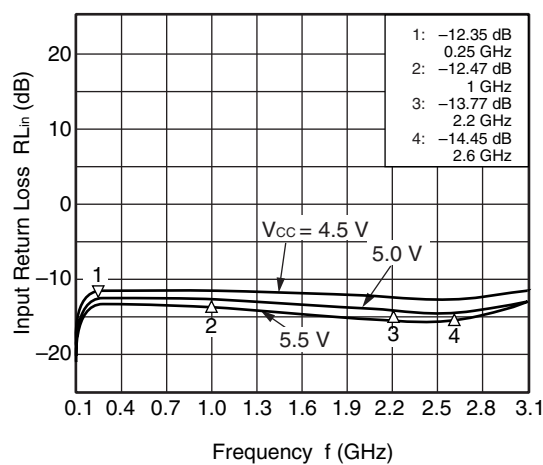
**POWER GAIN vs. FREQUENCY**



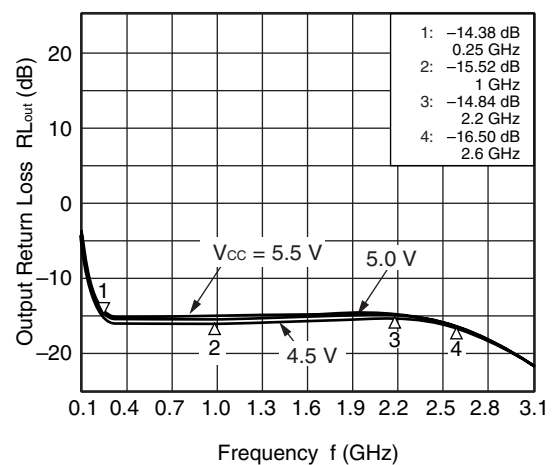
**ISOLATION vs. FREQUENCY**



**INPUT RETURN LOSS vs. FREQUENCY**

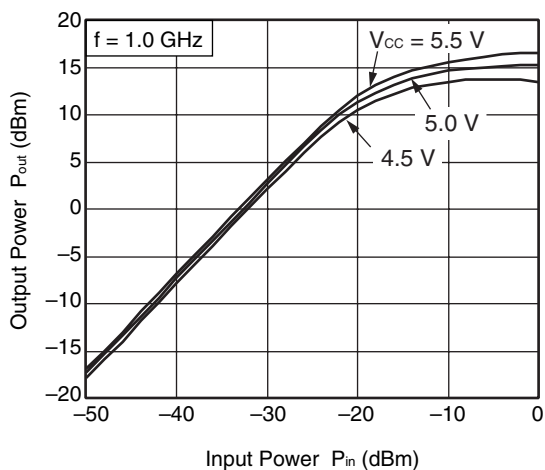


**OUTPUT RETURN LOSS vs. FREQUENCY**

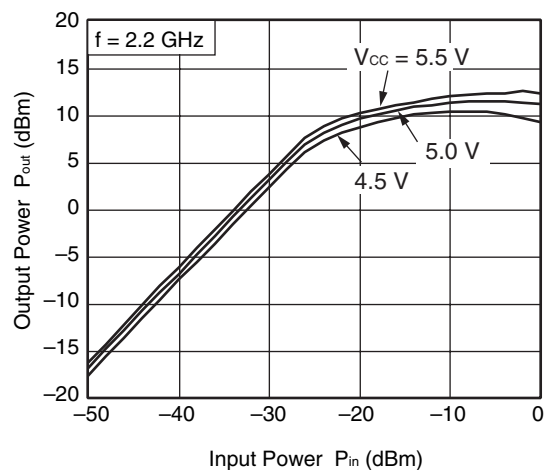


**Remark** The graphs indicate nominal characteristics.

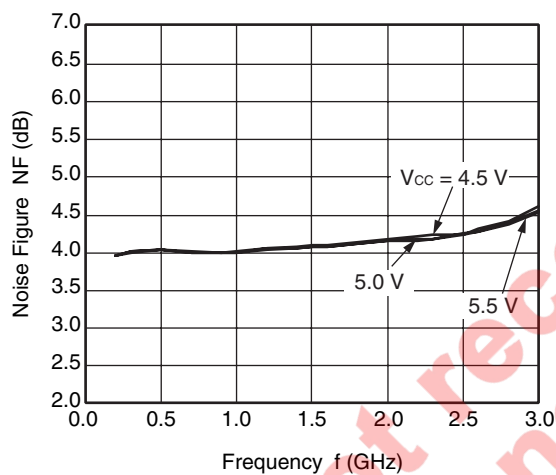
OUTPUT POWER vs. INPUT POWER



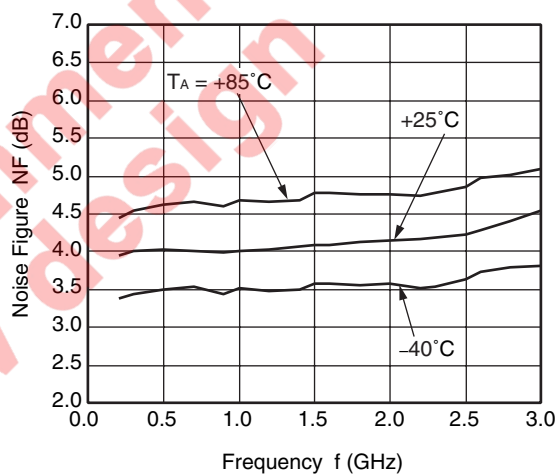
OUTPUT POWER vs. INPUT POWER



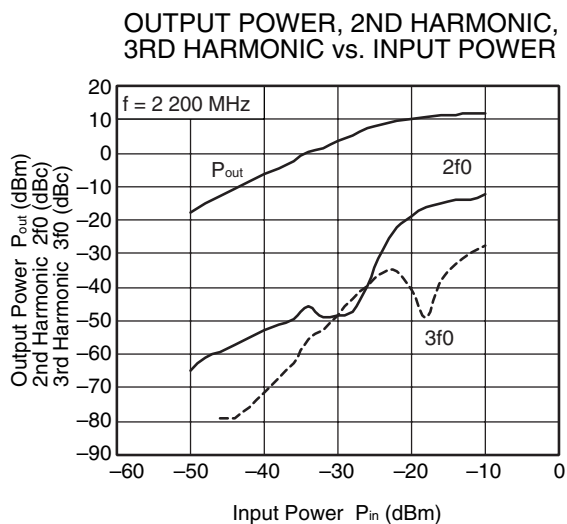
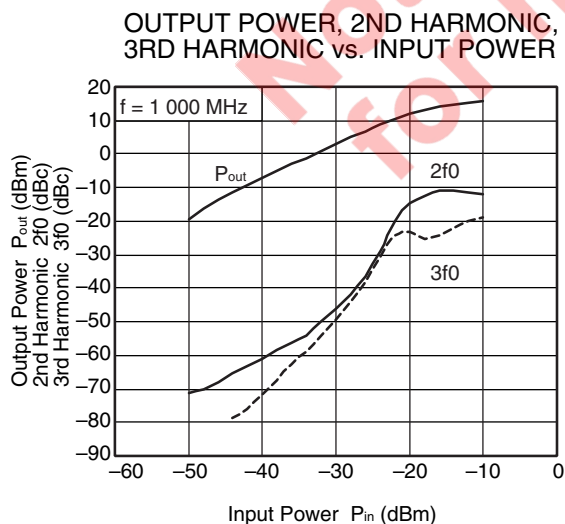
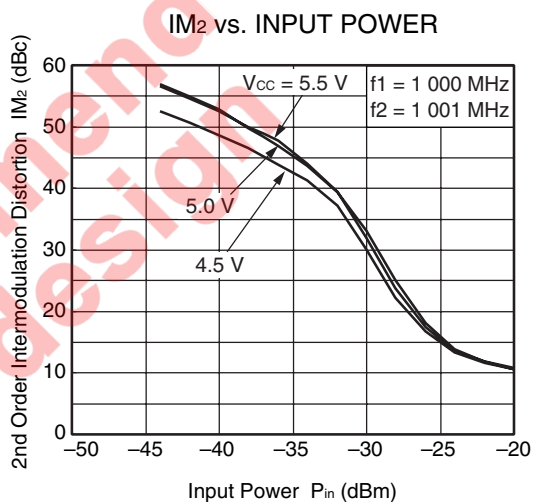
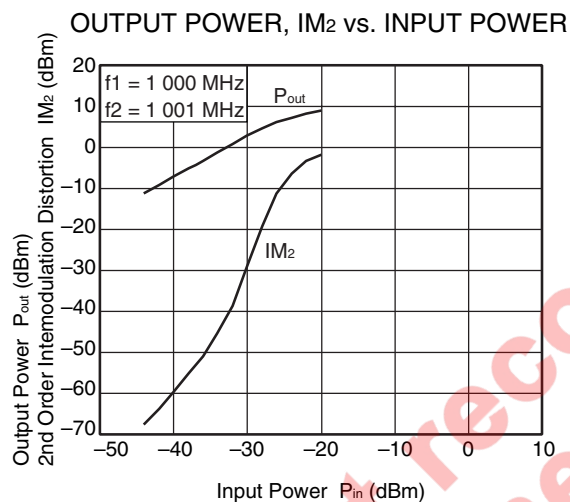
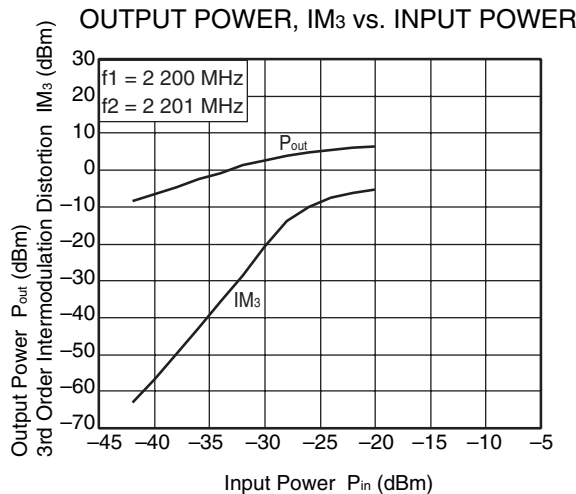
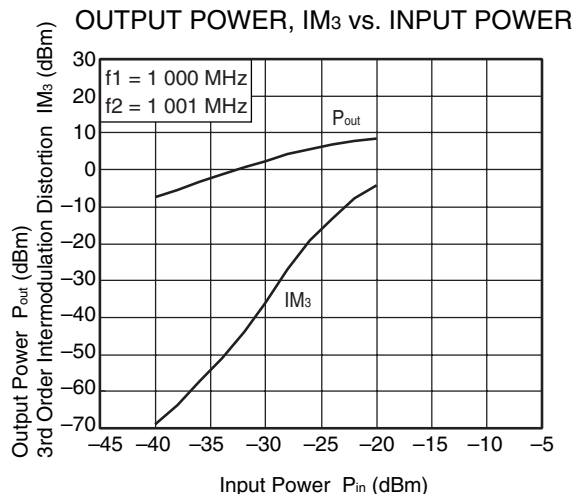
NOISE FIGURE vs. FREQUENCY



NOISE FIGURE vs. FREQUENCY



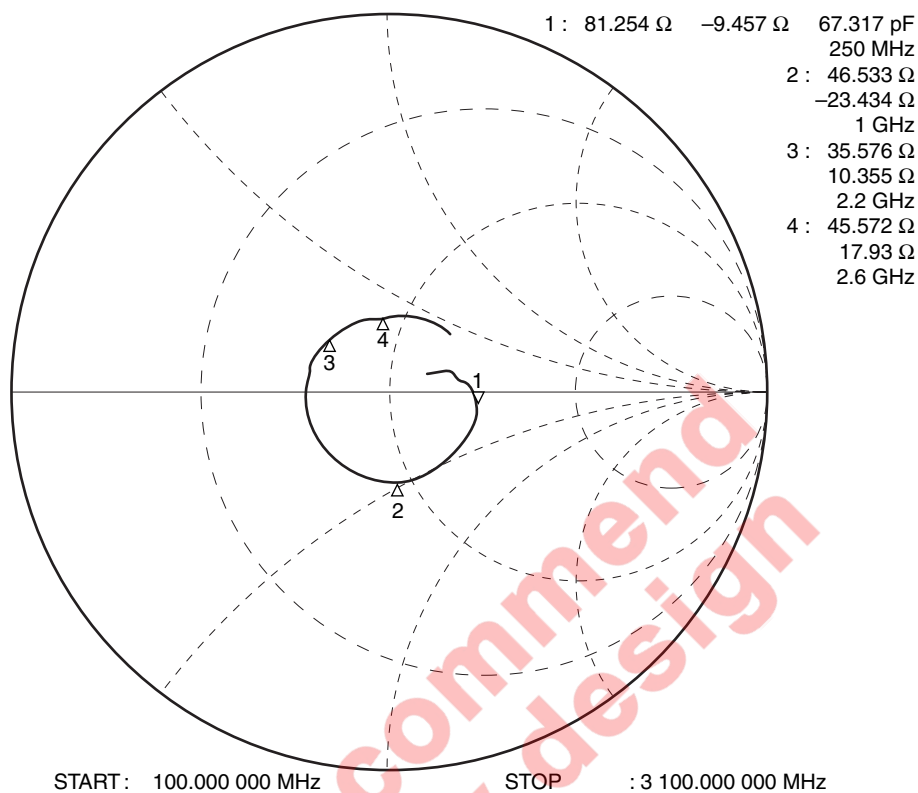
**Remark** The graphs indicate nominal characteristics.



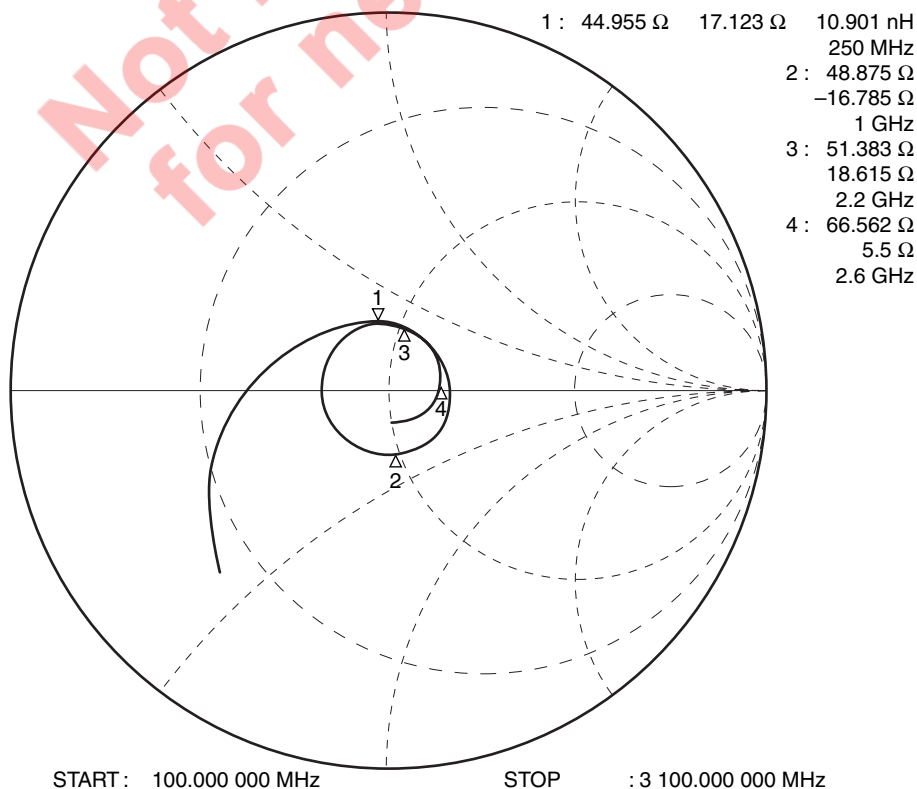
**Remark** The graphs indicate nominal characteristics.

S-PARAMETERS ( $T_A = +25^\circ\text{C}$ ,  $V_{DD} = V_{CC} = 5.0\text{ V}$ ,  $P_{in} = -35\text{ dBm}$ )

S<sub>11</sub>-FREQUENCY



S<sub>22</sub>-FREQUENCY



**S-PARAMETERS**

S-parameters/Noise parameters are provided on our web site in a form (S2P) that enables direct import to a microwave circuit simulator without keyboard input.

Click here to download S-parameters.

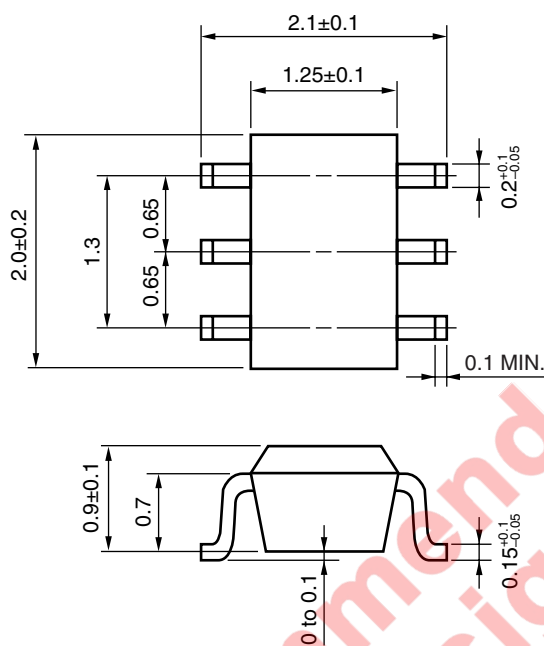
[RF and Microwave] → [Device Parameters]

URL <http://www.ncsd.necel.com/microwave/index.html>

Not recommend  
for new design

PACKAGE DIMENSIONS

6-PIN SUPER MINIMOLD (UNIT: mm)



**NOTES ON CORRECT USE**

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation).  
All the ground terminals must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to the V<sub>CC</sub> line.
- (4) The inductor (L) must be attached between V<sub>CC</sub> and output pins. The inductance value should be determined in accordance with desired frequency.
- (5) The DC cut capacitor must be attached to input and output pin.

**RECOMMENDED SOLDERING CONDITIONS**

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

**Caution** Do not use different soldering methods together (except for partial heating).

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