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Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: <http://www.renesas.com>

April 1st, 2010
Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (<http://www.renesas.com>)

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BIPOLAR ANALOG INTEGRATED CIRCUIT

μ PC3241TB

3.3 V, SILICON MMIC MEDIUM OUTPUT POWER AMPLIFIER

DESCRIPTION

The μ PC3241TB is a silicon monolithic integrated circuit designed as IF amplifier for DBS LNB.

This device exhibits low noise figure and high power gain characteristics.

This IC is manufactured using our UHS0 (Ultra High Speed Process) bipolar process.

FEATURES

- Low current : $I_{CC} = 19.8$ mA TYP.
- Power gain : $G_P = 23.5$ dB TYP. @ $f = 1.0$ GHz
: $G_P = 24.0$ dB TYP. @ $f = 2.2$ GHz
- Gain flatness : $\Delta G_P = 0.7$ dB TYP. @ $f = 1.0$ to 2.2 GHz
- Noise figure : $NF = 4.0$ dB TYP. @ $f = 1.0$ GHz
: $NF = 4.3$ dB TYP. @ $f = 2.2$ GHz
- High linearity : $P_{O(1dB)} = +7.5$ dBm TYP. @ $f = 1.0$ GHz
: $P_{O(1dB)} = +6.0$ dBm TYP. @ $f = 2.2$ GHz
- Supply voltage : $V_{CC} = 3.0$ to 3.6 V
- Port impedance : input/output 50Ω

APPLICATIONS

- IF amplifiers in DBS LNB, other L-band amplifiers, etc.

ORDERING INFORMATION

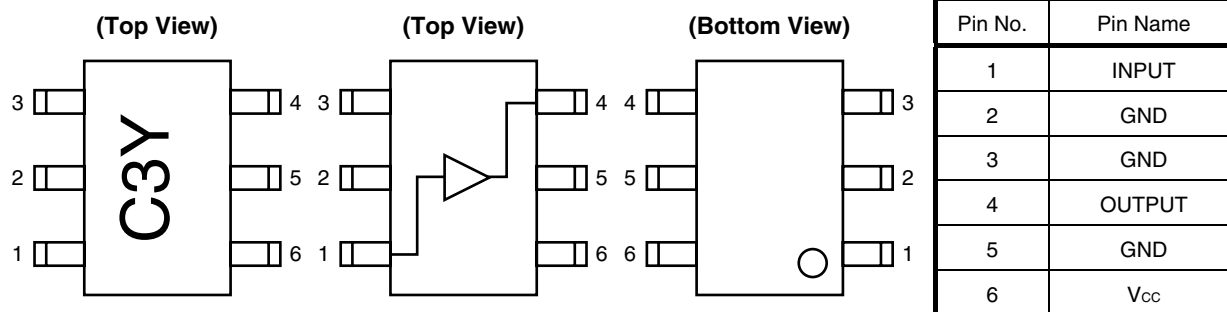
Part Number	Order Number	Package	Marking	Supplying Form
μ PC3241TB-E3	μ PC3241TB-E3-A	6-pin super minimold (Pb-Free)	C3Y	• Embossed tape 8 mm wide • Pin 1, 2, 3 face the perforation side of the tape • Qty 3 kpcs/reel

Remark To order evaluation samples, please contact your nearby sales office
Part number for sample order: μ PC3241TB

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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PIN CONNECTIONS AND INTERNAL BLOCK DIAGRAM



PRODUCT LINE-UP OF 3 V or 3.3 V-BIAS SILICON MMIC MEDIUM OUTPUT POWER AMPLIFIER
(T_A = +25°C, V_{cc} = V_{out} = 3.0 V or 3.3 V, Z_s = Z_L = 50 Ω)

Part No.	V _{cc} (V)	I _{cc} (mA)	G _p (dB)	NF (dB)	P _O (1 dB) (dBm)	P _O (sat) (dBm)	Package	Marking
μ PC2762TB	3.0	26.5	13.0 (0.9 GHz) 15.5 (1.9 GHz)	6.5 (0.9 GHz) 7.0 (1.9 GHz)	+8.0 (0.9 GHz) +7.0 (1.9 GHz)	+9.0 (0.9 GHz) +8.5 (1.9 GHz)	6-pin super minimold	C1Z
μ PC2763TB		27.0	20.0 (0.9 GHz) 21.0 (1.9 GHz)	5.5 (0.9 GHz) 5.5 (1.9 GHz)	+9.5 (0.9 GHz) +6.5 (1.9 GHz)	+11.0 (0.9 GHz) +8.0 (1.9 GHz)		C2A
μ PC2771TB		36.0	21.0 (0.9 GHz) 21.0 (1.5 GHz)	6.0 (0.9 GHz) 6.0 (1.5 GHz)	+11.5 (0.9 GHz) +9.5 (1.5 GHz)	+12.5 (0.9 GHz) +11.0 (1.5 GHz)		C2H
μ PC8181TB		23.0	19.0 (0.9 GHz) 21.0 (1.9 GHz) 22.0 (2.4 GHz)	4.5 (0.9 GHz) 4.5 (1.9 GHz) 4.5 (2.4 GHz)	+8.0 (0.9 GHz) +7.0 (1.9 GHz) +7.0 (2.4 GHz)	+9.5 (0.9 GHz) +9.0 (1.9 GHz) +9.0 (2.4 GHz)		C3E
μ PC8182TB		30.0	21.5 (0.9 GHz) 20.5 (1.9 GHz) 20.5 (2.4 GHz)	4.5 (0.9 GHz) 4.5 (1.9 GHz) 5.0 (2.4 GHz)	+9.5 (0.9 GHz) +9.0 (1.9 GHz) +8.0 (2.4 GHz)	+11.0 (0.9 GHz) +10.5 (1.9 GHz) +10.0 (2.4 GHz)		C3F
μ PC3239TB	3.3	29.0	25.0 (1.0 GHz) 25.5 (2.2 GHz)	4.0 (1.0 GHz) 4.3 (2.2 GHz)	+10 (1.0 GHz) +8 (2.2 GHz)	+12.5 (1.0 GHz) +10 (2.2 GHz)		C3V
μ PC3241TB		19.8	23.5 (1.0 GHz) 24.0 (2.2 GHz)	4.0 (1.0 GHz) 4.3 (2.2 GHz)	+7.5 (1.0 GHz) +6.0 (2.2 GHz)	—		C3Y

Remark Typical performance. Please refer to **ELECTRICAL CHARACTERISTICS** in detail.

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Conditions	Ratings	Unit
Supply Voltage	V _{CC}	T _A = +25°C, pin 4 and 6	4.0	V
Total Circuit Current	I _{CC}	T _A = +25°C, pin 4 and 6	55	mA
Power Dissipation	P _D	T _A = +85°C Note	270	mW
Operating Ambient Temperature	T _A		−40 to +85	°C
Storage Temperature	T _{stg}		−55 to +150	°C
Input Power	P _{in}	T _A = +25°C	−10	dBm

Note Mounted on double-sided copper-clad 50 × 50 × 1.6 mm epoxy glass PWB

RECOMMENDED OPERATING RANGE

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Supply Voltage	V _{CC}	The same voltage should be applied to pin 4 and 6.	3.0	3.3	3.6	V
Operating Ambient Temperature	T _A		−40	+25	+85	°C

Not recommended
for new design

ELECTRICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$, $V_{CC} = V_{out} = 3.3\text{ V}$, $Z_S = Z_L = 50\ \Omega$, unless otherwise specified)

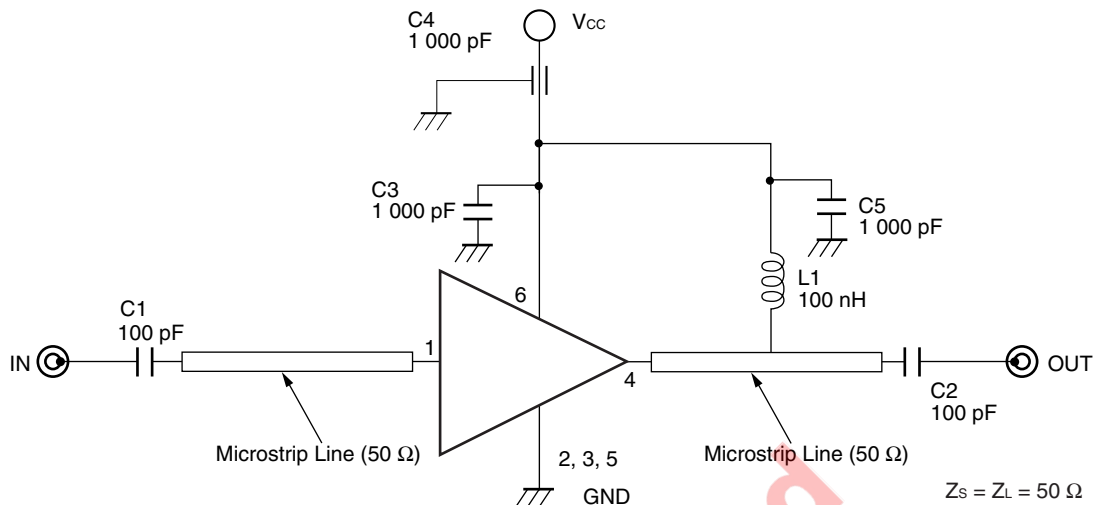
Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Circuit Current	I_{CC}	No input signal	15	19.8	25	mA
Power Gain 1	G_{P1}	$f = 0.25\text{ GHz}$, $P_{in} = -30\text{ dBm}$	20	23	26	dB
Power Gain 2	G_{P2}	$f = 1.0\text{ GHz}$, $P_{in} = -30\text{ dBm}$	20.5	23.5	26.5	
Power Gain 3	G_{P3}	$f = 1.8\text{ GHz}$, $P_{in} = -30\text{ dBm}$	21	24	27	
Power Gain 4	G_{P4}	$f = 2.2\text{ GHz}$, $P_{in} = -30\text{ dBm}$	21	24	27	
Gain 1 dB Compression Output Power 1	$P_{O(1\text{ dB})1}$	$f = 1.0\text{ GHz}$	+4.5	+7.5	–	dBm
Gain 1 dB Compression Output Power 2	$P_{O(1\text{ dB})2}$	$f = 2.2\text{ GHz}$	+3.0	+6.0	–	
Noise Figure 1	NF1	$f = 1.0\text{ GHz}$	–	4.0	4.8	dB
Noise Figure 2	NF2	$f = 2.2\text{ GHz}$	–	4.3	5.1	
Isolation 1	ISL1	$f = 1.0\text{ GHz}$, $P_{in} = -30\text{ dBm}$	27	32	–	dB
Isolation 2	ISL2	$f = 2.2\text{ GHz}$, $P_{in} = -30\text{ dBm}$	28	33	–	
Input Return Loss 1	RL_{in1}	$f = 1.0\text{ GHz}$, $P_{in} = -30\text{ dBm}$	15	20	–	dB
Input Return Loss 2	RL_{in2}	$f = 2.2\text{ GHz}$, $P_{in} = -30\text{ dBm}$	10	16	–	
Output Return Loss 1	RL_{out1}	$f = 1.0\text{ GHz}$, $P_{in} = -30\text{ dBm}$	11	17	–	dB
Output Return Loss 2	RL_{out2}	$f = 2.2\text{ GHz}$, $P_{in} = -30\text{ dBm}$	13	25	–	

STANDARD CHARACTERISTICS FOR REFERENCE

($T_A = +25^\circ\text{C}$, $V_{CC} = V_{out} = 3.3\text{ V}$, $Z_S = Z_L = 50\ \Omega$, unless otherwise specified)

Parameter	Symbol	Test Conditions	Reference Value	Unit
Power Gain 5	G_{P5}	$f = 2.6\text{ GHz}$, $P_{in} = -30\text{ dBm}$	24	dB
Power Gain 6	G_{P6}	$f = 3.0\text{ GHz}$, $P_{in} = -30\text{ dBm}$	23	
Gain Flatness	ΔG_P	$f = 1.0\text{ to }2.2\text{ GHz}$, $P_{in} = -30\text{ dBm}$	0.7	dB
K factor 1	K1	$f = 1.0\text{ GHz}$, $P_{in} = -30\text{ dBm}$	1.4	–
K factor 2	K2	$f = 2.2\text{ GHz}$, $P_{in} = -30\text{ dBm}$	1.5	–
Output 3rd Order Intercept Point 1	OIP_{31}	$f_1 = 1\ 000\text{ MHz}$, $f_2 = 1\ 001\text{ MHz}$	19.5	dBm
Output 3rd Order Intercept Point 2	OIP_{32}	$f_1 = 2\ 200\text{ MHz}$, $f_2 = 2\ 201\text{ MHz}$	15	
Input 3rd Order Intercept Point 1	IIP_{31}	$f_1 = 1\ 000\text{ MHz}$, $f_2 = 1\ 001\text{ MHz}$	–4	dBm
Input 3rd Order Intercept Point 2	IIP_{32}	$f_1 = 2\ 200\text{ MHz}$, $f_2 = 2\ 201\text{ MHz}$	–9	
2nd Order Intermodulation Distortion	IM_2	$f_1 = 1\ 000\text{ MHz}$, $f_2 = 1\ 001\text{ MHz}$, $P_{out} = -5\text{ dBm/ tone}$	50	dBc
2nd Harmonics	$2f_0$	$f_0 = 1.0\text{ GHz}$, $P_{out} = -15\text{ dBm}$	65	dBc

TEST CIRCUIT



The application circuits and their parameters are for reference only and are not intended for use in actual design-ins.

COMPONENTS OF TEST CIRCUIT FOR MEASURING ELECTRICAL CHARACTERISTICS

	Type	Value
L1 ^{Note}	Chip Inductor	100 nH
C1, C2	Chip Capacitor	100 pF
C3, C5	Chip Capacitor	1 000 pF
C4	Feed-through Capacitor	1 000 pF

Note There is a case to show a dimple wave of characteristic by a chip inductor L1 part in the high frequency area. In that case, please reduce a value of L1.

INDUCTOR FOR THE OUTPUT PIN

The internal output transistor of this IC, to output medium power. To supply current for output transistor, connect an inductor between the Vcc pin (pin 6) and output pin (pin 4). Select inductance, as the value listed above.

The inductor has both DC and AC effects. In terms of DC, the inductor biases the output transistor with minimum voltage drop to output enable high level. In terms of AC, the inductor makes output-port impedance higher to get enough gain. In this case, large inductance and Q is suitable (Refer to the following page).

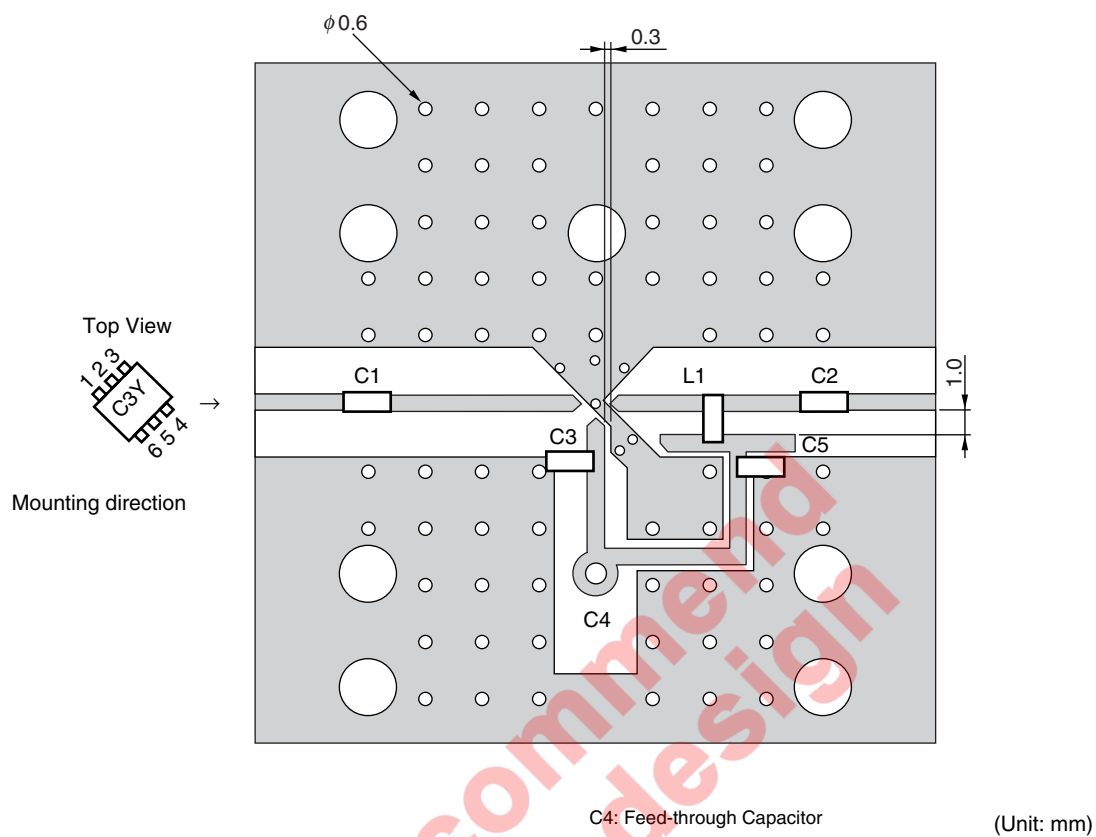
CAPACITORS FOR THE Vcc, INPUT AND OUTPUT PINS

Capacitors of 1 000 pF are recommendable as the bypass capacitor for the Vcc pin and the coupling capacitors for the input and output pins.

The bypass capacitor connected to the Vcc pin is used to minimize ground impedance of Vcc pin. So, stable bias can be supplied against Vcc fluctuation.

The coupling capacitors, connected to the input and output pins, are used to cut the DC and minimize RF serial impedance. Their capacitances are therefore selected as lower impedance against a 50 Ω load. The capacitors thus perform as high pass filters, suppressing low frequencies to DC.

ILLUSTRATION OF THE TEST CIRCUIT ASSEMBLED ON EVALUATION BOARD



COMPONENT LIST

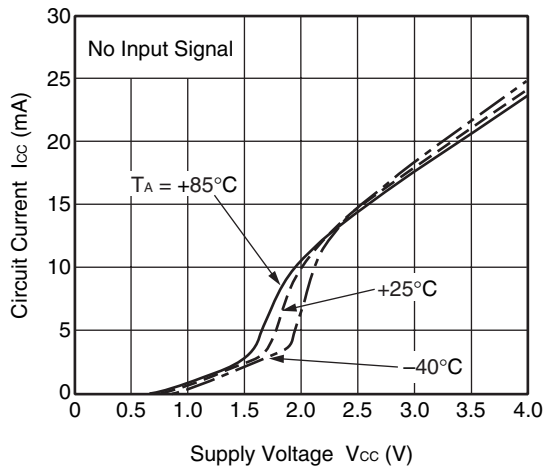
	Type	Value	Size
L1	Chip Inductor	100 nH	1005
C1, C2	Chip Capacitor	100 pF	1608
C3, C5	Chip Capacitor	1 000 pF	1005
C4	Feed-through Capacitor	1 000 pF	—

Notes

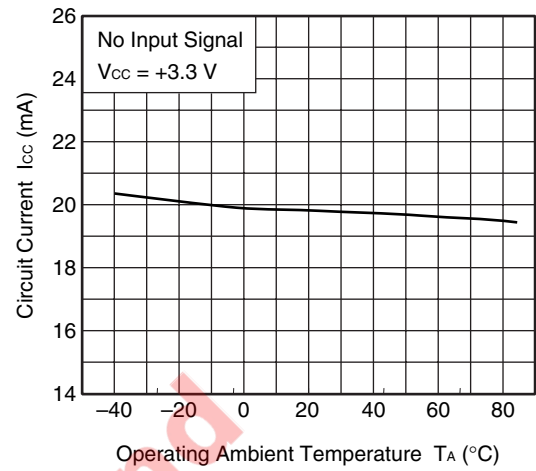
1. 30 × 30 × 0.4 mm double sided 35 μ m copper clad polyimide board.
2. Back side: GND pattern
3. Solder plated on pattern
4. ○: Through holes

TYPICAL CHARACTERISTICS ($T_A = +25^\circ\text{C}$, $V_{CC} = V_{out} = 3.3\text{ V}$, $Z_s = Z_L = 50\ \Omega$, unless otherwise specified)

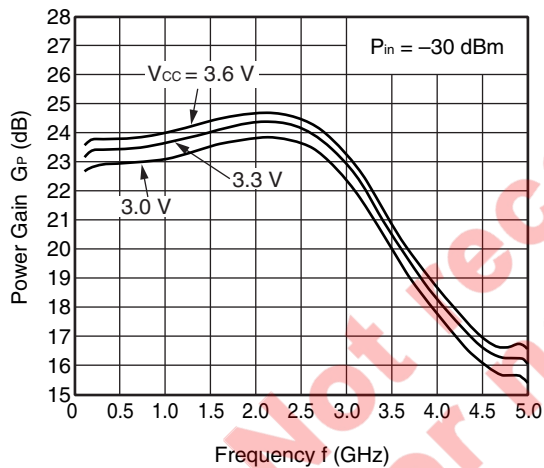
CIRCUIT CURRENT vs. SUPPLY VOLTAGE



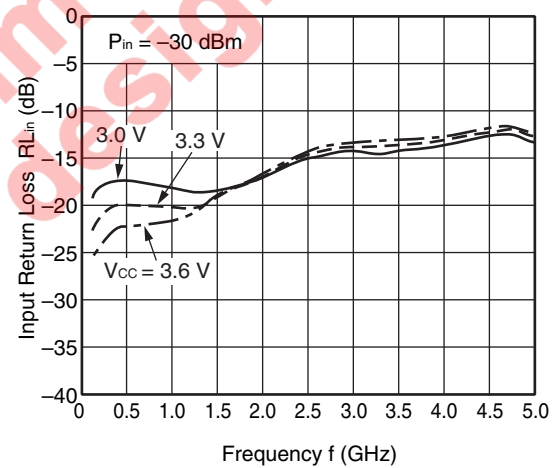
CIRCUIT CURRENT vs. OPERATING AMBIENT TEMPERATURE



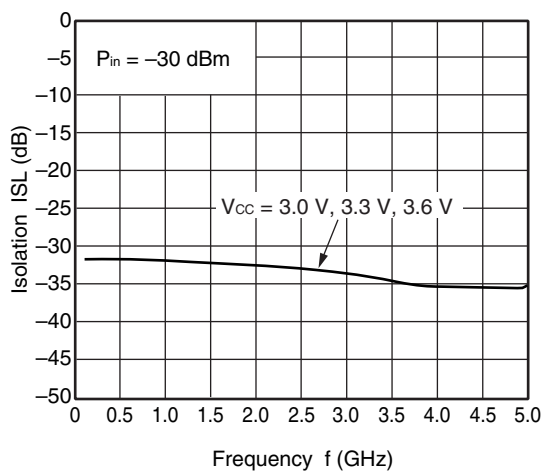
POWER GAIN vs. FREQUENCY



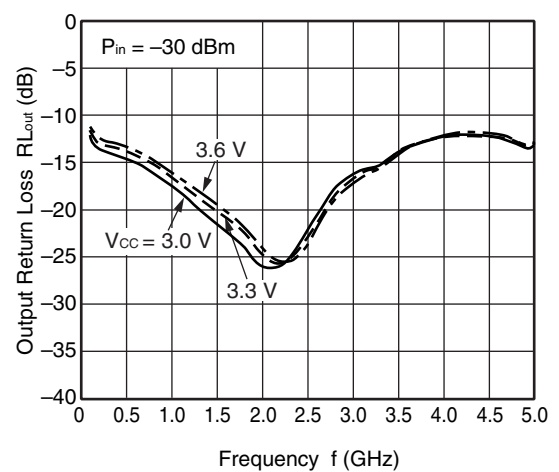
INPUT RETURN LOSS vs. FREQUENCY



ISOLATION vs. FREQUENCY

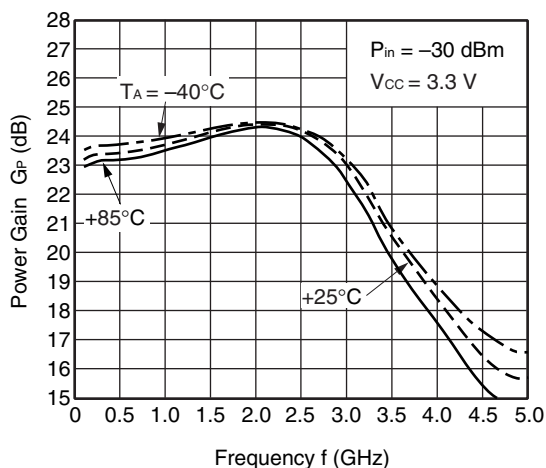


OUTPUT RETURN LOSS vs. FREQUENCY

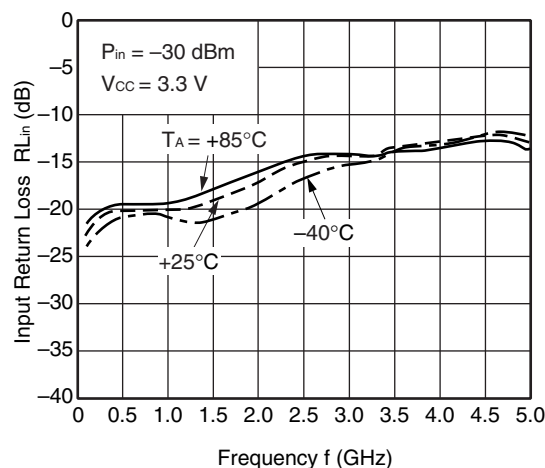


Remark The graphs indicate nominal characteristics.

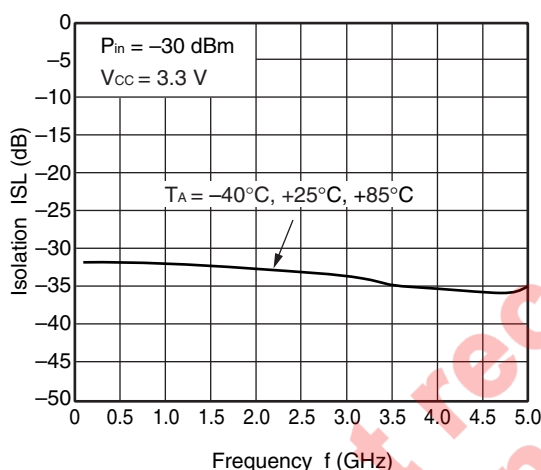
POWER GAIN vs. FREQUENCY



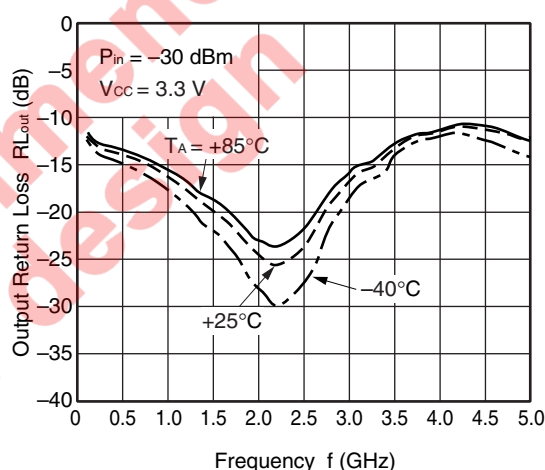
INPUT RETURN LOSS vs. FREQUENCY



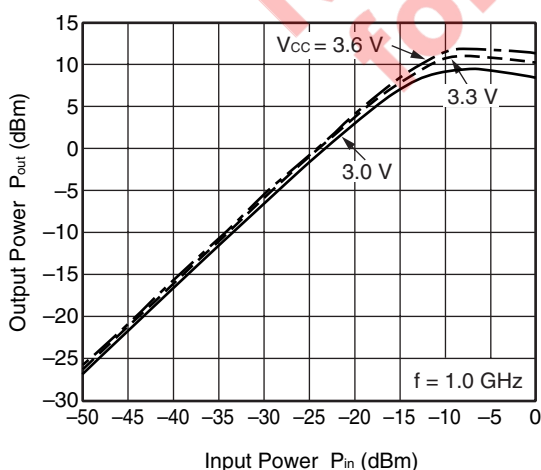
ISOLATION vs. FREQUENCY



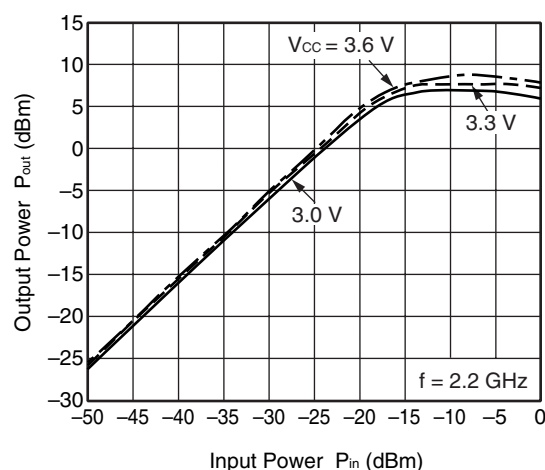
OUTPUT RETURN LOSS vs. FREQUENCY



OUTPUT POWER vs. INPUT POWER

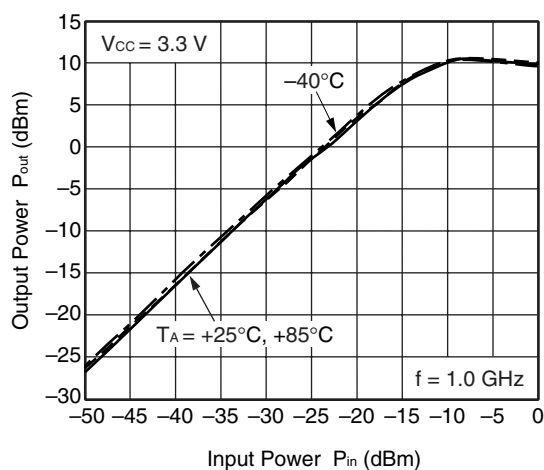


OUTPUT POWER vs. INPUT POWER

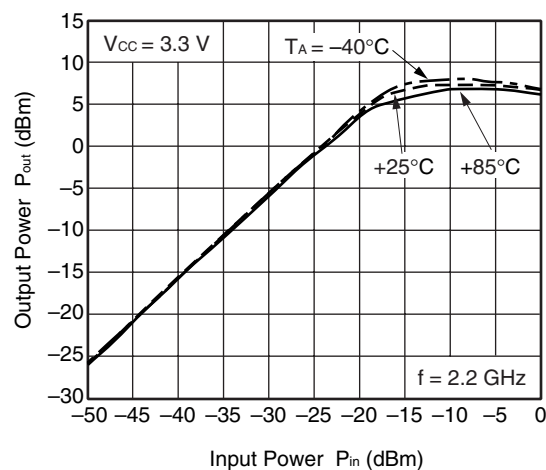


Remark The graphs indicate nominal characteristics.

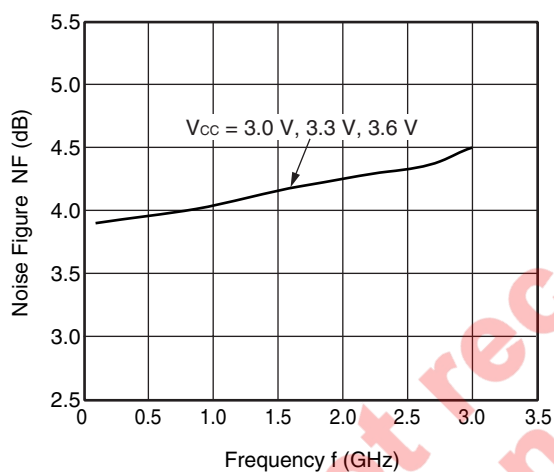
OUTPUT POWER vs. INPUT POWER



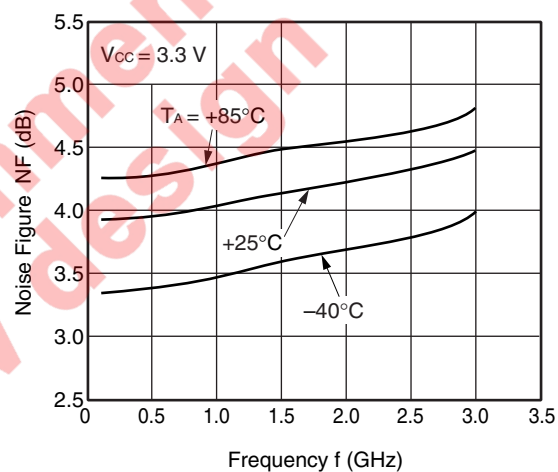
OUTPUT POWER vs. INPUT POWER



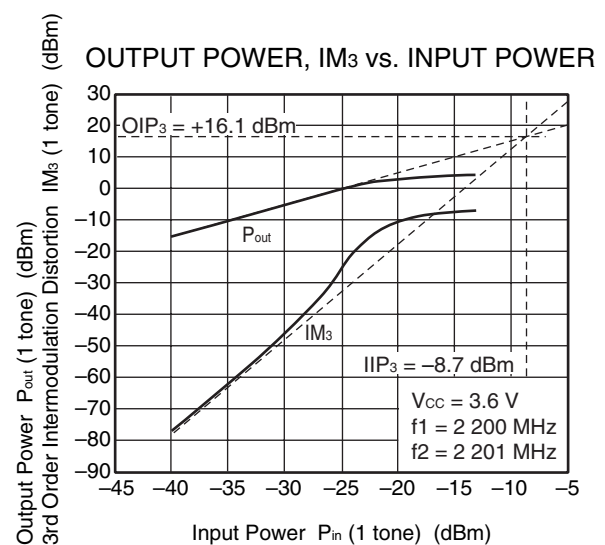
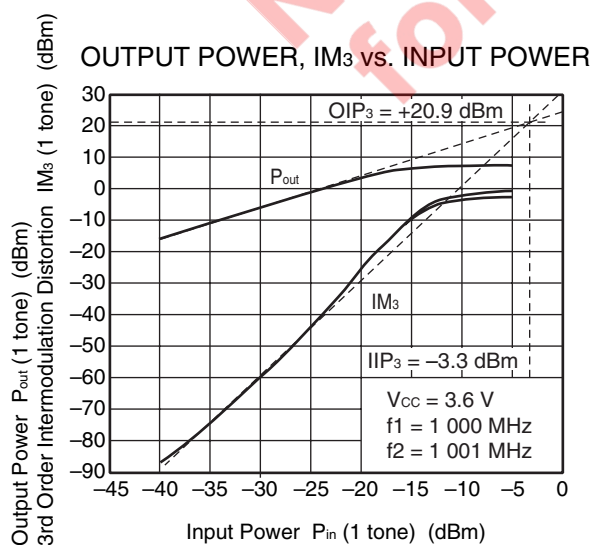
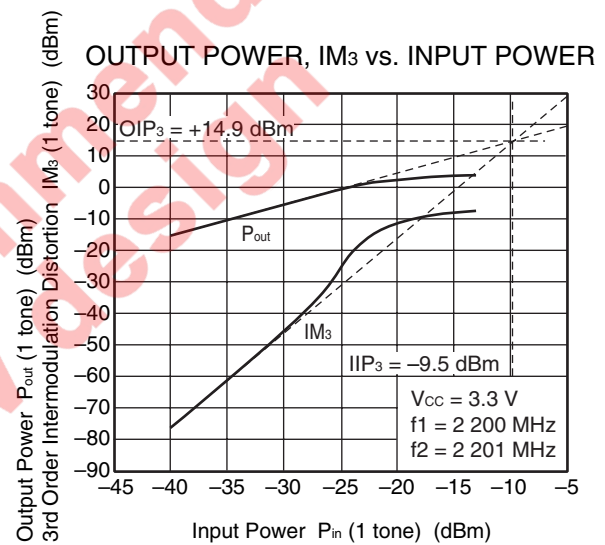
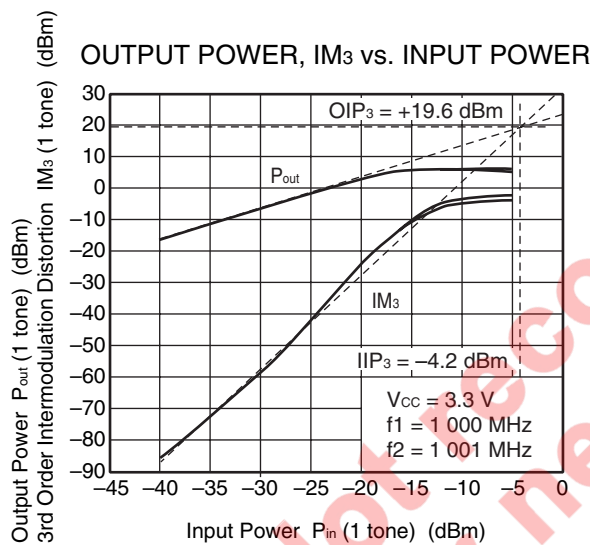
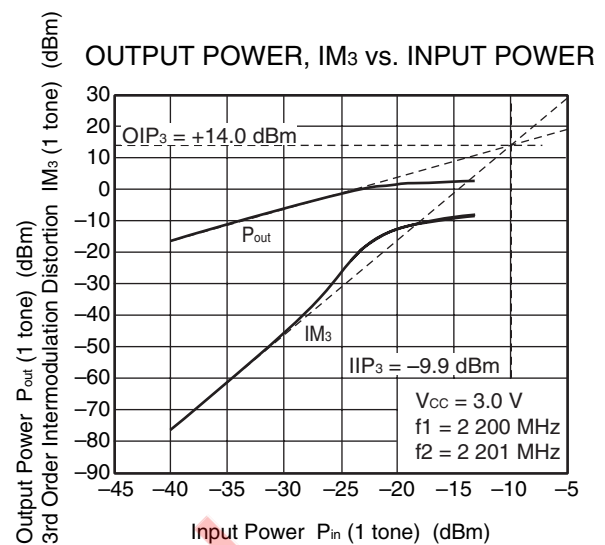
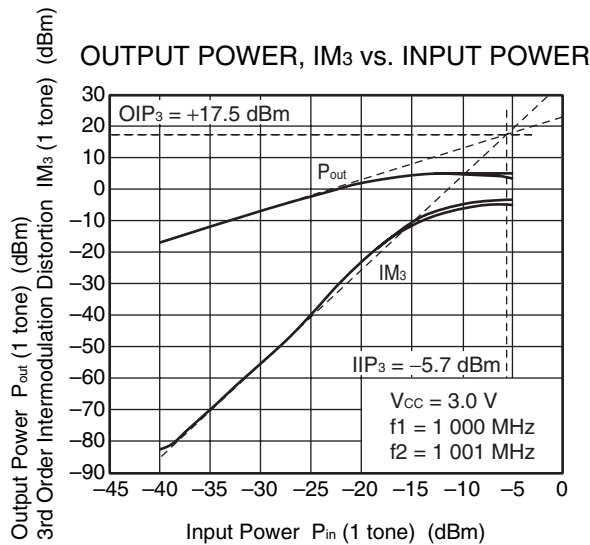
NOISE FIGURE vs. FREQUENCY



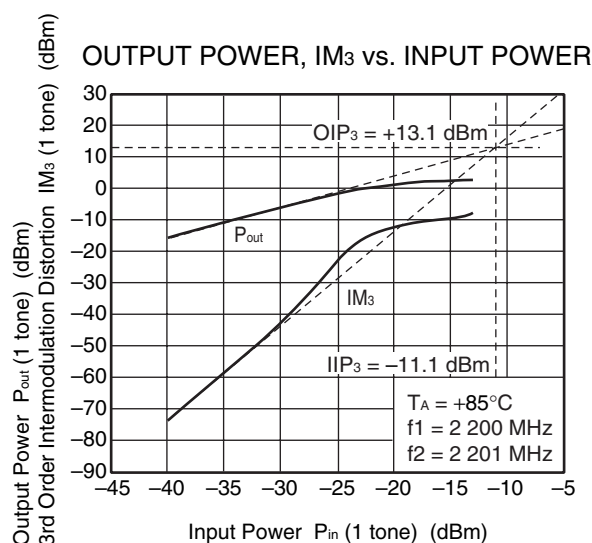
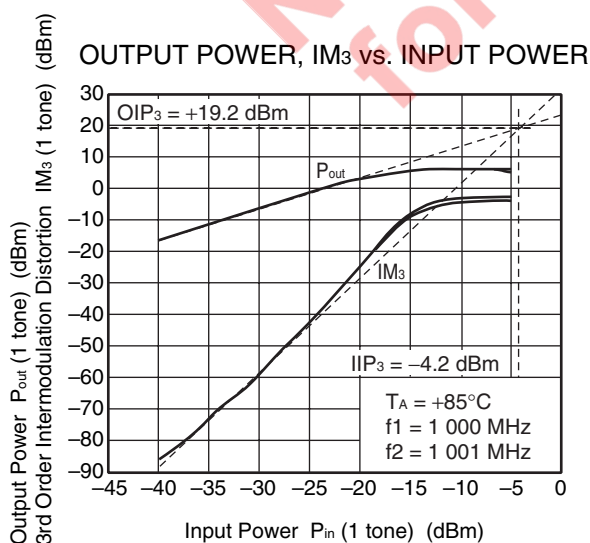
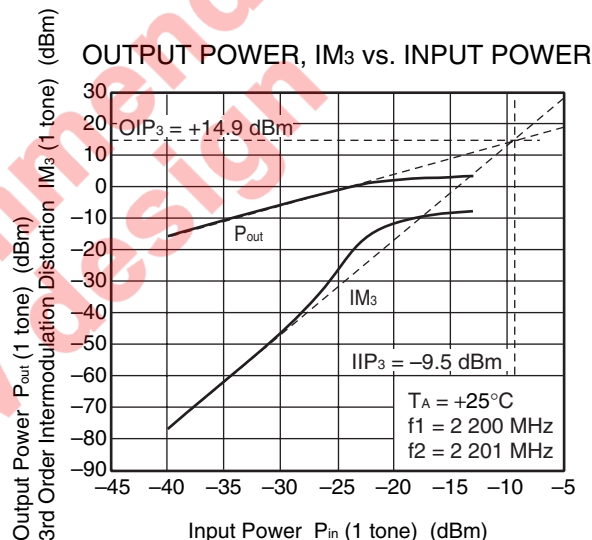
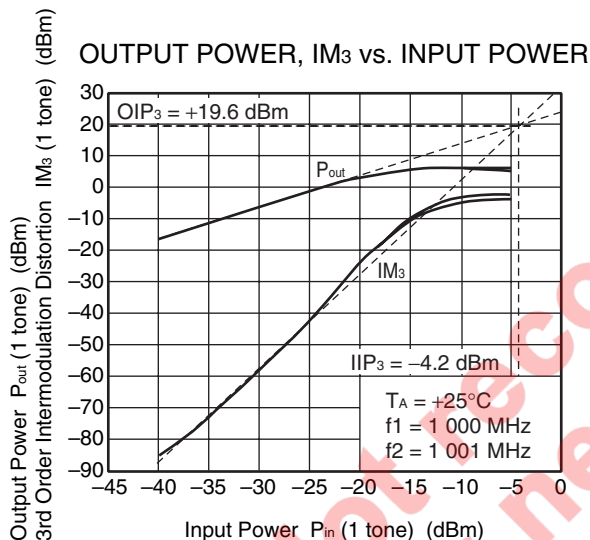
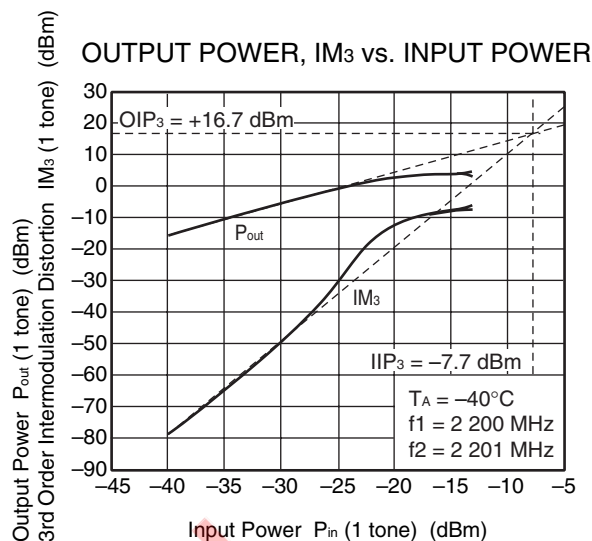
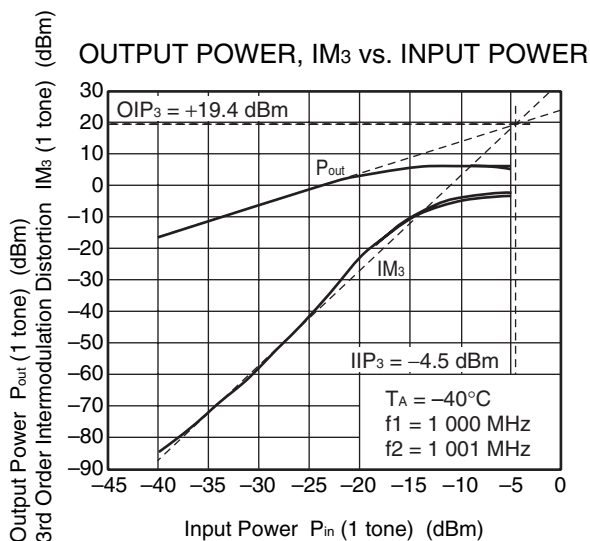
NOISE FIGURE vs. FREQUENCY



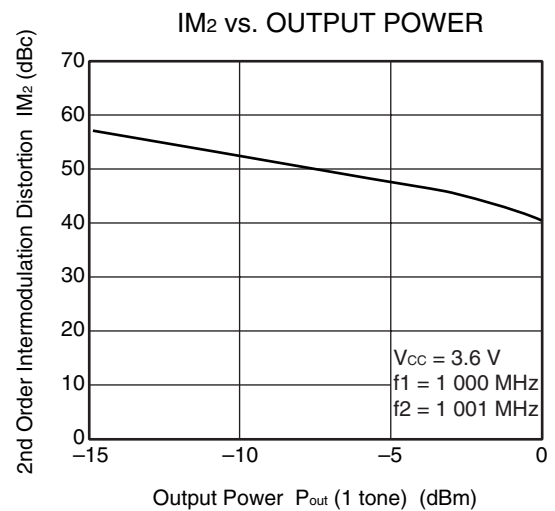
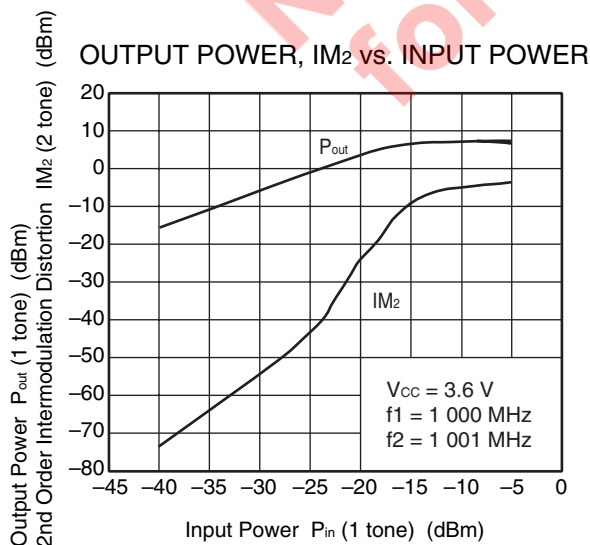
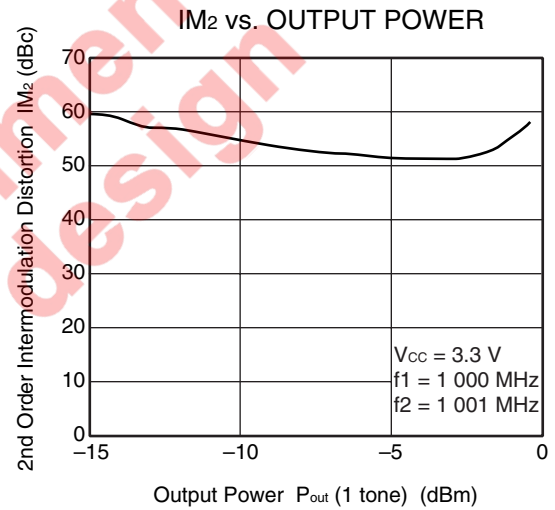
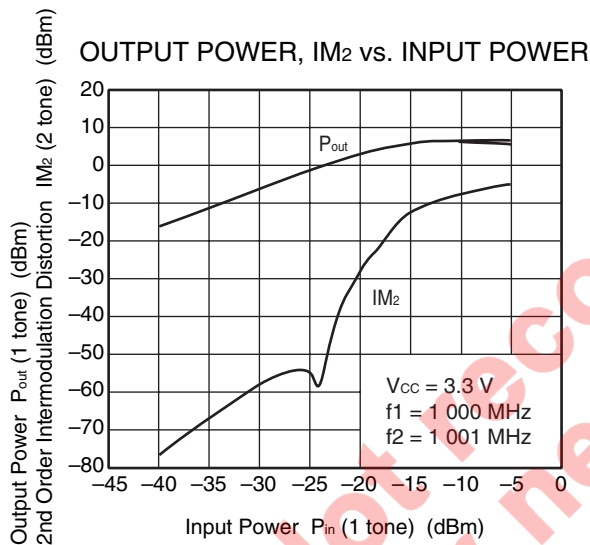
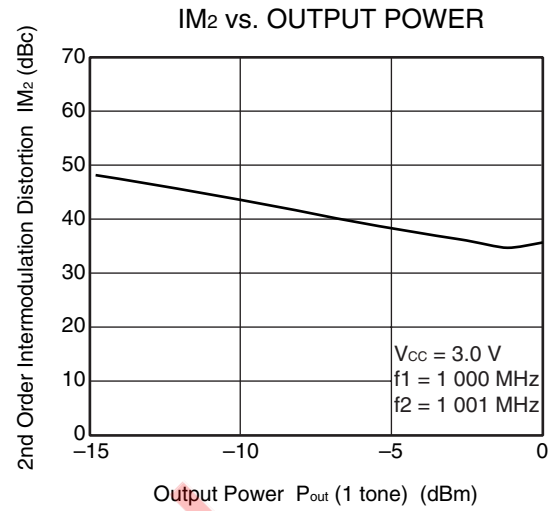
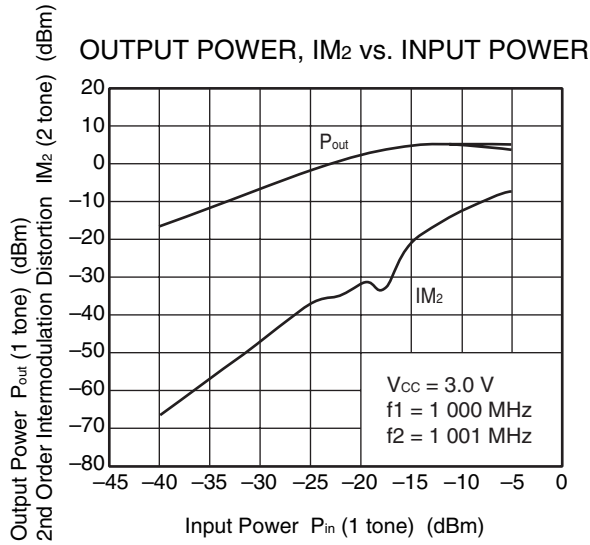
Remark The graphs indicate nominal characteristics.



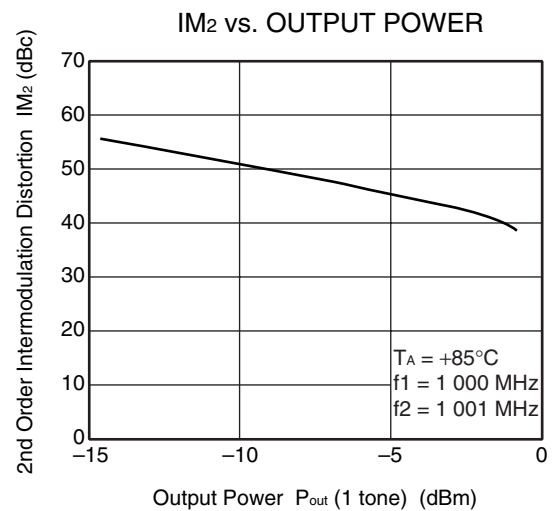
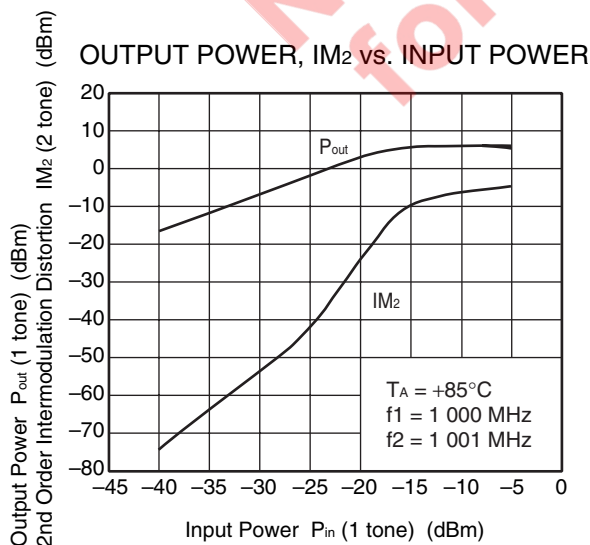
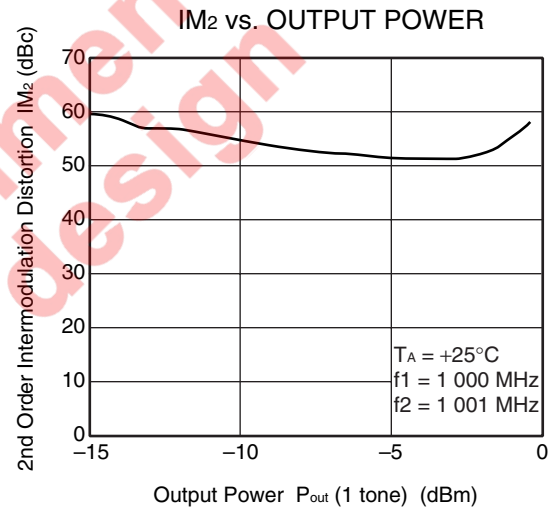
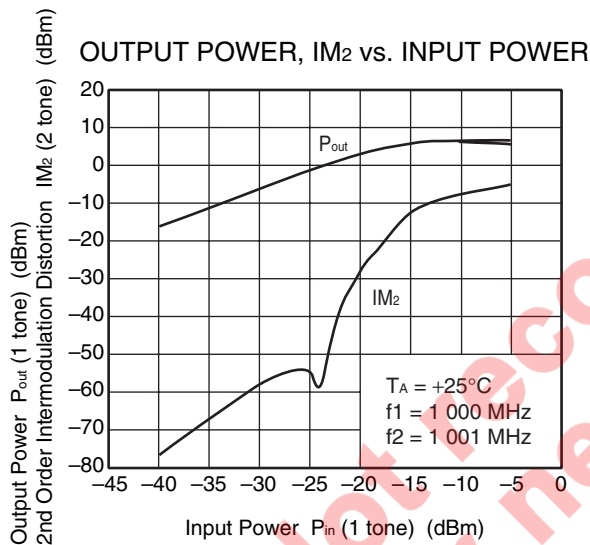
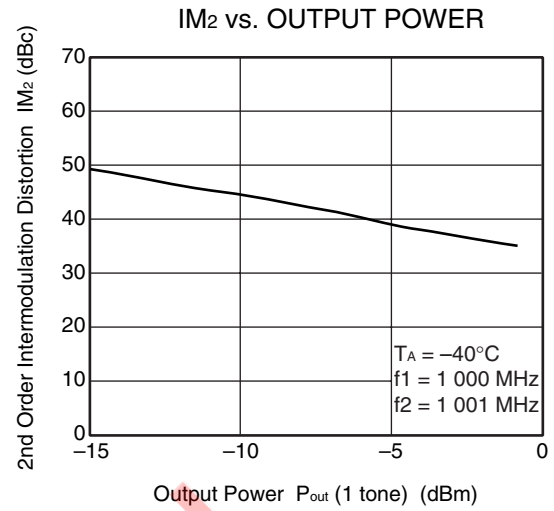
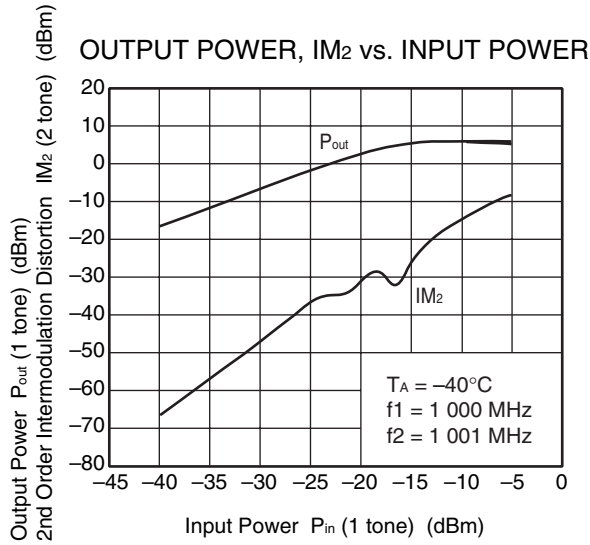
Remark The graphs indicate nominal characteristics.



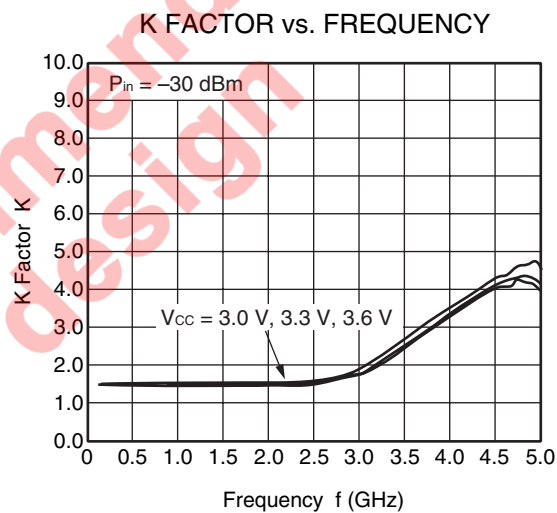
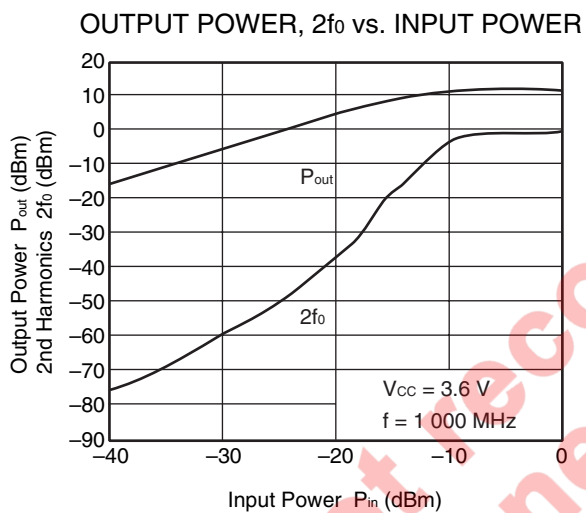
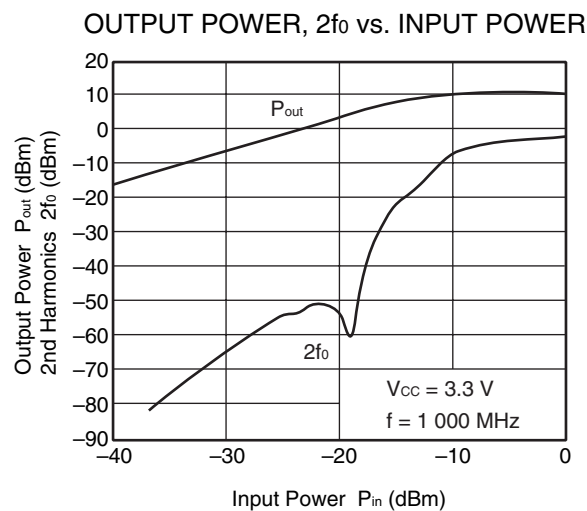
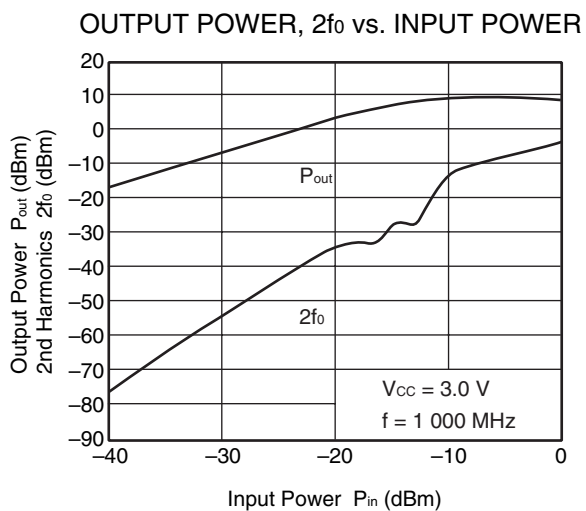
Remark The graphs indicate nominal characteristics.



Remark The graphs indicate nominal characteristics.



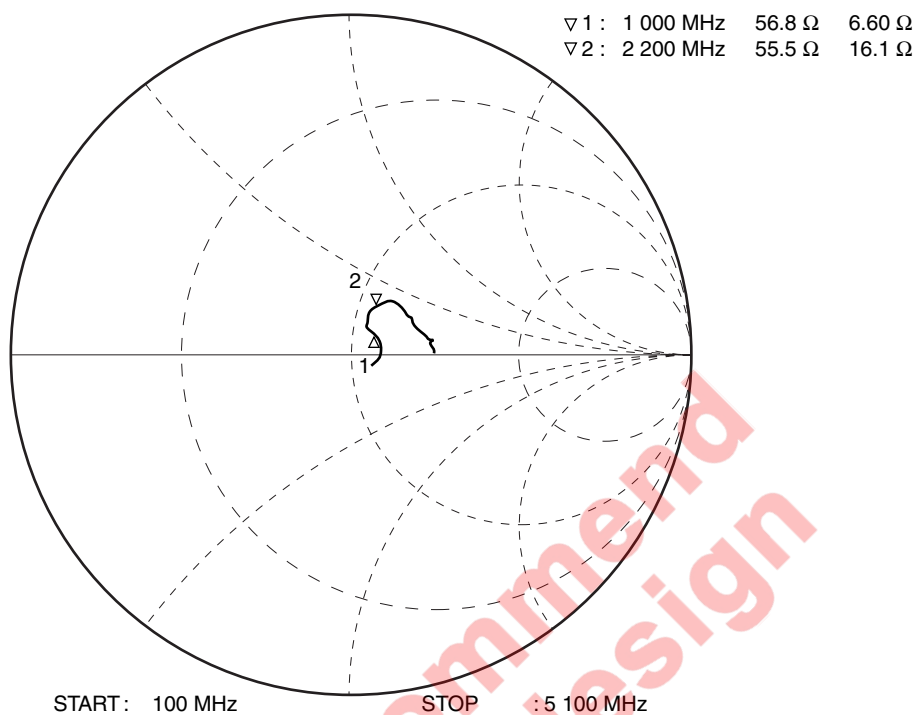
Remark The graphs indicate nominal characteristics.



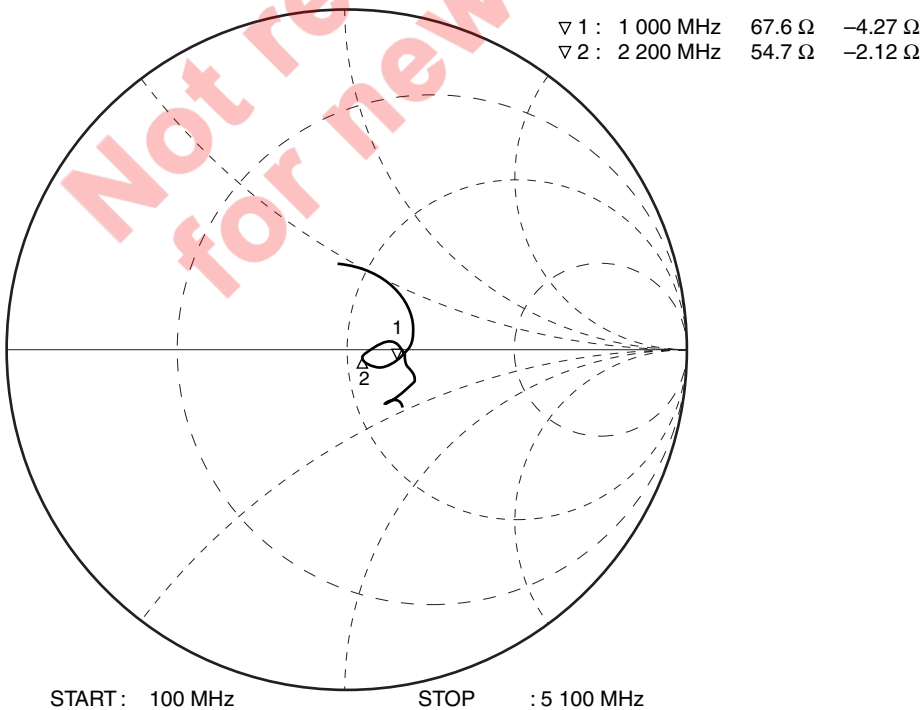
Remark The graphs indicate nominal characteristics.

S-PARAMETERS ($T_A = +25^\circ\text{C}$, $V_{CC} = V_{out} = 3.3\text{ V}$, $P_{in} = -30\text{ dBm}$)

S₁₁—FREQUENCY



S₂₂—FREQUENCY



Remarks 1. Measured on the test circuit of evaluation board.

2. The graphs indicate nominal characteristics.

S-PARAMETERS

S-parameters and noise parameters are provided on our Web site in a format (S2P) that enables the direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.

Click here to download S-parameters.

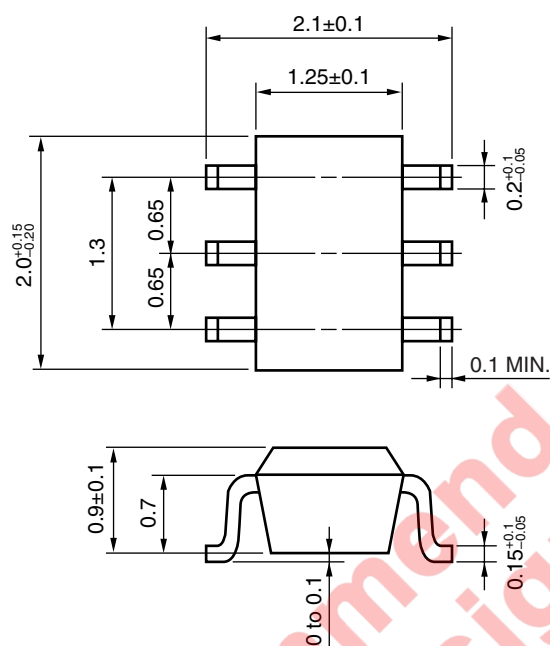
[RF and Microwave] → [Device Parameters]

URL <http://www.necel.com/microwave/en/>

Not recommend
for new design

PACKAGE DIMENSIONS

6-PIN SUPER MINIMOLD (UNIT: mm)



NOTES ON CORRECT USE

- (1) Observe precautions for handling because of electro-static sensitive devices.
- (2) Form a ground pattern as widely as possible to minimize ground impedance (to prevent undesired oscillation).
All the ground terminals must be connected together with wide ground pattern to decrease impedance difference.
- (3) The bypass capacitor should be attached to the V_{CC} line.
- (4) The inductor (L) must be attached between V_{CC} and output pins. The inductance value should be determined in accordance with desired frequency.
- (5) The DC cut capacitor must be attached to input and output pin.

RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

Soldering Method	Soldering Conditions	Condition Symbol
Infrared Reflow	Peak temperature (package surface temperature) : 260°C or below Time at peak temperature : 10 seconds or less Time at temperature of 220°C or higher : 60 seconds or less Preheating time at 120 to 180°C : 120±30 seconds Maximum number of reflow processes : 3 times Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	IR260
Wave Soldering	Peak temperature (molten solder temperature) : 260°C or below Time at peak temperature : 10 seconds or less Preheating temperature (package surface temperature) : 120°C or below Maximum number of flow processes : 1 time Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	WS260
Partial Heating	Peak temperature (terminal temperature) : 350°C or below Soldering time (per side of device) : 3 seconds or less Maximum chlorine content of rosin flux (% mass) : 0.2%(Wt.) or below	HS350

Caution Do not use different soldering methods together (except for partial heating).

- **The information in this document is current as of June, 2009. The information is subject to change without notice. For actual design-in, refer to the latest publications of NEC Electronics data sheets, etc., for the most up-to-date specifications of NEC Electronics products. Not all products and/or types are available in every country. Please check with an NEC Electronics sales representative for availability and additional information.**
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"Special": Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support).

"Specific": Aircraft, aerospace equipment, submersible repeaters, nuclear reactor control systems, life support systems and medical equipment for life support, etc.

The quality grade of NEC Electronics products is "Standard" unless otherwise expressly specified in NEC Electronics data sheets or data books, etc. If customers wish to use NEC Electronics products in applications not intended by NEC Electronics, they must contact an NEC Electronics sales representative in advance to determine NEC Electronics' willingness to support a given application.

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