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April 1st, 2010 Renesas Electronics Corporation

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DATA SHEET

BIPOLAR ANALOG INTEGRATED CIRCUIT

 μ PC337

3-TERMINAL NEGATIVE ADJUSTABLE REGULATOR

DESCRIPTION

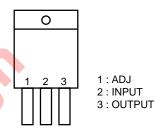
The μ PC337 is an adjustable 3-terminal negative voltage regulator, which has 1.5 A for the output current. The output voltage can be set any value between –1.3 V and –30 V by two external resistors.

FEATURES

- · Output current is up to 1.5 A
- On-chip some protection circuit (over current protection, SOA protection and thermal shut down).

PIN CONFIGURATION (Marking Side)

μPC337HF, μPC337HF-AZ

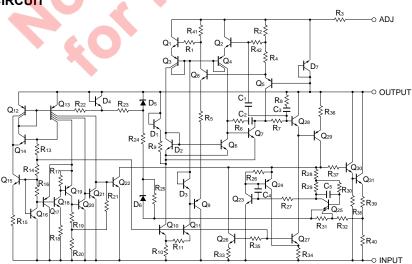


<R> ORDERING INFORMATION

Part Number	Package	Output Voltage	Marking	Package Type
μPC337HF	3PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	−1.3 V to −30 V	C337	Packed in envelope
μPC337HF-AZ Note	3PIN PLASTIC SIP (MP-45G) (Isolated TO-220)	−1.3 V to −30 V	C337	Packed in envelope

Note Pb-free (This product does not contain Pb in external electrode.)

EQUIVALENT CIRCUIT



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Document No. G13783EJ3V0DS00 (3rd edition) Date Published November 2006 NS CP(N) Printed in Japan ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified.)

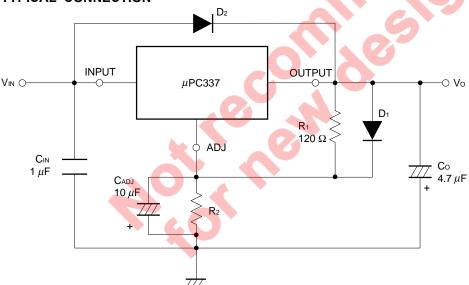
Parameter	Symbol	Rating	Unit
Input-Output Voltage Differential	Vin – Vo	-40	V
Total Power Dissipation (Tc = 25°C)	Рт	15 ^{Note}	W
Operating Ambient Temperature	TA	−20 to +85	°C
Operating Junction Temperature	TJ	−20 to +150	°C
Storage Temperature	Tstg	−65 to +150	°C
Thermal Resistance (junction to case)	Rth (J-C)	7	°C/W
Thermal Resistance (junction to ambient)	Rth (J-A)	65	°C/W

Note Internally limited.

When operating junction temperature rise above 150°C, the internal protection circuit shutdown output voltage.

<R> Caution Product quality may suffer if the absolute maximum rating is exceeded even momentarily for any parameter. That is, the absolute maximum ratings are rated values at which the product is on the verge of suffering physical damage, and therefore the product must be used under conditions that ensure that the absolute maximum ratings are not exceeded.

TYPICAL CONNECTION



Remark

R₁, R₂: Resistor to set the output voltage.

$$V_0 = (1 + \frac{R_2}{R_1}) \bullet V_{REF} + I_{ADJ} \bullet R_2 \cong (1 + \frac{R_2}{R_1}) \bullet V_{REF}$$

 $\ensuremath{\text{C}_{\text{IN}}}\ \ \, : \ \mbox{Need to stop the oscillation for the long input wire length.}$

Co : Need to stop the oscillation for the long output wire length.

Vo (V)	R ₂ (Ω : TYP.)
-2.5	120
-5.0	360
-12	1032
-24	2184
-30	2760

Improve the transient stability of the output voltage when the load current is suddenly changed.

CADJ: Improve the ripple rejection and the oscillate rejection.

D1 : Protect against CADJ when output pin is shorted.

 D_2 : Need for $V_{IN} > V_O$.



RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Input-Output Voltage Differential	VIN-Vo	-3	-5	-38.7	V
Input Voltage	Vin	-4.3		-40	V
Output Voltage	Vo	-1.3		-30	V
Output Current	lo	0.01		1.5	Α
Operating Junction Temperature	TJ	-20		+125	°C

ELECTRICAL CHARACTERISTICS

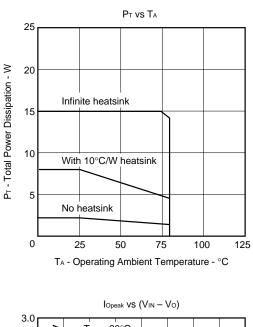
(VIN – Vo = –5 V, lo = 0.5 A, 0° C \leq TJ \leq +125 $^{\circ}$ C, unless otherwise specified.)

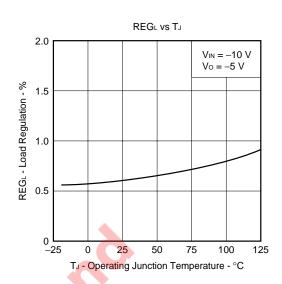
Parameter	Symbol	Conditions		MIN.	TYP.	MAX.	Unit
Line Regulation	REGIN	T _J = 25°C, 3 V ≤ V _{IN} − V _O ≤ 40 V			0.005	0.04	%/V
		3 V ≤ V _{IN} − V _O ≤ 40 V			0.01	0.07	%/V
Load Regulation	REG∟	T _J = 25°C,	Vo ≤ 5 V		30	50	mV
		10 mA ≤ lo ≤ 1.5 A	Vo ≥ 5 V		0.6	1.0	%
		10 mA ≤ lo ≤ 1.5 A	Vo ≤ 5 V		45	70	mV
			Vo ≥ 5 V	6	0.9	1.5	%
Thermal Regulation	REGTH	$T_J = 25^{\circ}C$, $ V_{IN} - V_{O} = 40 \text{ V}$, $V_{O} = -10 \text{ V}$, $0 \text{ A} \le I_{O} \le 0.25 \text{ A}$, $t = 10 \text{ ms}^{Note}$			0.005	0.04	%/W
ADJ pin Output Current	ladj				60	100	μΑ
I _{ADJ} Change	ΔIADJ	$T_J = 25^{\circ}C$, $3 \text{ V} \le V_{IN} - V_O \le 40 \text{ V}$, $10 \text{ mA} \le I_O \le 1.5 \text{ A}$, $P_T \le 15 \text{ W}$			2	5	μΑ
Reference Voltage	VREF	$3 \text{ V} \le V_{\text{IN}} - V_{\text{O}} \le 40 \text{ V},$ $10 \text{ mA} \le I_{\text{O}} \le 1.5 \text{ A}, P_{\text{T}} \le 15 \text{ W}$		-1.20	-1.25	-1.30	٧
Temperature Stability of VREF	$\Delta V_{REF}/\Delta T$	lo = 5 mA			-0.6		%
Minimum Load Current	Іомін.	V _{IN} - V _O = 40 V			2.1	10	mA
Peak Output Current	lOpeak	3 V ≤ V _{IN} – V _O ≤ 15 V		1.5	2.3	2.9	Α
	60	T _J = 25°C, V _{IN} – V _O = 40 V		0.15	0.8		Α
Output Noise Voltage (RMS)	Vn	T _J = 25°C, 10 Hz ≤ f ≤ 10 kHz			0.002		%
Ripple Rejection	R•R	T _J = 25°C, ΔV _{IN} = 1 V _{r.m.s.}	C _{ADJ} = 0 μF		60		dB
		f = 120 Hz, Vo = -10 V	C _{ADJ} = 10 μF	66	75		dB

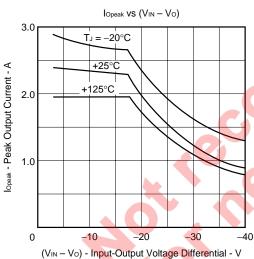
Note Pulse testing Duty Cycle $\leq 2\%$

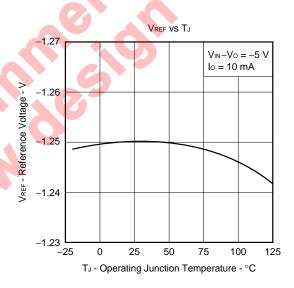


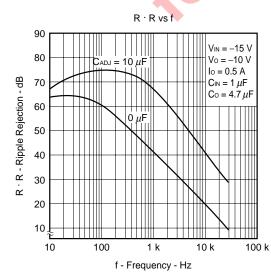
TYPICAL CHARACTERISTICS (TJ = 25°C, unless otherwise specified.)

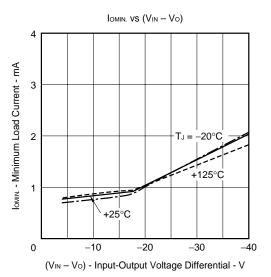








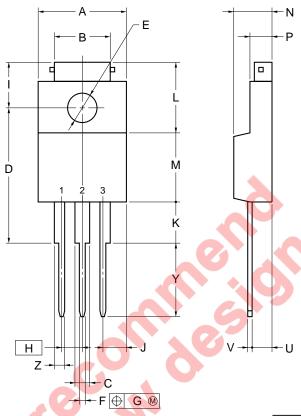




Data Sheet G13783EJ3V0DS

PACKAGE DRAWING

3PIN PLASTIC SIP (MP-45G)



NOTE

Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
Α	10.0±0.2
В	7.0±0.2
С	1.50±0.2
D	17.0±0.3
Е	φ3.3±0.2
F	0.75±0.10
G	0.25
Н	2.54 (T.P.)
I	5.0±0.3
J	2.46±0.2
K	5.0±0.2
L	8.5±0.2
М	8.5±0.2
N	4.5±0.2
Р	2.8±0.2
U	2.4±0.5
V	0.65±0.10
Υ	8.9±0.7
Z	1.30±0.2

P3HF-254B-4



<R> RECOMMENDED SOLDERING CONDITIONS

The μ PC337 should be soldered and mounted under the following recommended conditions.

For soldering methods and conditions other than those recommended below, contact an NEC Electronics sales representative.

For technical information, see the following website.

Semiconductor Device Mount Manual (http://www.necel.com/pkg/en/mount/index.html)

Through-hole devices

μPC337HF: 3PIN PLASTIC SIP (MP-45G) (Isolated TO-220)

Process	Conditions	Recommend
Wave soldering	Solder temperature: 260°C or below,	WS60-00-1
(only to leads)	Flow time: 10 seconds or less.	
Partial heating method	Pin temperature: 350°C or below,	P350
	Heat time: 3 seconds or less (Per each pin).	

μPC337HF-AZ: 3PIN PLASTIC SIP (MP-45G) (Isolated TO-220) Note

Process	Conditions	Recommend
Wave soldering	Solder temperature: 260°C or below,	WS60-00-1
(only to leads)	Flow time: 10 seconds or less.	
Partial heating method	Pin temperature: 350°C or below,	P350
	Heat time: 3 seconds or less (Per each pin).	

Note Pb-free (This product does not contain Pb in external electrode.)

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

<R> REFERENCE DOCUMENTS

Document Name	Document No.
Usage of Three-Terminal Regulators User's Manual	G12702E
Semiconductor Device Mount Manual	http://www.necel.com/pkg/en/mount/index.html
Review of Quality and Reliability Handbook Information	C12769E

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