

Analog IP

Analog-PLL For Frequency Multiplying

Key Features

- Including Loop-filter
- VCO operating range : 2000MHz - 4000MHz
- Output frequency range : 62.5MHz - 4000MHz
- Input frequency range : 20MHz - 200MHz
- Multiplying (Output freq. / PFD freq.) : 1.5625 - 200
- Divider
 - 7bit feedback divider, 3bit input divider and 3bit output divider
- 14bit fractional accuracy
- Power-down Mode
- SSC Input is available (recommend triangle profile)VCO operating range : 2000MHz - 4096MHz

Technology

Process : TSMC 28nm HPC+

Operating Condition

Parameter	Min	Max	Unit
Operating Voltage (AVDD)	1.62	1.98	V
Operating Voltage (VDD)	0.935	1.08	V
Junction Temperature	-40	125	°C
Input Clock	Duty	30	%
	Rise/Fall time	-	0.2

CTPD-24-133
R06PF0100EJ0100