

Interface IP

MIPI D-PHY and FPD-Link(LVDS) Combinational Transmitter for TSMC 22nm ULP

Overview

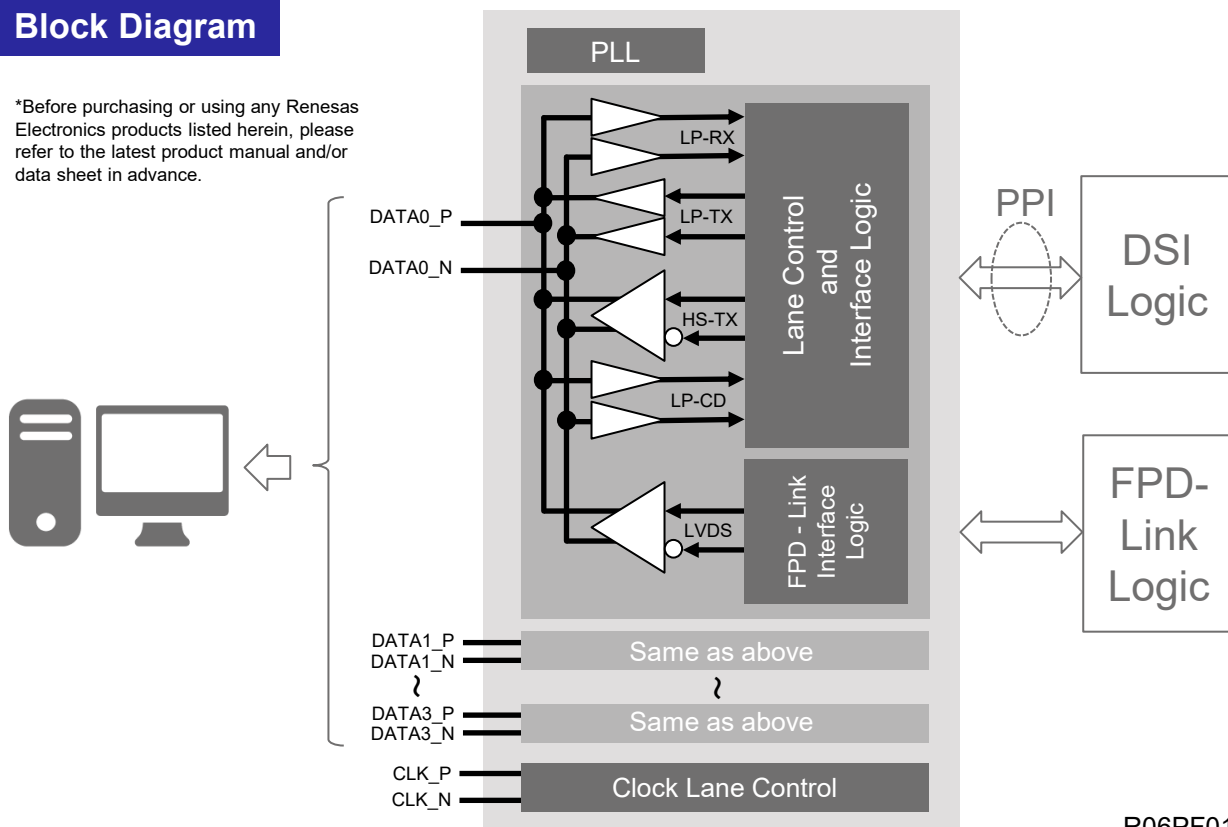
The Renesas MIPI D-PHYSM and FPD-Link(LVDS) combinational Transmitter is useful 4 Data Channel driver hard macro for DSI[®] of TSMC 22nm ULP process.

Features

- Renesas MIPI D-PHY Transmitter can be used for analog Driver of following interface .
 - MIPI alliance Specification for D-PHY Version 2.1 15 December 2016.
 - MIPI alliance Specification for Display Serial Interface (DSI).
- Renesas LVDS Transmitter can be used for analog Driver of following interface.
 - FPD-Link(LVDS) Specification for Version D-PHY ANSI/TIA/EIA-644-A FEBRUARY 2001.
- Technology is TSMC 22nm ULP 1p10M.
- Supply voltage can be applied 1.0V for core voltage, 1.8V for IO voltage.
- Maximum data rate of each channel is 1.5Gbps at High-speed mode for MIPI D-PHY Transmitter.
- Data rate of each channel is 609Mbps for FPD-Link(LVDS).

Block Diagram

*Before purchasing or using any Renesas Electronics products listed herein, please refer to the latest product manual and/or data sheet in advance.



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